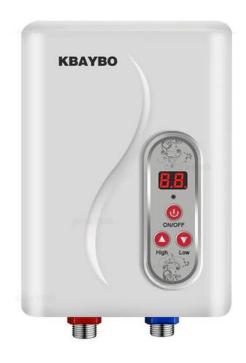
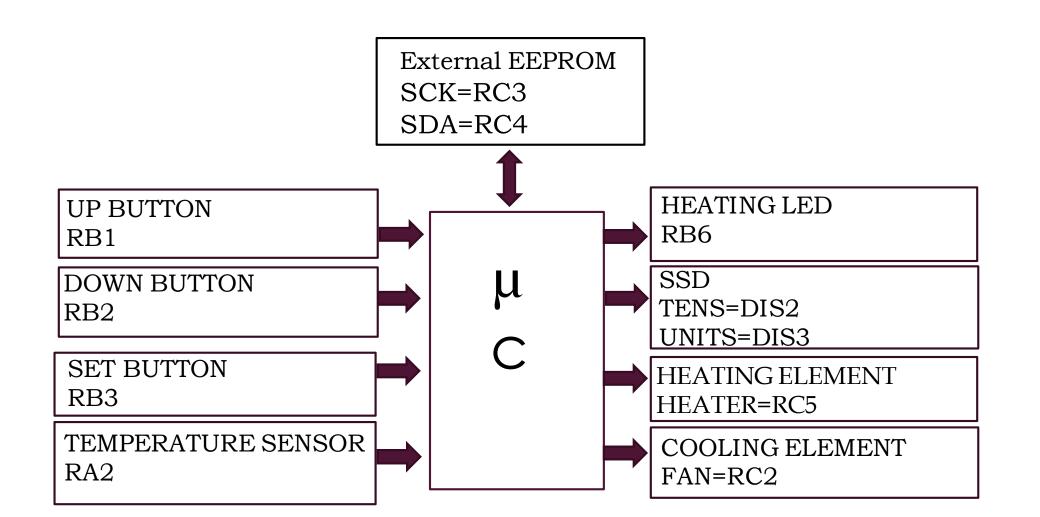
# ELECTRIC WATER HEATER

BY: RINAD GAMAL EL-DIN ASHOUR

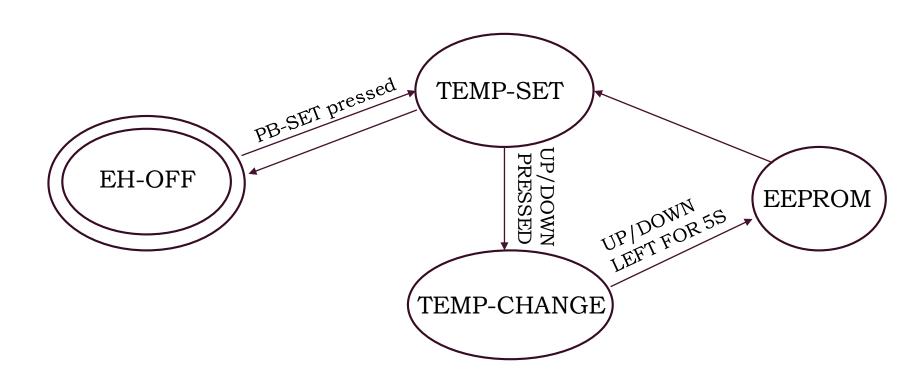
Swift Act training final project



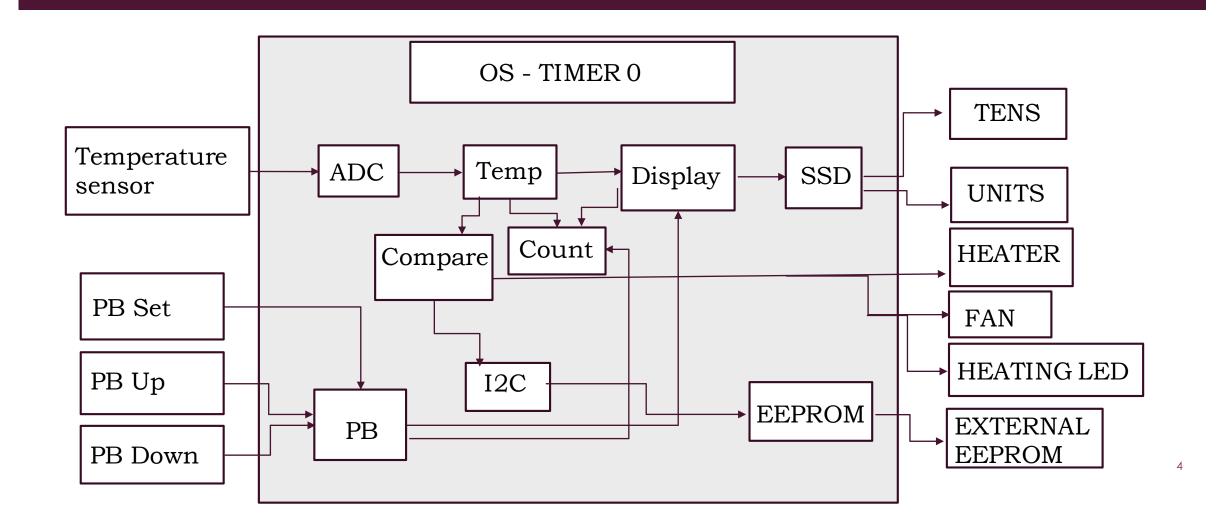
#### OVERVIEW AND CONNECTIONS



## STATE MACHINE



### CONTEXT DIAGRAM & MODULES



#### DETAILED DESIGN

- PB
  - 1. PB\_Init
  - 2. PB\_Update
  - 3. PB\_GetState
- SSD
  - 1. SSD\_Init
  - 2. SSD\_Update
  - 3. SSD\_SetSymbol
  - 4. SSD\_SetState
  - 5. SSD\_GetSymbol
  - 6. SSD\_GetState

- Count
  - 1. count\_Init
  - 2. count\_Update
  - 3. count\_GetCount
- Compare
  - 1. compare\_Init
  - 2. compare\_Update
- Temp
  - 1. temp\_Init
  - 2. temp\_Update
  - 3. temp\_GetResults

#### Timer

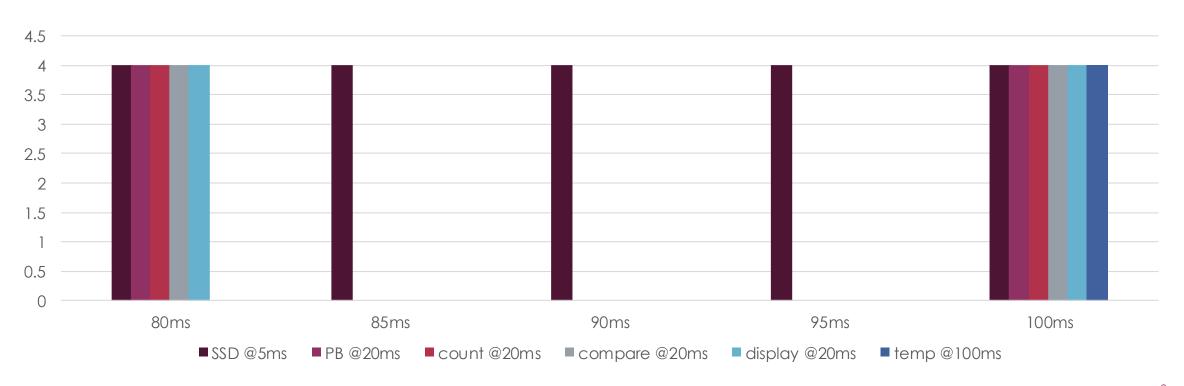
- 1. TMR\_Init
- 2. TMR\_Start
- 3. TMR\_CheckOverFlow
- 4. TMR\_Stop
- 5. TMR\_Update
- ADC
  - 1. ADC\_Init
  - 2. ADC\_Update
  - 3. ADC\_GetResult
- Display
  - 1. display\_Init
  - 2. display\_Update

- 3. display\_ONOFF
- 4. display\_SetState
- 5.display\_GetState
- 6. display\_GetPOWERState
- I2C
  - 1. i2c\_init
  - 2. i2c\_start
  - 3. i2c\_stop
  - 4. i2c\_wb
  - 5. i2c\_rb
- EEPROM
  - 1. e2pext\_r
  - 2. e2pext\_w

# TIMING ANALYSIS

Task	BCET (ms)	WCET (ms)	Period of Task (ms)
PB	<b>≅</b> 0	<b>≅</b> 0	20
Compare	$\cong 0$	$\cong 0$	20
Count	$\cong 0$	$\cong 0$	20
Display	$\cong 0$	$\cong 0$	20
SSD	<b>≅</b> 0	<b>≅</b> 0	5
Temp	$\cong 0$	$\cong 0$	100
		TICK (ms)	5
		Major Cycle (ms)	100

## SCHEDULABILITY CHECK



# THANK YOU