

Y86 Processor: pipe-std.hcl

Quit Go Stop Step Reset

Simulator Speed (10*log Hz)
0

Controls

Pipeline Registers

W State Input

Stat	Instr	valE	valM	dstE	dstM
AOK	jne	00000000000000000000	00000000000000000000	----	----
AOK	movmovq	00000000000000000018	00000000D000D000D	----	%r10

Memory Stage

M State Input

Stat	Instr	Cnd	valE	valA	dstE	dstM
AOK	movmovq	Y	00000000000000000018	00000000000000000000	----	%r10
BUB	nop	Y	00000000000000000000	00000000000000000000	----	----

Execute Stage

E State Input

Stat	Instr	valC	valA	valB	dstE	dstM	srcA	srcB
BUB	nop	00000000000000000000	00000000000000000000	00000000000000000000	----	----	----	----
AOK	addq	00000000000000000000	00000000D000D000D	00000000000000000000	%rax	----	%r10	%rax

Decode Stage

D State Input

Stat	Instr	rA	rB	valC	valP
AOK	addq	%r10	%rax	00000000000000000000	00000000000000000083
AOK	addq	%r8	%rdi	00000000000000000000	00000000000000000085

Fetch Stage

F State Input

Stat	predPC
AOK	00000000000000000083
AOK	00000000000000000085

Pipeline stages

Register File

%rax	%rcx	%rdx	%rbx	%rsp
0				1f0
%rbp	%rsi	%rdi	%r8	%r9
	4	18	8	1
%r10	%r11	%r12	%r13	%r14

Register file

Stat AOK Condition Codes Z 0 S 0 O 0

Performance Cycles 11 Instructions 11 CPI 1.00

Status & condition codes

Performance monitor