

Quit

Go

Stop

Step

Reset

Simulator Speed (10*log Hz)

6

Controls

Processor State

newPC

00000000000000087

PC Update Stage

valM

00000000000000000

Memory Stage

Cnd

valE

N 00000000000000003

Execute Stage

valA

valB

dstE

dstM

srcA

srcB

00000000000000001 00000000000000004 %rsi ---- %r9 %rsi

Decode Stage

Instr

rA

rB

valC

valP

subq %r9 %rsi 00000000000000000 00000000000000087

Fetch Stage

PC

00000000000000085

Stage values

Register File

%rax

%rcx

%rdx

%rbx

%rsp

d000d000d

%rbp

%rsi

%rdi

%r8

%r8

4

20

8

%r10

%r11

%r12

%r13

%r14

d000d000d

Register file

Stat

AOK

Condition Codes

Z 0 S 0 O 0

Condition codes

Status