Homework 2 Sheredeko Azina N1 unsigned: 0, -> 000000, [0; 63] 13,0 -> 001101, 24,0 -> 0(1000) 63,0 - 111111₂ signed: 16,0 -> 010000 [-32;31] -2,0 -> 111110, +2- 000010 +32-3 100000 reverse [1110] reverse 011111 3110 - 0111115 111110 100000 -32₁₀ - 100000, given traft: unsigned signed 000101, ----> 5, -1: (0 010 0 10101 -> 21 101011, _____ 43,0 -21, -1: 111110 ||||||, \longrightarrow 00000(🥆 1 63. -10 -l: ol((l) 100000, ----32,0 - 32₁₀ (00000 -> 32 $\sqrt{3}$. 7_{10} \longrightarrow 00000111₂ \longrightarrow 0×07 (or 07₁₆)

 $240_{10} \longrightarrow 0 \times FO$ 171,0 --> 101010112 --> 0x AB 126,0 --> OIIIIIIO2 --> 0x7E $\sqrt{4}$. 0x3C \longrightarrow 00111100₂ 0x7E ---> 011111102 0xFF --- 111111112 0×A5 _____ 101001012 $\sqrt{5}$. $00111100_2 \longrightarrow 11000011+1 = 11000100_2$ $01111110_2 \longrightarrow (00000001 + 1 = (00000010_2)$ $|| || || || ||_2 \longrightarrow 00000000 + 1 = 00000000|_2$

 $10100101, \longrightarrow 01011010 + 1 = 01011011,$

N2.

NG. Ox DEADBEEF Big - Endian Little - Endian adress: 0x100 0x101 0x102 0x103 EF BE AD DE,

(ess significant byte 0×100 0×101 0×102 0×103 value: DE, AD, BE, EF sign-extended Zezo-extended $\sqrt{7}$. $7_{10} \longrightarrow 00111_2$ 000001112 000001112 15,0 --> 011112 00001111, 000011112 -16₁₀ -10000₂ 00010000, 111100002 -5₁₀ -5₁₀ 11011₂ 000110112 111110112 N8. unsigned: $\begin{bmatrix}
 0000_{2} & \text{is 0} \\
 10000_{2} & \text{is 16}
 \end{bmatrix}$ $4 \longrightarrow 0100$ $-5 \longrightarrow \frac{1011}{1111_2} \text{ (which is -1)}$ signed: N8.*(16)) X&(X-1) - turning off the zightmost 1-bit · X-1 - all the bits after the rightmost 1-bit are inverted α : 01011000 - 1 = 01010111· X&(X-1) -> all the bits stay the same up to the eightmost 1-bit, which "turns off" ex: 01011000 AND 0101011 01010000 2) x ((x+1) - turning on the zightmost 0-bit . X+1 -> all the bits after the zightmost D-bit are inverted

ex: (0(0011) + 1 - 10101000)

- $X \mid (x+1) \rightarrow \text{all bits remain unchanged}$, the rightmost 0-bit "turns on" ex: $\frac{10101000}{10101111}$ 3) $X \mid (X-1) \text{turning on the trailing 0's}$
- (x-1) luching on the latting US $\cdot x-1$ all the bits after the rightmost 1-bit are inverted
 - ex: (0101000 1 = 10100111• $X | (X-1) \rightarrow trailing 0's of the original number "turn on", all other bits stay the same$
 - 0k: 10100111
- (12) $X = X ^ y; \rightarrow X = X \text{ XOR } y$ $y = X ^ y; \rightarrow y = X \text{ XOR } y \text{ XOR } y = 0$ $X = X ^ y; \rightarrow X = X \text{ XOR } y \text{ XOR } X = X \text{ XOR } x = y = y$
 - that is, x and y are swapped
 - example: let x = 10101111y = 00010011
 - then after $x = x^y$; x = 10111100 y = 00010011after $y = x^y$; x = 10111100
 - y = 10101111 $aftez \quad x = x^{4}y; \quad x = 00010011$ y = 10101111 x and y are snapped now