

Schedulability Analysis for Dual Priority Scheduling

I. DUAL PRIORITY

Given a task system $\tau = \{\tau_1, \tau_2, \dots, \tau_n\}$, we first make the following **assumptions**:

- 1) Each task τ_i has a original priority $n + i$ and a promotion priority i .
- 2) Each task has a fixed promotion point p_i . The concerned job $J_{i,k}$ has its promotion point $P_i = r_{i,k} + p_i$.

Lemma 1 (Worst Case Pattern): Suppose τ could experience deadline miss with the current priority and promotion point assignment, then it must be that there exists a busy time interval $[0, t]$ during which some job of task $\tau_i \in \tau$ would miss its deadline when all other tasks release their first job at time instant 0, and all jobs including those released by τ_i are released as soon as possible with corresponding period.

Proof 1: Among all possible legal event sequences in which τ misses some deadline, let S denote such a sequence where some job of $\tau_i \in \tau$ misses its deadline within the shortest busy interval $[0, t_F]$. As a result no deadline miss would happen earlier than t_F .

Assuming in the event sequence S , there exists some tasks, e.g., τ_j whose first release is not at time instant 0 and the separation between some job release is greater than T_j . Then as we shift $r_{j,1}$ to 0 and reduce time separation between each job releases to T_j , τ_i would consume no more resource than before.

This is because that all jobs of τ_j with deadline before t would still meet its deadline (otherwise we can construct a new sequence S), while the last job of τ_j released before t (e.g., $J_{j,m}$) is more likely to deny processor from τ_i since the promotion point of job $J_{j,m}$ will also decrease. Therefore after we modify the release pattern of τ_j , τ_i would still miss its deadline. Finally by repeating the above steps for all such tasks, the deadline miss of τ_i would still happen at t_F . Therefore Lemma 1 is true.

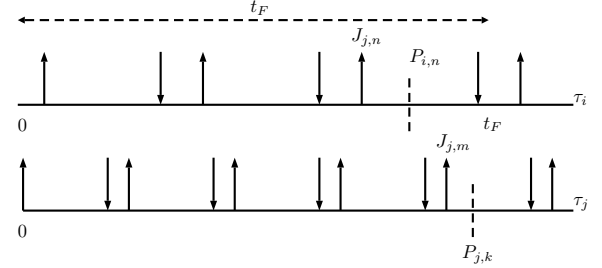


Fig. 1. Worst Case Pattern

From Lemma 1 we know that if τ would experience some deadline miss, then there exists a τ_i that will have its deadline miss with the worst case release pattern defined in Lemma 1. Therefore the following corollary can tell us whether a task system τ is schedulable by the dual priority scheduling algorithm.

Corollary 1: If $\forall \tau_i \in \tau$ no deadline miss happens for all possible time interval length t with the worst case release pattern: 1) some job of τ_i has deadline at t and all jobs of τ_i are released as soon as possible with period T_i ; 2) first job of $\tau_j \in \tau - \tau_i$ is released at 0 and all jobs are released as soon as possible with period T_j , then τ is schedulable by the dual priority scheduling algorithm.

Proof 2: This corollary is a direct deduction from Lemma 1.

From Corollary 1, we know that as long as we can guarantee that $\forall \tau_i \in \tau : \forall t : \text{no deadline miss happens with the worst case release pattern}$, then we can declare that the task system τ is schedulable by the dual priority scheduling algorithm. Therefore in the simplest case, an exact but computational expensive schedulability test for dual priority scheduling algorithm could be derived by simulating the behavior of the system with the worst case release pattern.

To reduce the complexity, we need a more efficient test to determine whether $\tau_i \in \tau$ is schedulable

when jobs are released with the worst case release in Lemma 1. Let $ibf_i(\tau_j, t')$ (ibf is short for interference bound function) denote maximum possible resource consumed by $\tau_j \in \tau - \tau_i$ in the system during the time interval $[0, t']$, and let $dbf(\tau_i, t)$ denotes the maximum execution requirement of τ_i during $[0, t]$, when all tasks are released with worst case pattern in Lemma 1.¹ Therefore the following theorem can be used to determine whether $\tau_i \in \tau$ is schedulable when all jobs in the system is released in the worst case release pattern.

Theorem 1: τ_i would not experience any deadline miss with the worst case release pattern on a single processor by the dual priority scheduling algorithm if the following condition holds:

$$\forall t : \exists t' \in [t - D_i, t] : dbf(\tau_i, t) + ibf_i(\tau - \tau_i, t') \leq t' \quad (1)$$

where

$$ibf_i(\tau - \tau_i, t') = \sum_{\tau_j \in \tau - \tau_i} ibf_i(\tau_j, t')$$

Proof 3: We can prove the statement that if τ_i is not schedulable with the worst case release pattern, then Equation 1 would not hold. Suppose that τ_i is not schedulable then there are legal event sequences in which τ_i misses some deadline when τ_i is assigned with the current priority and promotion point. Let S' denote such a sequence where τ_i misses deadline at the earliest time at t .

It must be the cases that during $[0, t]$ some tasks are executing because otherwise the time interval between the last idle instant to t could also construct such a sequence. Then it must be that during the time interval $[0, t']$, other tasks have consumed an amount of resource more than

$$ibf_i(\tau - \tau_i, t') > t' - dbf(\tau_i, t)$$

which contradicts the Equation 1.

With Theorem 1, we can use the following test to whether a task system τ is schedulable by the dual priority scheduling algorithm.

Theorem 2: A task system τ is schedulable by the dual priority scheduling on unit-speed uniprocessor

if the following hold: $\forall \tau_i \in \tau : \forall t : \exists t' \in [t - D_i, t] :$

$$dbf(\tau_i, t) + ibf_i(\tau - \tau_i, t') \leq t' \quad (2)$$

In the worst case release pattern, we can easily calculate the exact value of $dbf(\tau_i, t)$. However the exact value of $ibf_i(\tau - \tau_i, t')$ is not trivial except we simulate the system. As a result, here we use an upper bound of the actual interference to represent $ibf_i(\tau_j, t')$ instead.

Here we first present some notations that will be used later in the paper.

TABLE I
NOTATIONS

$m = \lfloor \frac{t'}{T_j} \rfloor$	$r_{j,m} = m \times T_j$	$P_{j,m} = m \times T_j + p_j$
$P_{i,n} = t - D_i + p_i$	$r_{i,n} = P_{i,n} - p_i$	$[a]_0 = \max(a, 0)$
$m' = \lfloor \frac{r_i}{T_j} \rfloor$	$r_{j,m'} = m' \times T_j$	
$m'' = \lfloor \frac{P_{i,n}}{T_j} \rfloor$	$r_{j,m''} = m'' \times T_j$	

II. SCHEDULABILITY TEST

¹Note that the two functions are specific for the worst case pattern.