

Compliance group

Oct 2018 week3 Minutes

Attendees

- Allen (Esperanto) [Chair]
- SimonD (Imperas)
- Lee Moore (Imperas)
- Grant Martin (Cadence)
- Stuart Hoad (Microsemi)
- Radek Hajek (Codalip)
- Justin Tseng (Andes)
- ilJa Stepanov (Syntacore)
- Neel Gala (IIT Madras)
- Ken Dockser (Qualcomm)
- Dave Connelly (Qualcomm)

Oct 18 discussion

- Cudasip wants to put their framework onto github – how?
 - Answer: easiest it to use personal site and link to it from compliance github site
 - We add an "Other Tools link to the compliance githubsite
- Microsemi has a team to integrate formal group's model with test suite for summit and the softcore competition
 - config mechanism is essential
- RFQ discussion
 - Gary Crumley (WD) will be Tech Committee Program Manager for this proposal and contract
 - Many detailed comments on proposal – see accompanying marked up [RISC-V Compliance Suite RFP -draft.docx](https://workspace.riscv.org/higherlogic/ws/groups/Compliance_TG/documents) in https://workspace.riscv.org/higherlogic/ws/groups/Compliance_TG/documents
- If time permits (it didn't):
 - Compliance at the Risc-V summit-
 - Nikhil (Bluespec/Formal) and Stuart (Microsemi) will discuss plans –
 - Update on compliance suite – Simon (Imperas)
 - Discussion of what coverage means – Simon (Imperas)
- Need to set goals and schedule
 - get suites running on the formal groups selected model
 - get framework(s) to run suites on the formal groups selected model
 - Do we support a single framework, or more than one?