

Mar 2, 2023 | 📅 RISC-V Control Transfer History TG Meeting

Attendees: tech.meetings@riscv.org Beeman Strong Bruce Ableidinger

Notes

- **Attendees:** Beeman, DavidW, JohnS, Snehasish, StasN
- **Slides/video** [here](#)
- Quick review of CSR vs MMIO interface tradeoffs discussed in last meeting

CSR	MMIO
Lower Latency	Higher latency
Requires indirect access	Direct access an option
Accessible to local hart only	Accessible to local hart and external agents
Virtualization on a per-register basis	Virtualization on a per-page basis
	Simplifies integration with trace encoder

- Summarized [further discussion on email list](#)
- **Attendees agreed to standardize and require a CSR interface**
 - Could also standardize an optional MMIO interface, but no urgent need for this identified
- Will review this conclusion with the email list
- Next meeting can begin constructing a spec and working through extension specifics

Action items

