



IOPMP Task Group Meeting

May 25, 2023

[Video link](#)

IOPMP and WorldGuard Discussion

- RTI discussion about IOPMP and WorldGuard update:
 - IOPMP main structure, <https://lists.riscv.org/g/sig-runtime-integrity/message/51>
 - Rapid-1 model, <https://lists.riscv.org/g/sig-runtime-integrity/message/54>
 - Follow up questions:
 - Why would a device need execute permission?
 - DSP/GPU/non-RISC-V core/legacy CPU. They're running on the main memory.
 - Optional, when omitted, it is read-only and wired to read permission.
 - Priority encoding: more robust.
 - Locked higher priority rules can protect sensitive assets when M-mode SW is compromised during the runtime.
 - The number of SID? and the number of rules? Are 63 Memory domains enough?
 - Need feedbacks.
 - For more MDs, we can cascade IOPMPs.

Topics

- Register Table Review:
 - version 1.0.0-draft2, <https://github.com/riscv-non-isa/iopmp-spec/blob/main/chapter5.adoc>
 - More figures needed
- SystemC Verification:
 - Presented in the slides: will be posted separately.