



IOPMP Task Group Meeting

October 26, 2023

[Video link](#)

Minutes

- Multi-fault report, RERI?
 - Channing will follow it up.
- The draft5 plan:
 - The first entry of a MD is TOR: add a programming notice
 - Section about “Multi-fault report”: complete a draft
 - Section about “Reset and static entries”: complete a draft
 - Switch SID to SDID
 - Add a figure to demonstrate the routing by SDID in “A2.1 Parallel IOPMP.”
- Draft4 feedback:
 - Fields r/w/x in ENTRY_CFG(*i*)
 - To allow an implementation to have its own legal values.
 - Swap HWCFG0 and HWCFG1 ?
 - The fields ‘model’ and ‘enable’ are considered in front of the rest of the fields.

Fields r/w/x in ENTRY_CFG(i)

ENTRY_CFG(i), $i = 0 \dots \text{HWCFG1.entry_num}-1$

ENTRYOFFSET + 0x8 + (i)*16

Field	Bits	R/W	
r	0:0	RW	
w	1:1	WARL	
x	2:2	WARL	

- We should add more description regarding the r/w/x. The spec should allow an implementation to have its own legal values.
- For example:
 - x is optional,
 - w is optional,
 - $x = r$,
 - w and x are exclusive, or
 - ...

Swap HWCFG0 and HWCFG1

Draft 4:

HWCFG0 (0x08):
sid_num [15:0]
entry_num [31:16]

HWCFG1 (0x0C):
model [3:0]
....
enable [31]

Draft 5:

HWCFG0 (0x08):
model [3:0]
....
enable [31]

HWCFG1 (0x0C):
sid_num [15:0]
entry_num [31:16]