



IOPMP Task Group Meeting

May 11, 2023

[Video link](#)

Summary

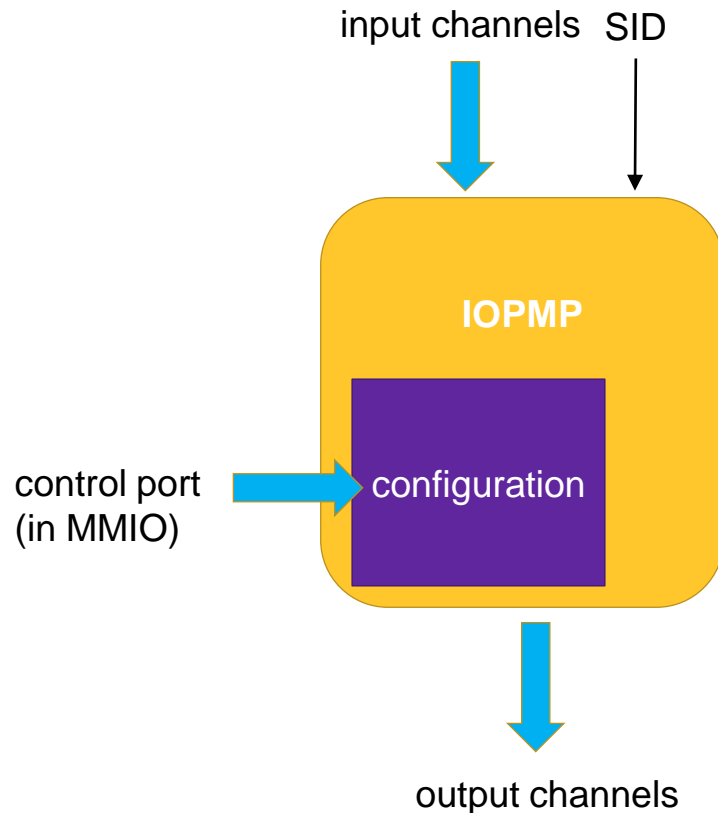
- Status update of ratification plan: Not passed yesterday
 - Items to explain: see following pages
 - 3 to-do Items by Mark:
 - 1) Why should IOPMP be within RISC-V or an independent project? (HC-level)
 - 2) What really is SID? How does it work? (HC-level)
 - 3) Explain better how to test it: SystemC for RTL and QEMU for M-mode SW
- Any update related to IOPMP from RTI SIG:
 - MTT:
 - WorldGuard vs IOPMP:
- Next meeting:
 - Register table

Items to explain

- SID is not fixed nor hardwired. The IOPMP spec will need to cooperate with the other extension to define how to program SID. Before that, hardwired SID can work somehow.
- When program SID, should we go through a CSR? → Depends on the above extension, it could be a CSR, a MMIO, or even hardwired. IOPMP is a SID consumer.

IOPMP Interface

- Input port:
 - Original bus channels/signals
 - SID signals
- Output port:
 - Original bus channels/signals
 - no SID signals
- Control port:
 - Original bus channels/signals



How IOPMP fits in a system with IOMMU?

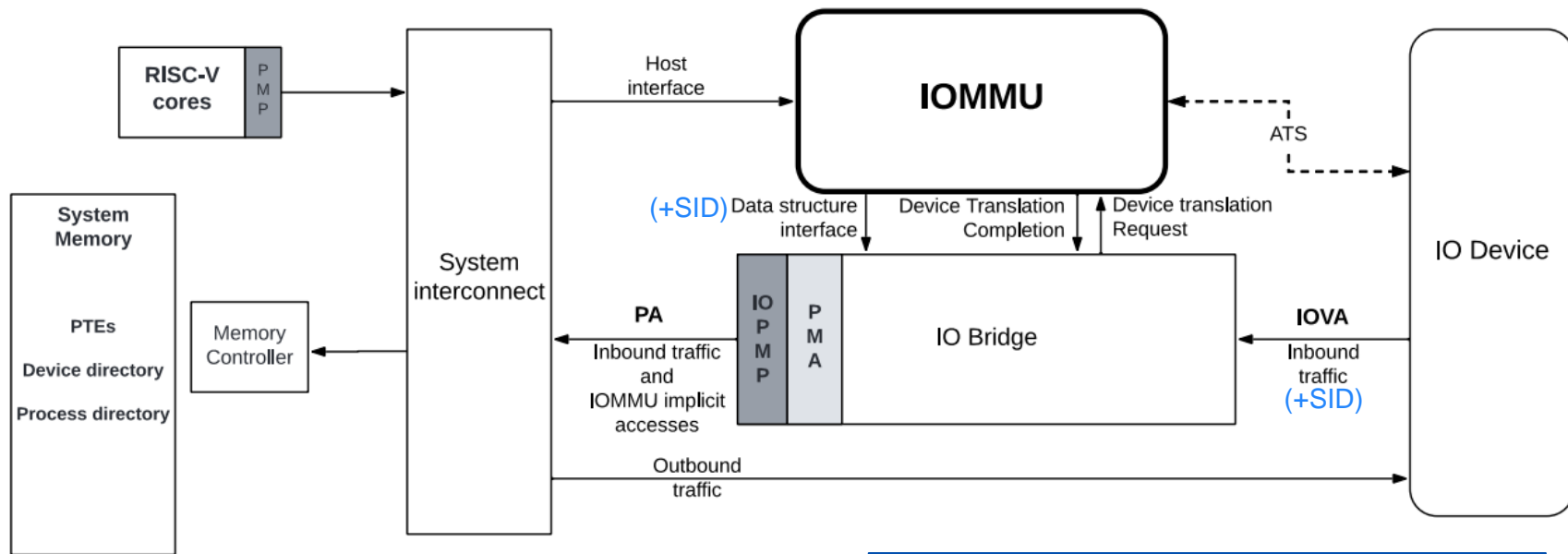


Figure 6. IOMMU interfaces.

Section 1.3. Placement and data flow,
RISC-V IOMMU Architecture Specification