Enter the RISC-V AI era with Andes

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Andes Vector Processor Families

More features for AX47MPV
- **. CHI-based multi-cluster**
- **. RVA23**
- **. fp8**

			AX47MPV*
		AX46MPV*	more features
	AX45MPV	Integrated Matrix Ext. (IME)	
	8-core cluster	16-core cluster with private L1/L2	
	HVM (High-speed Vector Memory) Interface		
AX25-V100	**NX27V**	ACE for RVV	
ACE (Andes Automated Custom Extension™)			
int4~64, fp16~64; bf16 (conv.)	+bf16 (full arithmetic)	+fp8	
VLEN: 128~512	+1024	+2048	
RVV 0.8	RVV 1.0		
5-stage single-issue	8-stage dual-issue with shared cache for multicore		

* Future products subject to change

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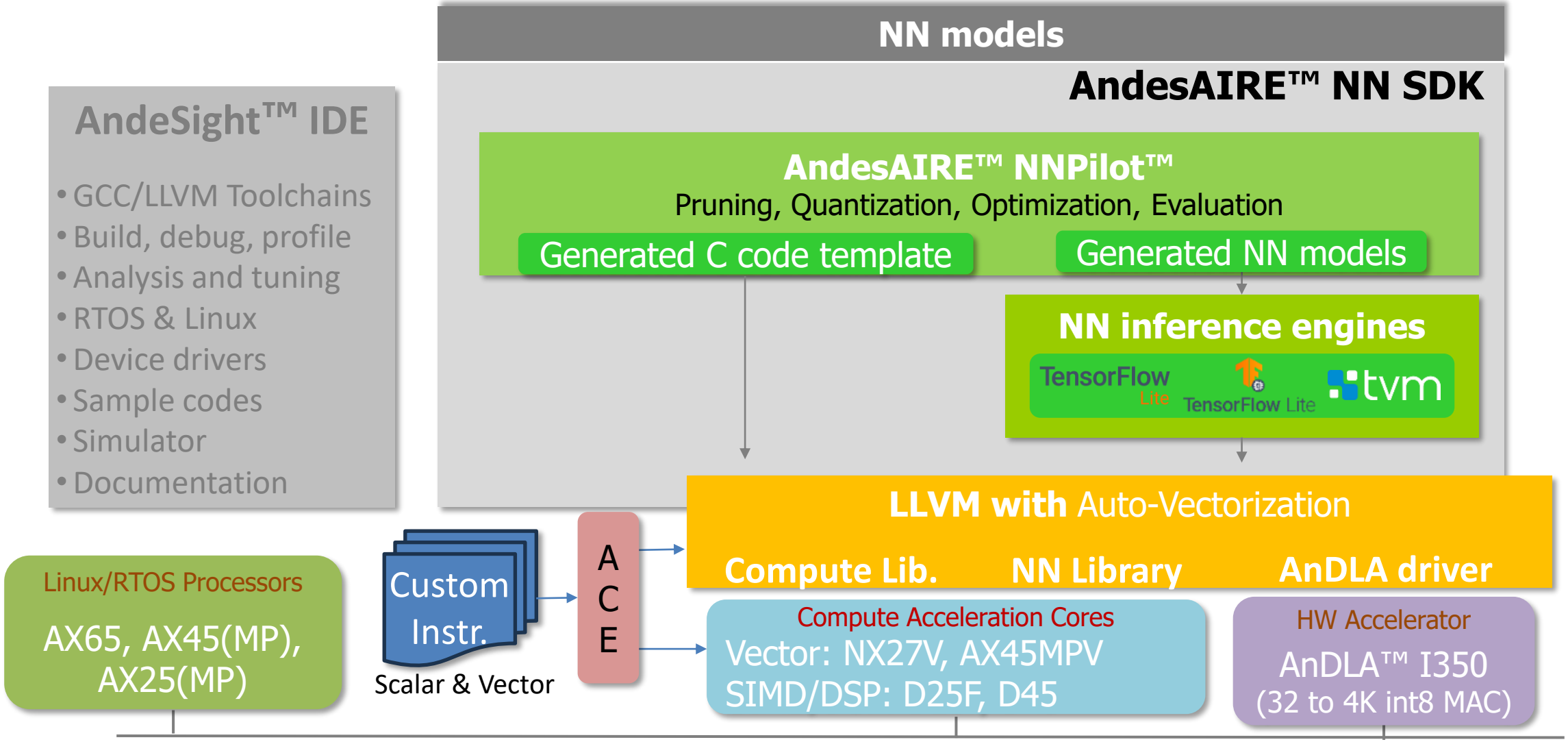
AndesAIRE™: Andes AI Runs Everywhere™

- **HW/SW Solutions for AI from the Endpoint to the Cloud**



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Thank You!