KEERTHANA B

International Institute of Information Technology, Bangalore

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Education

International Institute of Information Technology, Bangalore	**2023 – Present**
Master of Science by Research (VLSI)	*3.3/4 CGPA*
International Institute of Information Technology, Bangalore	**2024**
PG Diploma in Advanced VLSI	*3/4 CGPA*
Government Sri Krishnarajendra Silver Jubilee Technological Institute	**2020 – 2024**
Bachelor of Engineering in Electronics and Communication	*8.14/10 CGPA*

Internship Experience

Karnataka Power Transmission Corporation Limited (KPTCL)	**August 2023 – September 2023**
SCADA System centre Intern	*Bangalore*

- Gained good knowledge about SCADA System

Projects

Design and Verification of Dual Port SRAM

- Implemented Dual Port SRAM using Verilog HDL and verified Using System Verilog Testbench Architecture

Reconfigurable fir filter using control logic for 5G applications

- Implemented a reconfigurable Finite Impulse Response (FIR) filter optimized for 5G wireless communication systems.
- The filter design incorporates advanced control logic to dynamically adjust filter coefficients and characteristics based on real-time channel conditions and signal requirements, ensuring optimal performance across varying frequencies.

AI voice assistant using python

- Implemented voice assistant for desktop

Technical Skills

Languages: Python, C, Verilog, System Verilog
Tools: Yosys, Xilinx Vivado, LTSpice, Cadence Virtuoso, OpenLane, iverilog, Arduino IDE

Relevant Coursework

- System Design Through FPGA
- Digital CMOS VLSI Design
- Hardware Modelling Using Verilog
- Physical Design Using ASIC
- MATLAB

Achievements

- Won 3rd prize in Pitcher Perfect held at BMSCE-2022.

Leadership / Extracurricular

Volunteer in Hack4sustainability conducted by SIEMENS.

Volunteer in International Conference on Computer Vision,Graphics and Image processing 2024 held at IIITB.

Participated as Fellow in VLSID 2025 conference held in Bangalore.