

CHANDAN KUMAR N S

International Institute of Information Technology, Bangalore

+91-8495008958 chandankumar.ns@iiitb.ac.in [linkedin.com/in/chandan-kumar-n-s-8578b521b/](https://www.linkedin.com/in/chandan-kumar-n-s-8578b521b/) github.com/dhanu-07

Education

International Institute of Information Technology, Bangalore **2024 – Present**

Master of Science by Research (VLSI)

International Institute of Information Technology, Bangalore **2024**

PG Diploma in Advanced VLSI *3.52/4 CGPA*

Government SKSJ Technological Institute **2020 – 2024**

Bachelor of Engineering in Electronics and Communication *8.42/10 CGPA*

Internship Experience

Central Manufacturing Technological Institute **August 2023-September 2023**

Sensor Technology Development Centre Intern *Bangalore*

- Worked on developing UltraSonic and Accelerometer Sensors for industrial purposes.

Projects

Design and Verification of APB Slave

- Designed a APB Slave Module in Verilog HDL. The design was tested for different cases using SystemVerilog.

Current Reuse Oscillator Design for 5G Mobile Applications Using 90nm Technology

- Designed Current Reuse Oscillator circuit for 5G applications.

SparkFun Triple Axis Accelerometer Breakout - ADXL335

- Designed a PCB for Breakout board for the 3 axis ADXL335 from Analog Devices.
- The schematic and layout was done using Altium PCB Designer.

Channel Estimation Analysis In MIMO - OFDM Wireless Communication

- The objective of channel estimation is to estimate the time-varying channel between the transmitter and receiver in a wireless communication system.
- To determine the accuracy of the channel estimation process and its impact on the performance of the system.

Line Following Robot using 8051 Microcontroller

- Built a line following robot using 8051 Microcontroller.

Technical Skills

Languages: Python, C, Verilog

Tools: OpenRoad, Yosys, Xilinx Vivado, LTSpice, Cadence Virtuoso, OpenLane, Altium PCB Designer, Keil µVision 5, Matlab

Relevant Coursework

- System Design Through FPGA
- Physical Design Using ASIC
- Digital CMOS VLSI Design
- Machine learning

Certifications

- VSD-SoC Design of the PicoRV32 RISCv micro-processor, Udemy
- VSD - Static Timing Analysis I II, Udemy
- Python 101 for Data Science, cognitiveclass.ai
- Basics of Machine Learning, Great Learning
- Python programming internship at InternPE
- Antenna design, testing and fabrication, Entuple Technologies Pvt Ltd

Achievements/Extra-curricular

- Core Member of various committees for hosting events at Government SKSJ Technological Institute.
- Finalist-Hack4Sustainability, Siemens and Smile Foundation
- Finalist-PitchPerfect, BMS College of Engineering, Bangalore
- Project Exhibition and Paper Presentation in 18th Karnataka State Level ISTE Student Convention, held at Vemana IT, Bengaluru