Rishabh Iyer

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RESEARCH INTERESTS

I am a computer systems researcher. My current research focuses on developing techniques and tools that enable engineers to build systems with *well-understood performance properties*. My research draws on insights and techniques from operating systems, networking, computer architecture, formal methods, and compilers.

EDUCATION

EDUCATION
Ecole Polytechnique Federale de Lausanne (EPFL) Doctor of Philosophy (PhD), Computer Science Thesis: Latency Interfaces for Systems Code Advisors: Prof. George Candea & Prof. Katerina Argyraki
Indian Institute of Technology Bombay
Professional Experience
Postdoctoral Scholar at UC Berkeley
Postdoctoral Scholar at EPFL
Visiting Researcher at UC Berkeley
Summer Intern at EPFL
Honors & Awards
Dimitris N. Chorafas Dissertation Award
eBPF Foundation Research Award
Eurosys Roger Needham Dissertation Award
ACM SIGOPS Dennis M. Ritchie Doctoral Dissertation Award
Best Paper Award
Government of India KVPY Fellowship

1. The Case for Validating Inputs in Software-Defined WANs

Alexander Krentsel, Rishabh Iyer, Isaac Keslassy, Sylvia Ratnasamy, Anees Shaikh, Rob Shakir. Hot Topics in Networking (HotNets), 2024. Acceptance rate: 27.8%

2. Revisiting Cache Freshness for Emerging Real-Time Applications

Ziming Mao, Rishabh Iyer, Scott Shenker, Ion Stoica.

Hot Topics in Networking (HotNets), 2024. Acceptance rate: 27.8%

3. If Layering Is Useful, Why Not Sublayering?

Rathin Singha, Rishabh Iyer, Charles Liu, Caleb Terrill, Todd Millstein, Scott Shenker, George Varghese. Hot Topics in Networking (HotNets), 2024. Acceptance rate: 27.8%

4. Fast, Flexible, and Practical Kernel Extensions

Kumar Kartikeya Dwivedi, Rishabh Iyer, Sanidhya Kashyap.

Symposium on Operating Systems Principles (SOSP), 2024. Acceptance rate: 17.3%

Also accepted to the Linux Plumbers Conference (LPC), 2024

eBPF Foundation Research Award

Upstreamed into the Linux kernel mainline

5. Automatically Reasoning About How Systems Code Uses the CPU Cache

Rishabh Iyer, Katerina Argyraki, George Candea.

Symposium on Operating Systems Design and Implementation (OSDI), 2024. Acceptance rate: 15.6% Also accepted to the Linux Plumbers Conference (LPC), 2024

6. Performance Interfaces for Hardware Accelerators

Jiacheng Ma, Rishabh Iyer, Sahand Kashani, Mahyar Emami, Thomas Bourgeat, George Candea. Symposium on Operating Systems Design and Implementation (OSDI), 2024. Acceptance rate: 15.6%

7. Achieving Microsecond-Scale Tail Latency Efficiently with Approximate Optimal Scheduling

Rishabh Iyer, Musa Unal, Marios Kogias, George Candea.

Symposium on Operating Systems Principles (SOSP), 2023. Acceptance rate: 18.7%

8. The Case for Performance Interfaces for Hardware Accelerators

Rishabh Iyer, Jiacheng Ma, Katerina Argyraki, George Candea, Sylvia Ratnasamy. Hot Topics in Operating Systems (HotOS), 2023. Acceptance rate: 26.4%

9. Performance Interfaces for Network Functions

Rishabh Iyer, Katerina Argyraki, George Candea.

Symposium on Networked Systems Design and Implementation (NSDI), 2022. Acceptance rate: 19.7%

10. Bypassing the Load Balancer Without Regrets

Marios Kogias, Rishabh Iyer, Edouard Bugnion.

Symposium on Cloud Computing (SoCC), 2020. Acceptance rate: 24.4%

Deployed as part of Alibaba's Next-Generation Load Balancer

11. Classification-Based Scheduling for Heterogeneous-ISA Architectures

Nirmal Boran, Dinesh Yadav, Rishabh Iyer.

Symposium on VLSI Design and Test (VDAT), 2020. Acceptance rate: 28.7%

12. Verifying Software Network Functions with No Verification Expertise

Arseniy Zaostrovnykh, Solal Pirelli, <u>Rishabh Iyer</u>, Luis Pedrosa, Matteo Rizzo, Katerina Argyraki, George Candea.

Symposium on Operating Systems Principles (SOSP), 2019. Acceptance rate: 13.7%

13. Performance Contracts for Software Network Functions

Rishabh Iyer, Luis Pedrosa, Arseniy Zaostrovnykh, Solal Pirelli, Katerina Argyraki, George Candea. Symposium on Networked Systems Design and Implementation (NSDI), 2019. Acceptance rate: 14.7%

14. Performance Modeling and Dynamic Scheduling for Heterogeneous-ISA Architectures Nirmal Boran, Dinesh Yadav, Rishabh Iyer.

Symposium on VLSI Design and Test (VDAT), 2019. Acceptance rate: 27.3%

Awarded Best Paper

15. Automated Synthesis of Adversarial Workloads for Network Functions

Luis Pedrosa, Rishabh Iyer, Arseniy Zaostrovnykh, Jonas Fietz, Katerina Argyraki. ACM SIGCOMM Conference (SIGCOMM), 2018. Acceptance rate: 18%

RESEARCH MENTORSHIP

1. Jiacheng Ma (PhD student at EPFL)	Fall 2022 - Present
Performance interfaces for hardware accelerators	
Second author on publication at HotOS'23, lead author on publication at OSDI'24.	

- 6. Narek Galstyan (PhD student at UC Berkeley) Fall 2022 Spring 2023 Application-integrated record and replay for distributed systems
- 7. Musa Unal (summer intern at EPFL \rightarrow PhD student at EPFL) Summer 2022 Fall 2023 Cooperative scheduling for microsecond-scale datacenter applications Second author on publication at SOSP'23.
- 8. Daneshvar Amrollahi (summer intern at EPFL \rightarrow PhD student at Stanford) Summer 2022 Loop summarization for succinct performance interfaces Significant contributor to the PIX open source tool.
- 9. Anastasia Safargalieva (summer intern at EPFL \rightarrow PhD student at TU Denmark) Summer 2022 Performance interfaces for microservice-based distributed applications
- Ayoub Chouak (summer intern at EPFL → security engineer at Taurus SA)
 Using performance interfaces to identify constant-time violations in cryptographic code
 Significant contributor to publication at NSDI'22.
- 11. Beyazit Yalcinkaya (summer intern at EPFL \rightarrow PhD student at UC Berkeley) Summer 2019 Accurately estimating network function throughput

TEACHING ASSISTANTSHIPS

• CS 522: Principles of Computer Systems (EPFL)	Fall 2019, 2020, 2021
• CS 305: Software Engineering (EPFL)	Fall 2018
• CS 306: Software Development Project (EPFL)	Spring 2020
• MA 207: Vector Calculus (EPFL)	Spring 2018, 2019
• PH 107: Quantum Physics (IITB)	Spring 2014

SERVICE

- Member of Program Committee for NSDI 2025, Eurosys 2025, eBPF Workshop (SIGCOMM 2024), SOSP 2024 (Posters), and SOSP Doctoral Workshop 2024.
- Member of EPFL Doctoral Admissions Committee in 2022 and 2023.
- Member of Artifact Evaluation Committee for SOSP 2019.

Talks

 Performance Clarity for Systems Software and Hardware Carnegie Mellon University Systems Research at Google Dagstuhl on Programmable Host Networking UT Austin May 2 	2024 2024
• Automatically Reasoning About How Systems Code Uses the CPU Cache Linux Plumbers Conference (LPC)	
• Achieving Microsecond-Scale Tail Latency Efficiently with Approximate Optimal Scheduling Symposium on Operating Systems Principles (SOSP)	_
• The Case for Performance Interfaces for Hardware Accelerators Workshop on Hot Topics in Operating Systems (HotOS)	023
 Performance Interfaces for Network Functions Systems Research at Google UC Berkeley Harvard University Symposium on Networked Systems Design and Implementation (NSDI) April 2	2022 2022
 Performance Contracts for Network Functions University of Michigan Symposium on Networked Systems Design and Implementation (NSDI) Feb 2 ETH Zurich Imperial College London Cambridge University Feb 2 	2019 2019 2019

REFERENCES

Prof. George Candea

Associate Professor of Computer Science Ecole Polytechnique Federale de Lausanne (EPFL) Email: george.candea@epfl.ch

Prof. Sylvia Ratnasamy

Associate Professor of Computer Science University of California Berkeley Email: sylvia@eecs.berkeley.edu

Dr. David Culler

Distinguished Software Engineer Google

Email: dculler@google.com

Prof. Katerina Argyraki

Associate Professor of Computer Science Ecole Polytechnique Federale de Lausanne (EPFL) Email: katerina.argyraki@epfl.ch

Prof. George Varghese

Distinguished Professor of Computer Science University of California Los Angeles Email: varghese@cs.ucla.edu