CIRCUIT DESIGN FOR PIC ACTIVITY 3

GND

VDD

MPLAB
PIC KIT 4
CIRCUIT DEbugger

R_1

R_2

$0.1\mu F$

MCLR/Vpp/RE3	1	28	RB7/ICSPDAT
RA0	2	27	RB6/ICSPCLK
RA1	3	26	RB5
RA2	4	25	RB4
RA3	5	24	RB3
RA4	6	23	RB2
RA5	7	22	RB1
Vss	8	21	RB0
RA7	9	20	VDD
RA6	10	19	Vss
RC0	11	18	RC7
RC1	12	17	RC6
RC2	13	16	RC5
RC3	14	15	RC4

VDD

$0.1\mu F$

$R = 470\,\Omega$
$R_1 = 10K\,\Omega$
$R_2 = 100\,\Omega$