

This is a bdaq53 noise_occupancy_scan for chip 0x0001.

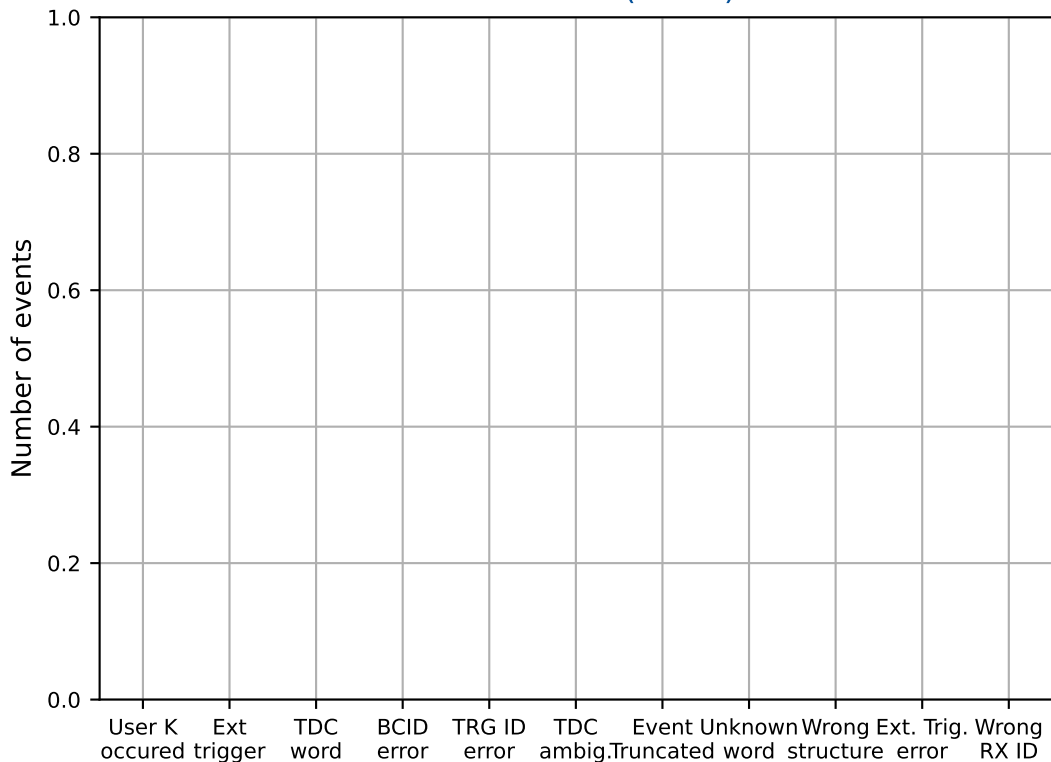
Run 20211112_104839_noise_occupancy_scan was started 2021-11-12 10:48:3

Scan config	Value
min_occupancy	1000
n_triggers	10000000.0
start_column	128
start_row	0
stop_column	264
stop_row	192
trigger_pattern	0xffffffff
wait_cycles	400

SYNC config	Value
IBIASP1_SYNC	90
IBIASP2_SYNC	140
IBIAS_SF_SYNC	80
IBIAS_KRUM_SYNC	55
IBIAS_DISC_SYNC	300
ICTRL_SYNC_SYNC	100
VBL_SYNC	380
VTH_SYNC	390
VREF_KRUM_SYNC	450

LIN config	Value
PA_IN_BIAS_LIN	410
FC_BIAS_LIN	20
KRUM_CURR_LIN	29
LDAC_LIN	185
COMP_LIN	110
REF_KRUM_LIN	300
Vthreshold_LIN	343

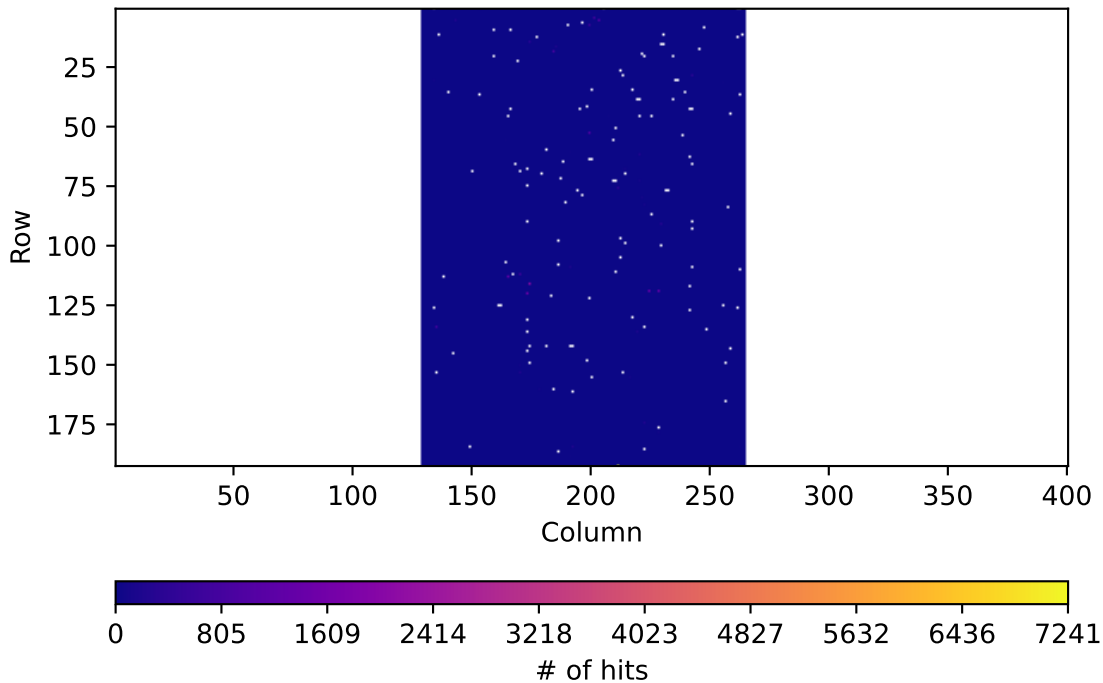
DIFF config	Value
PRMP_DIFF	511
FOL_DIFF	542
PRECOMP_DIFF	512
COMP_DIFF	1023
VFF_DIFF	40
VTH1_DIFF	600
VTH2_DIFF	50
LCC_DIFF	20
CONF_FE_DIFF	0

Event status ($\Sigma = 0$)

rd53a preliminary

Chip S/N: 0x0001

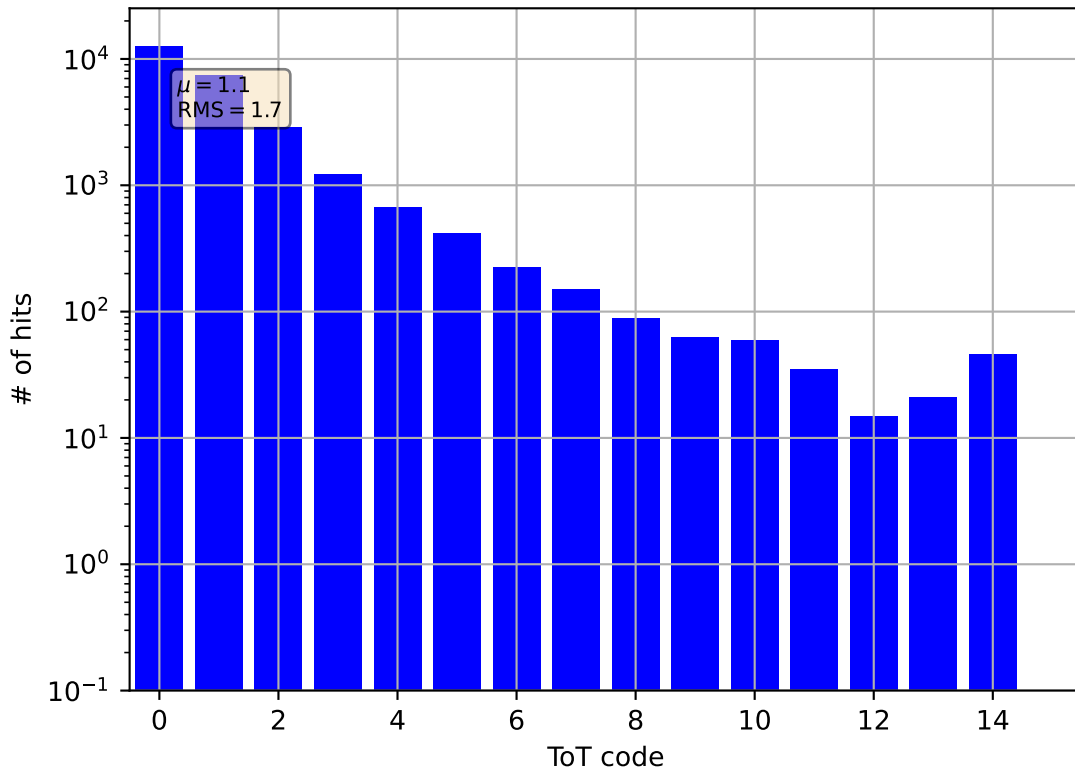
Occupancy ($\Sigma = 25946$)

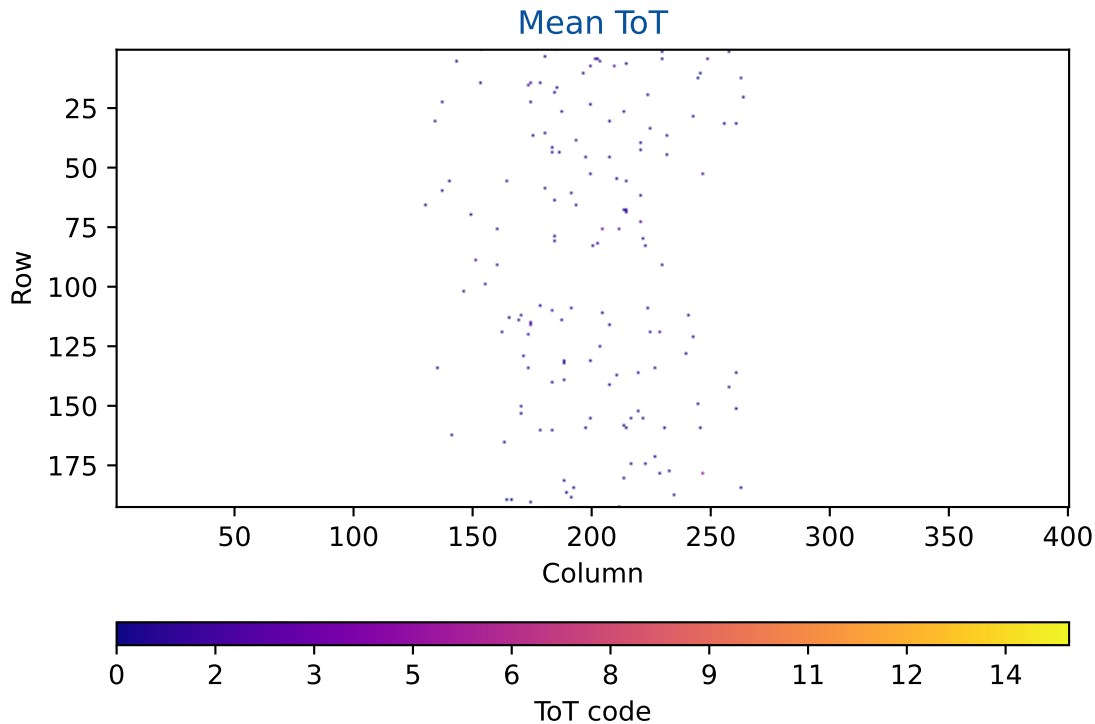


rd53a preliminary

Chip S/N: 0x0001

Time-over-Threshold distribution ($\Sigma = 25946$)

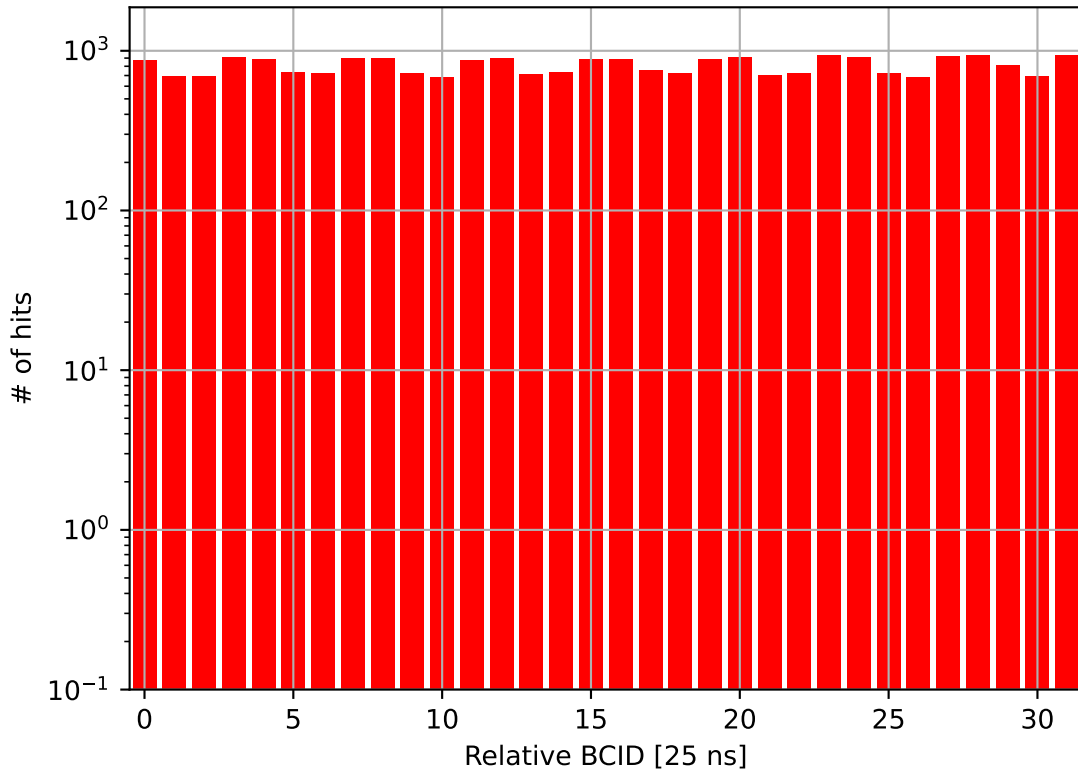


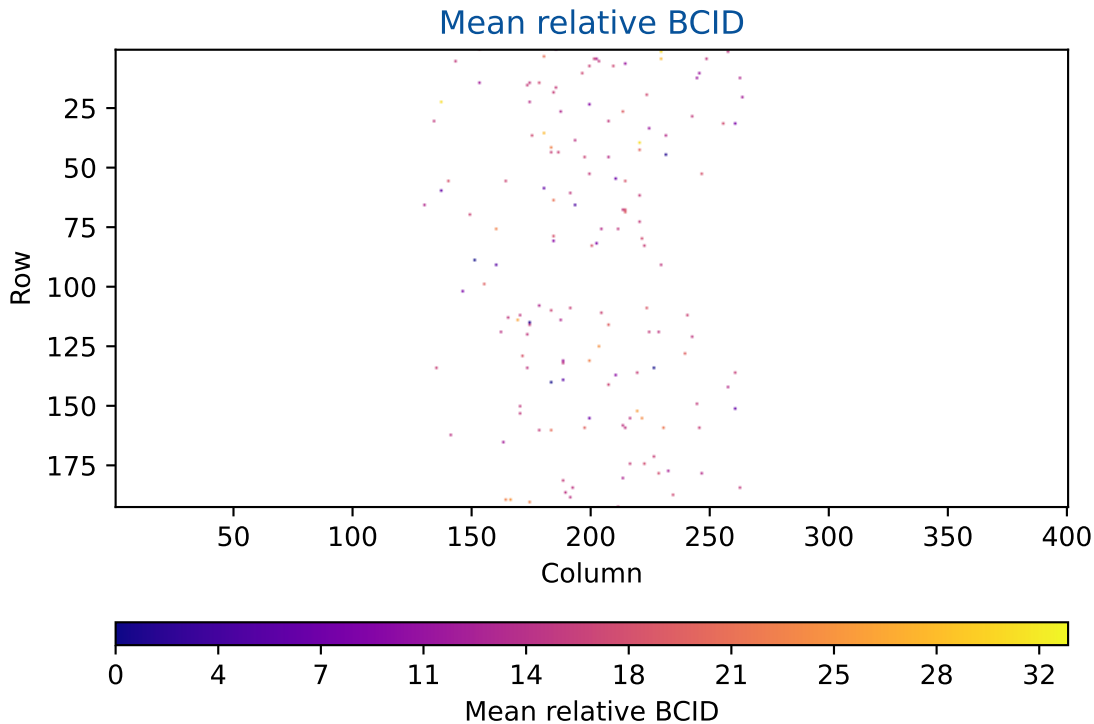


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Chip S/N: 0x0001

Relative BCID ($\Sigma = 25946$)

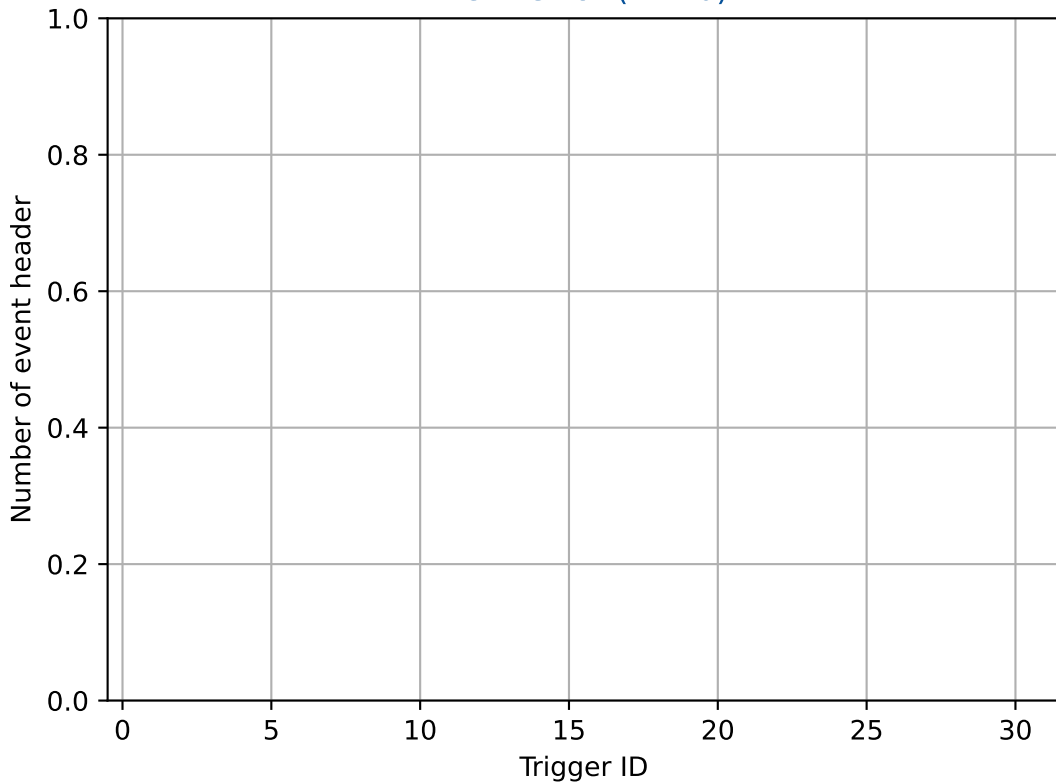




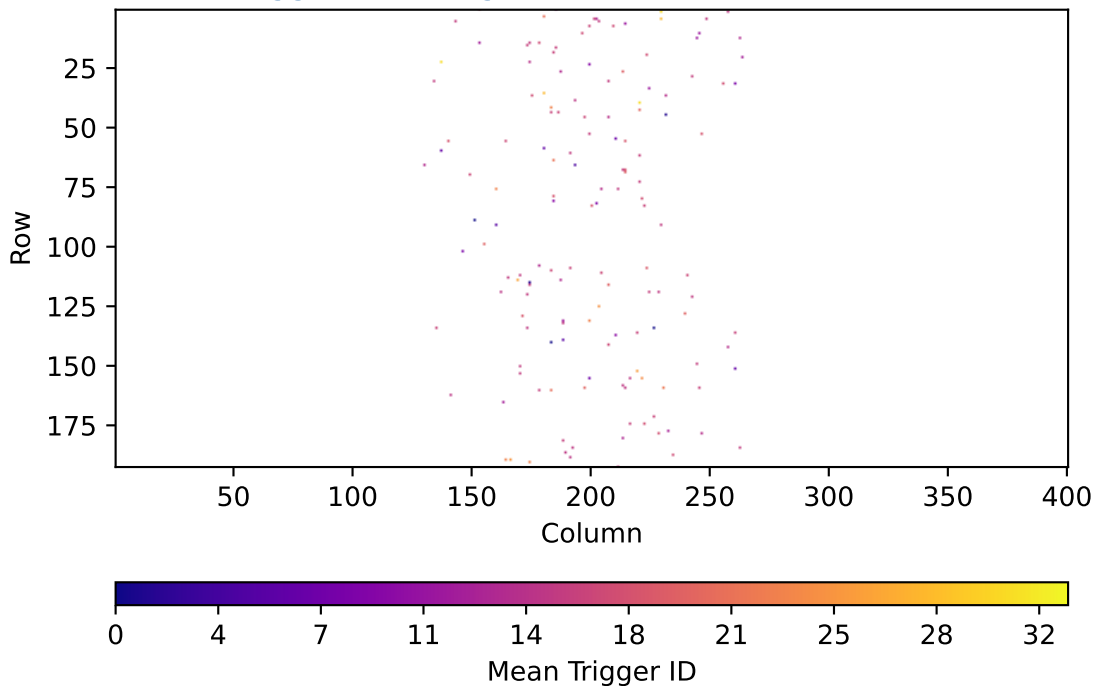
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Chip S/N: 0x0001

BCID error ($\Sigma = 0$)



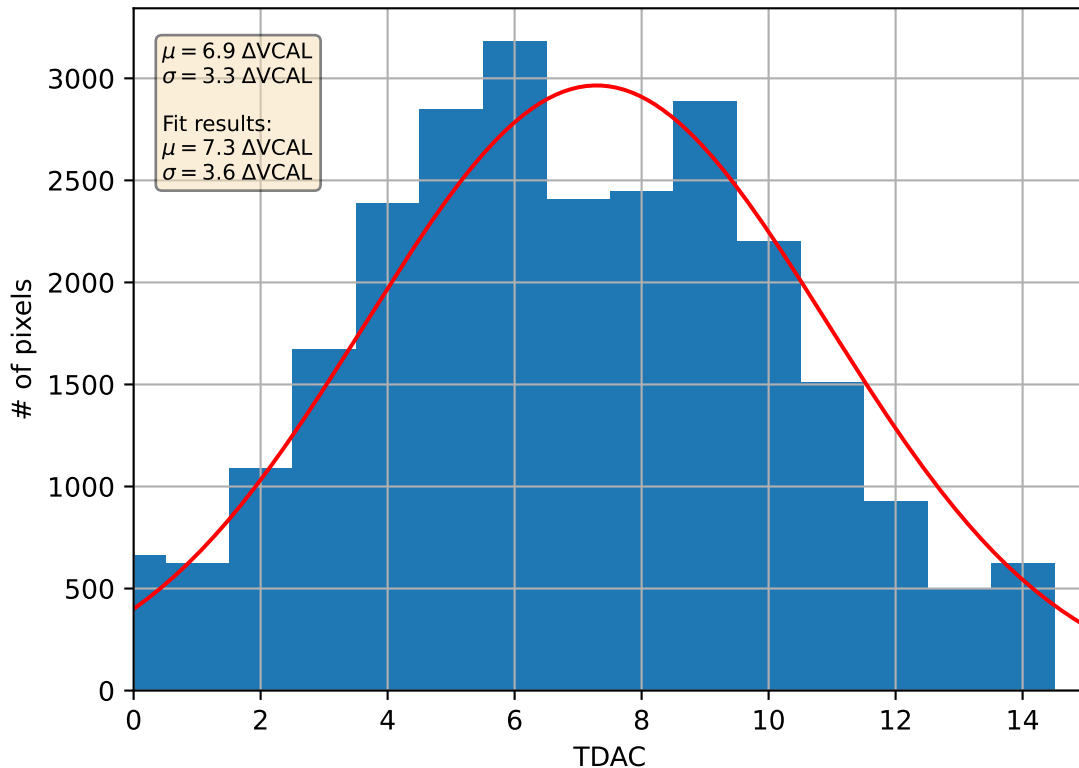
Trigger ID Histogram without Error correction



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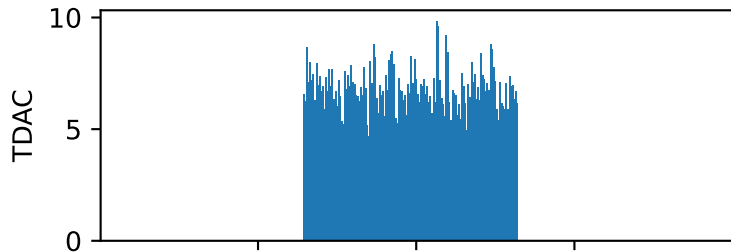
Chip S/N: 0x0001

TDAC distribution for enabled pixels

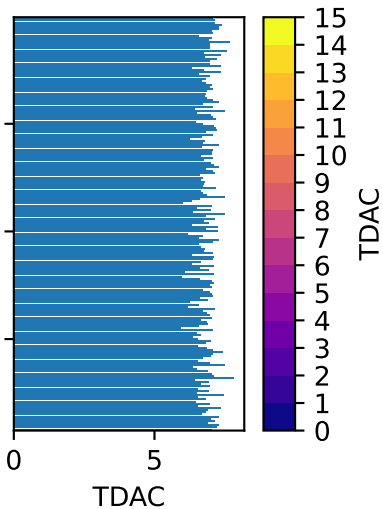
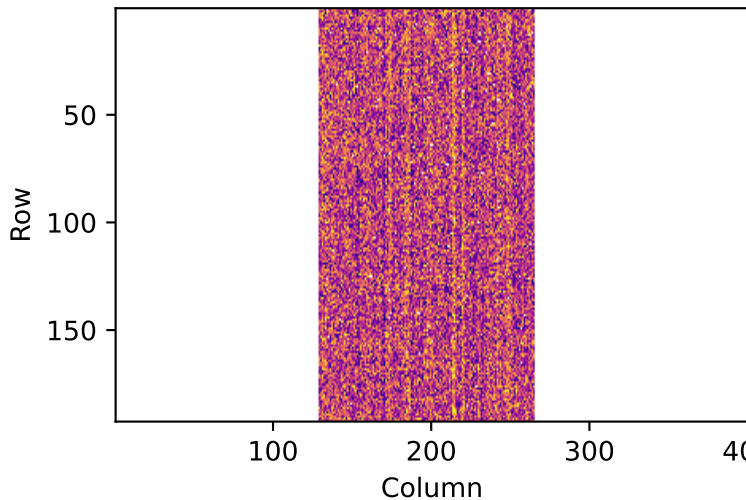


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Chip S/N: 0x0001



TDAC map
with projections
($\Sigma = 178642$)



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Chip S/N: 0x0001

Hits per Pixel (logscale)

