



प्रायोगिक सूक्ष्मतरंग इलेक्ट्रॉनिक्स अभियांत्रिकी तथा अनुसंधान संस्था (समीर)
अनुसंधान तथा विकास प्रयोगशाला, सूचना प्रौद्योगिकी विभाग, संचार और सूचना प्रौद्योगिकी मंत्रालय, भारत सरकार
Society for Applied Microwave Electronics Engineering and Research (SAMEER)
(R & D Lab of Dept. of Information Technology, Ministry of Communications and Information Technology, Govt. of India)

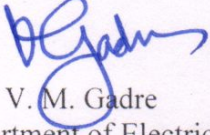
To whom it may concern

This is to certify that Mr. Rishabh Ramteke, student of B.E. Electrical from IIT-Bombay, had practical training as an undergraduate Research in our organisation from 10th March 2019 to 20th May, 2019.

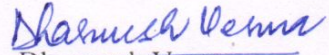
His major contribution was in 'Implementation of GRAPPA algorithm on Xilinx FPGA Zynq chip for MRI image reconstruction' which involved C & MATLAB code, reconstruction & analysis of k-space data from magnetic resonance imaging data streams.

We found him intelligent, diligent and hardworking. He possesses professional research skills.

We recommend him as an intern for his further research work.


Prof. V. M. Gadre
Department of Electrical Engineering
I.I.T. Bombay




Dharmesh Verma
Scientist, S.A.M.E.E.R.

