Rishav Kumar Dokania

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EDUCATION

Texas A&M University   College Station, TX
*MS in Computer Science - **GPA: 4/4***   *Aug. 2022 – 2023*
- **Courses:** Operating System, Software Engineering, Cloud Computing, AI Robotics

Delhi Technological University   New Delhi, India
B.Tech in Electrical & Electronics Engineering - GPA 8.88/10   *Aug. 2015 – May 2019*

TECHNICAL SKILLS

Technical Stack: Node/ReactJS, Git, MySQL, AWS, Jenkins, RoR, Gradle, REST
Area of Expertise: Full-Stack, TDD, OOP, Design Pattern, Linux, Data Structure, OS, Semiconductors
Languages: Java (IntelliJ), Python (PyTest, Pandas, Poetry, Black), C, C++, Linux Bash, JavaScript, HTML, CSS

EXPERIENCE

Cirrus Logic   May 2023 – Aug 2023
Software Developer Intern | Java, Python   *Austin, TX*
- Designed complex reusable Java components with functional, asynchronous programming, saved 28+ hrs/month.
- Built clean parametric unit, robot test on Junit5 framework with mockito for 100% code coverage.
- Architectured 4 generators into 1 object oriented modular, extensible package for automated collateral generation over 30+ commits to reduce turn around time by 25%.
- Setup complex Gradle projects, review peer code, incorporate 100% PR feedback, use established code standards.

Texas Instruments   July 2019 – July 2022
Software/Product Engineer | Python, CI CD, OO Design, Linux   *Bangalore, India*
- Lead object oriented software development to detect pre-manufacturing quality defects in Integrated Circuits. Impact of 1 year, $100M+ business. 3rd highest contributor of Patent Disclosure among 8.
- Orchestrated scalable python infrastructure from scratch using Poetry, Pytest, CLI, Code Style, SVN and CI/CD using Github Actions to streamline DevOps flow by 35%.
- Developed modular self-documented python architecture with poetry. Used Strategy & factory pattern to build 150+ test bench using single generator to reduce 30+ weeks churn time across 15+ Projects.
- Promoted to Senior and received Global recognition for risk management software and robotic solutions. Collaborated with Senior Fellow & Factories to upgrade quality of 25M+ chips/year using 5-Why, 8D techniques.

ACADEMIC & PROJECTS

Web Developer | *Node/ReactJS, AWS, MySQL*   August 2022 – Dec 2022
- Developed 10+ customizable, reusable, and dynamic frontend components using ReactJS, React Hooks with HTML, CSS for Aggie Rotaract club's 1000+ members using Agile CI/CD in AWS.
- Designed data diagram, schema, CRUD and filters for 6 tables in MySQL and used RestAPIs for DB access.
- Instantiated infrastructure as code for AWS EC2 with inbound access to HTTP. Familiar with IAM Roles, Security Groups, S3 bucket, RDS, Beanstalk and TCP/IP protocols.

Software Architect | *Python, ROS*   Jan 2023 – May 2023
- Architected AI Rescue Robot for clean, robust, testable and sustainable software design with 2000+ lines of code.
- Used Machine Learning based Human Interaction library to detect 5 hand gestures and face tracking.
- Designed multi-threading algorithm for parallel image & audio processing. Reduced latency by 95% to below 0.1s

ACHIEVEMENTS

1. Patent Disclosure: Software to Simulate and Visualize all Datasheet Parameters Over the Lifetime of the Product.
2. Texas Instruments Global Recognition Award - 4 times : Software to detect Quality Defect, De-risk and pull in for AWS $100M+ business, Improved Qual Board Design, Released LM74700DDF in record 3.5 months.
3. Publications - 3 Papers: Poster Presentation IC Quality failures (April 2022); Qualification HW Design for contamination issue (Nov. 2022); Co-author, Robotic Handler for Qualification Test Automation (April 2020).