Evaluating the Deployment of Microclimate Prediction Models On Embedded Systems

# **Abstract**

# This paper explores the feasibility of utilizing powerful machine learning models for microclimate modelling andprediction in extremely remote environments. The experiments outlined in this paper simulate rigid constraints on economic cost, computing power, and internet connectivity in order to draw comparisons to real-world settings such as isolated farms or offshore environments, where weather prediction is often critical, but resource constraints make it exceedingly difficult. Cost and computing constraints are considered through the use of an inexpensive STM32F767 board which hosts 2MB of flash memory, 512KB SRAM, and operates within a supply voltage range of 1.7 V to 3.6 V. The paper examines the trade-offs between model accuracy, computing power, and inference latency. Multiple custom neural network architectures are designed, built, trained, **pruned** and **quantized** to minimize memory usage, and deployed to the board, situatedin a rural area within the city of Couva, Trinidad and Tobago. The various model performances are then evaluated and compared. Finally, as we aim to demonstrate the feasibility of deploying sufficiently accurate models within these resource-constrained settings, the performances of the pruned and quantized models are evaluated against full-sized models deployed in the cloud, highlighting that stellar performance can be achieved despite rigorous hardware constraints.

# **Keywords**

TinyML, Embedded Systems, Microclimate, Machine Learning, EdgeAI, Caribbean Climates

# **Introduction**

Accurate weather forecasting is critical for the majority of tasks that we undertake in a wide variety of areas of modern society. In the context of an individual, weather forecasts determine the type of clothing worn, the type of outdoor activities planned and the mode(s) of transportation chosen for travel for work or leisure.

On a larger scale, however, these forecasts become even more crucial. In the field of power demand forecasting, for example, weather is closely tied to energy consumption patterns. For example, weather variables significantly impact the need for heating, cooling and lighting (Diaz-Iglesias et al., 2025, p. 4). In the field of agriculture, Shen et al. found a positive correlation between the frequency at which agricultural production entities pay attention to meteorological forecast information, and a subsequent increase in their income (2024). In the field of offshore installations and maintenance as well, Døskeland et al. highlighted how work performed close to offshore platforms or subsea infrastructure is often very sensitive to weather, and how decisions made around weather forecasts could have serious Health and Safety (HSE) and economic ramifications (2023).

Historically, weather forecasting has been achieved primarily through the use of Numerical Weather Prediction (NWP). Predictions are obtained by numerical integration of partial differential equations to determine the current state of the Earth’s atmosphere (Ben Bouallègue et al., 2024, p. ,y). These equations to model the physical laws of fluid dynamics and thermodynamics were first introduced in the early 20th century. Since then, there have been periodic, incremental improvements in NWP through advancements in supercomputers, numerical modelling and other techniques. Forecast accuracy can be increased by raising the resolution of the model (through reducing time steps or decreasing grid spacing), however, we quickly run into limitations imposed by high computational and timeline costs (de Burgh-Day and Leeuwenburg, 2023).

As highlighted by Kumar et al. (2021), the commercial weather stations which capture the data for these NWP may be located a significant distance away from regions of interest, leading to degradations in climatic patterns and poor forecasting performance. Microclimates describe the climatic parameters (such as temperature, humidity and precipitation) in and around a homogenous, smaller zone of interest. Improving prediction for these microclimates could result in substantial economic gains in an assortment of fields such as offshore-platform activity planning, commercial fishing scheduling, and large-scale agricultural crop rotations.

Data-driven Machine Learning (ML) solutions are being increasingly recognised for their potential in this space, as they are able to deliver these predictions with much lower computational costs. As the amount of publicly-available weather datasets grows, so too does the viability of weather forecasting ML solutions. For example, the European Centre for Medium-Range Weather Forecasts (ECMWF) publishes and maintains a massive open dataset of hourly estimates for global weather data (Copernicus Climate Change Service and Climate Data Store, 2018). Dueben and Bauer were able to utilize this dataset to create several Neural Networks (NNs, one type of ML model available), and were able to forecast weather in a 24 hour timeframe reasonably well (2018).

The recent explosion of research into NNs has given rise to a myriad of model architectures which propagate input data through multiple layers, with each layer progressively extracting higher level features from the data. The figure below illustrates the basic architecture of a neural network. In this example, we have 3 input nodes (which represent 3 features of input data). These 3 input nodes are all connected to 4 hidden nodes (which perform computations on the data to learn its features and patterns), and finally, these are connected to 2 output nodes (which will produce a label or prediction for our input data).



***Figure X:*** *Basic feedforward neutral network architecture (adapted from Goodfellow et al., 2016)*

To further illustrate, if we were to utilize an architecture such as this to predict weather, you might imagine the 3 input nodes as temperature, humidity and pressure data that we collect from sensors, and the output nodes as temperature and humidity that we are trying to predict using our model.

Neural Network architectures with several hidden layers are commonly termed Deep Learning (DL) or Deep Neural Networks (DNNs), and have been shown to be excellent at learning features of the input data, which enables better eventual predictions or classifications (Shiri et al., 2023). Within this broad class of DNNs, there exist subclasses such as Multi-Layer Perceptrons (MLPs), Convolution Neural Networks (CNNs), Recurrent Neural Networks (RNNs) and Long Short Term Models (LSTMs), with each subclass having its own set of particular strengths and being well suited to its own set of tasks.



***Figure X.*** *The hierarchical relationship between several major ML classes, adapted from (Shiri et al., 2023).*

Typically, these ML models are run in large datacenters, on servers commonly referred to as cloud servers. These cloud servers often include powerful Central Processing Units (CPUs) alongside multiple Graphical Processing Units (GPUs), each equipped with several gigabytes (GBs) of memory. This is necessary because these models require large amounts of memory, memory bandwidth, and parallel computing power to handle speedy, high-dimension matrix operations. This, in turn, translates to high power demand by these servers (commonly several hundreds of Watts,W) alongside high operational and maintenance costs. Additionally, it means that all systems that incorporate these models must configure and maintain one (or more) Internet connections (which can be pricey, depending on the location), and must accept the high latency and privacy risks incurred during network communication with these cloud servers.

However, in recent times there has been growing interest in deploying these models on low-power embedded devices on the edge (i.e. close to where we produce and utilize the data). By running these models on these embedded devices at the edge, we can provide better privacy and lower our bandwidth requirements (since we don’t need to transmit all of our data over the Internet), lower our power requirements (to several milliwatts, mW), and provide more real-time data processing (Han et al., 2015). This alternative way of running ML algorithms by using these low-profile devices is termed TinyML (Abadade et al., 2023). More specifically, TinyML refers to ML inference performed on edge devices that typically consume power in the milliwatt range, while devices that consume more than this fall into the broader class of Edge AI devices (Warden and Situnayake, 2019).

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **ML Model Type (and example use case)** | **Platform Example** | **Memory Footprint** | **Power Consumption** | **Internet Connection Required** |
| CNN (Image Classification) | CPU & GPU Server (eg. Intel Xeon & NVIDIA V100) | 200–500 MB | 200–300 W | Yes |
| LSTM (Weather Forecasting) | CPU & GPU Server | 100–300 MB | 50–150 W | Yes |
| Shallow Neural Network (MLP) | Laptop or CPU-only Server | 50–200 MB | 30–80 W | Yes |
| **Quantized CNN (Keyword Spotting)** | Embedded Microcontrollers (e.g. Arduino Nano 33 BLE Sense) | 20–150 KB | ~5–20 mW | No |
| **Tiny LSTM (Temperature Prediction)** | Embedded Microcontrollers (e.g. STM32 / Raspberry Pi Pico) | 100–300 KB | ~50–150 mW | No |
| **Compressed DNN (Gesture Recognition)** | Embedded Microcontrollers (e.g. STM32 / Raspberry Pi Pico) | 100–500 KB | ~1–10 mW | No |

*Table X. Comparison of resource requirements for conventional ML and TinyML models (adapted from Warden and Situnayake, 2019; Codeluppi et al., 2021; Morales-García et al., 2023; Heydari et al., 2025).*

This deployment, though, requires these ML models to be highly optimized and compressed so that they can fit on the on-chip SRAM (Static Random Access Memory) and flash memory, which are both ordinarily only a few megabytes (MB) or hundreds of kilobytes (KB) in size. In order to achieve this, researchers utilize techniques such as pruning and quantization, which are able to produce ML models at much lower sizes, but similar levels of accuracy. Pruning involves removing less important neurons and connections from the models, while quantization involves reducing the precision of weights and activations stored by the model. These techniques can enable compression ratios of up to 49x and memory footprint reductions greater than 90%, while maintaining similar levels of accuracy as the full models (Heydari and Mahmoud, 2025).

In the following section, we further examine research and analysis performed into deep neural network architectures, compression techniques employed on these architectures to enable TinyML, and specific use cases where these TinyML model architectures have been deployed.

# **Related Work**

## Background to Neural Networks and Dense Neural Networks

Sze et al. presented a thorough background and efficiency analysis of DNNs (2017). They first discussed the basic computations involved in NNs, which we will now summarize.



***Figure X.*** *The basic computations involved in a single neuron, adapted from (Sze et al., 2017).*

In the above figure, they illustrate a series of *n* input features of X, fed into a neuron. The *n* features represent different aspects of the input dataset, X, that were captured. For example, a weather dataset might have 3 input features: temperature, pressure and humidity (*n*=3 in this case).

The weights, *w*,represent multipliers that allow the NN to assign lower or higher importance to linear combinations of the input features.

The biases, *b*, are offsets that allow the NN to shift the output so that the function does not always need to pass through the origin.

The functions, *f*, are non-linear activation functions that transform the weighted sum. This allows NNs to closely approximate and model real-world functions. Common examples include the Rectified Linear Unit (ReLU) and hyperbolic tangent (tanh) functions.

The predicted output Ŷ, is then taken as the output of this activation function. The true output Y for several input data points are usually labelled prior, and the accuracy of the NN is computed by measuring the difference between the predicted output Ŷ and the true output Y, using a loss function.

Note that these multiply and accumulate (MAC) operations between X, w and b may occur millions of times across the entire NN. These operations typically dominate the computation cost of the NN architecture. The number of MACs is thus a common measure to benchmark the performance of an architecture, and becomes even more significant in the field of TinyML, where compute power is often severely limited.

In practice, X, Y (and Ŷ) are commonly vectors (i.e data represented by rows and columns), which allow the NNs to process large datasets with multiple dimensions using highly-parallelized computations.

These individual neurons can then be scaled to form layers. A single layer of a NN can be composed of hundreds or thousands of neurons, and DNNs can often have tens or hundreds of these layers. Due to the non-linearity of the activation functions, these large NN architectures have been found to be capable of learning high-level features of input data with significant complexity and abstraction, which allows them to perform advanced predictions of numerical values, recognise composite objects and even interpret entire scenes in images.



*Figure X: An example of an image classification task using a DNN, adapted from (Sze et al., 2017). Image is open-sourced from* [*Unsplash.com*](http://unsplash.com)

Sze et al. then go on to discuss the two high-level stages typically involved in all ML tasks. The first stage is termed the training stage, and involves randomly initializing the parameters (weights and biases) of the model, then splitting the input dataset into three parts to optimize these parameters using techniques such as gradient descent:

1. The training subset is used to improve these parameters by computing a loss (the difference between the estimated output from the model and the true output of the dataset).
2. The validation subset is used to tune hyperparameters for the model (which control things such as the speed at which the model updates its parameters) and prevent overfitting (ensure that the model is generalizable to all future data, and doesn’t just perform well on this input dataset).
3. The testing subset is used to provide an unbiased estimate of the performance of the model, using data that the model has never seen before, so that it can be compared fairly to other models.

To evaluate the performance of the model, two main loss functions are used:

Mean Absolute Error (MAE):

and Mean Squared Error (MSE).

Where ​ = true value, = predicted value, = total number of samples

MAE computes the aggregation of the L1 losses, while MSE computes the aggregate L2 losses. MSE is the most widely used for time series forecasting tasks, but both functions have their comparative advantages. For example, MSE is much more sensitive to outliers than MAE, and MAE is much quicker to compute (Jadon et al., 2024).

This training stage is usually computationally-heavy and time-consuming, and is thus usually performed on the cloud. The output of this first stage is our trained model, which consists of a series of weights and biases along with the activation functions, all arranged in the particular NN architecture (measured primarily by its total MACs).

The second stage is called the inference stage, where the trained model is utilized to predict new datapoints, and it is on this stage that optimized computation can be performed on embedded devices. Since this is the stage at which TinyML can truly be leveraged, we will focus our discussions mainly on this second stage.

## Efficient Neural Networks and TinyML

Typically, the size and complexity of DNN architectures increase as the capability of the model is increased. The table below shows the typical requirements of some popular architectures.

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Metric** | **LeNet5** | **AlexNet** | **Overfeat fast** | **VGG 16** | **GoogLeNet v1** | **ResNet 50** |
| Top-5 error rate | n/a | 16.4% | 14.2% | 7.4% | 6.7% | 5.3% |
| Total Weights | 60k | 61M | 146M | 138M | 7M | 25.5M |
| Total MACs | 341k | 741M | 2.8G | 15.5G | 1.43G | 3.9G |

Table X: An illustration of some typical DNN architectures with their metrics, adapted from Sze et al. (2017)

In this table, we can see that the capability and complexity increase from left to right, and the error rate decreases (i.e. the accuracy increases) in the same manner. The higher number of weights translate directly to higher memory requirements, and the heightened MACs translate to greater processing time. Both translate to a much greater energy requirement for inference in the model.

However, researchers have found several techniques to reduce these weight and MAC requirements, while keeping the accuracy of the model approximately constant. The two main techniques we will delve into are pruning and quantization.

As presented by Abadade et al., pruning a NN involves setting a particular threshold for weights and eliminating those weights below that chosen level. In this manner, only the most significant weight contributors (most important neurons) are kept in the model. There is an initial loss of accuracy, but this can be easily restored through retraining of the sparse NN (as opposed to the initial dense NN) (2023).

Cheng et. al expanded on this by first explaining that pruning could be broadly divided into two classes: unstructured and structured pruning (2024). Unstructured pruning refers to the removal of weights anywhere in the network (in individual neurons), while structured pruning conducts pruning across entire filters, channels or layers. They advised that without special hardware or software libraries, structured pruning was more suitable in most cases because of the complexity in implementing unstructured pruning.

Quantization is also presented as a technique to reduce the precision of weights and activations in the NN architecture. Instead of representing these as 64-bit or 32-bit floating-point numbers, quantized models represent these parameters as 8-bit fixed-point numbers or other, more-efficient schemes.



Figure X: A simple visual illustration of pruning and quantization of a subset of neurons

Abadade et al. also discussed several other techniques for compressing these DNNs such as huffman coding and knowledge distillation, but we chose to focus mainly on pruning and quantization as they were the best supported in standard application and tooling environments (2023).

In the next section, we explore some of these popular toolsets, and how well they’re able to implement these techniques.

## TinyML Framework and Library Analysis

The field of TinyML has created no single framework or library to implement all of the optimization techniques discussed so far. As such, various vendors involved in the embedded system ecosystem have created their own tools to assist in implementing these techniques. Immonen and Hämäläinen presented an overview of several of these tools, and we highlight the three most applicable here (2022).

The first framework discussed was TensorFlow Lite for Microcontrollers (TFLM), which is an open-source framework for running ML inference on embedded devices. It can be used with an assortment of ARM Cortex-M microcontrollers, and is widely supported by vendors such as Espressif and Arduino.

They then discuss the Cortex Microcontroller Software Interface Standard-NN or CMSIS-NN, developed by ARM. This library allows NN interference on ARM Cortex-M processors, and is specifically built to support low-level optimized versions of functions and networks such as convolution layers and softmax layers. This library is not typically used by itself, but rather integrated with other tools such as TFLM.

Finally, they discuss STM32Cube.AI, which is a NN and ML toolkit for the ST family of microcontrollers. This toolkit supports several advanced features such as automatic conversion of pretrained models and support for most other tools such as TFlite and keras, but is only supported for STM32 microcontrollers, leaving a large subset of ARM boards without support.

Several studies note that STM32Cube.AI produces reduced memory usage and faster execution times than TFLM, indicating that it might be preferable for STM32 devices (Hasanpour et al.,2025;Osman et al., 2022). ST also provides a cloud-based AI developer environment, which supports similar optimizations and additionally facilitates remote benchmarking of inference performance across a multitude of their boards using a server board-farm (“ST Edge AI Developer Cloud,” 2023).

The figure below illustrates a typical toolchain for a TinyML workflow.



Figure X: Example of a typical TinyML toolchain for training/inference on an embedded device.

In the next section, we briefly explore some past studies where TinyML devices have been deployed in the field.

## TinyML and EdgeAI Case Studies

Codeluppi et al. performed an extensive experiment on running ML models on edge AI devices, by creating and analysing Long Short-Term Memory (LSTM) networks, Recurrent NN (RNNs) and Artificial Neural Networks (ANNs) (i.e. Multilayer Perceptrons, MLPs) deployed on a Raspberry Pi device (2021). These NNs were used to predict air temperature inside a greenhouse (in its microclimate), and were chosen as they were found in the literature to be particularly good at time-series forecasting.

The authors highlighted how climate variables inside a greenhouse are critical to its commercial operation, as the growth rate of the products being grown depend on variables such as soil moisture, air humidity and temperature. They mentioned that this makes greenhouses one of the best use-cases for such a ML model, as the outputs of the model can be directly connected to actuators to regulate the temperature, which in turn directly improves revenue for the greenhouse operation.

For their experiment, the authors collected air temperature data from sensor nodes deployed within the greenhouse at ten minute intervals, over a period of sixteen months. They then engineered this large dataset to create seventy subsets of data, which were used to train seventeen models using the Keras ML framework.

They found that the RNN and LSTM models resulted in the highest performance, with the lowest root mean squared errors (RMSE) of 0.289 and 0.294 celsius respectively. However, these models required more processing power than the regular MLP. For the overall balance of performance and power, they concluded that the MLP model was the best candidate.



Figure X: Possible application scenario for DNNs in a greenhouse microclimate, adapted from (Codeluppi et al., 2021)

It should be noted that these authors did not employ any pruning or quantization techniques, likely because they were working with a larger, higher-specification single board computer (SBC, with RAM in the range of hundreds of megabytes), and not a true TinyML embedded device.

Deutel et al. studied DNN implementation on ARM Cortex-M-based systems, utilizing pruning and quantization to compress the models for these TinyML devices (2022). They mainly focused on a proprietary compression pipeline that they created, but illustrated that the performance was comparable to a standard pipeline involving Keras, TensorFlow and TFLM.

They utilized a Raspberry Pi Pico and an Arduino Nano 33 BLE Sense, which both come equipped with only 256 Kilobytes of SRAM and one or two megabytes of flash memory. They were able to compress popular DNN architectures such as LeNet, AlexNet and ResNet to enable them to be run on these miniscule amounts of RAM.

|  |  |  |  |
| --- | --- | --- | --- |
| **Model** | **Compression Ratio** | **Equivalent Memory Reduction** | **Relative Accuracy** |
| LeNet | > 99 % | Model size reduced by > 100× | > 99 % |
| AlexNet | > 98 % | Model size reduced by ~50× | ≈ 95 % |
| ResNet | ≈ 93 % | Model size reduced by ~15× | ≈ 96 % |

Table X: Summary of DNN model compression, adapted from (Deutel et al., 2022)

The table above summarizes the results that the authors were able to achieve. In all cases, they found that increasing the compression ratio decreased the execution time required for inference, but also decreased the relative accuracy. Thus, there was a tradeoff between maximum tolerable execution/inference time and minimum tolerable accuracy.

They found that the combination of pruning and quantization produced the best results in terms of lowest memory requirements and lowest execution times. They highlighted the difference between element-wise pruning (removal of individual weights within a layer) and structural pruning (removal of entire structures of weights such as filters, channels, layers etc.), and concluded that structural pruning resulted in better savings of memory and execution time. Additionally, they found that weights quantized to integers resulted in better system performance than when they remained as floating point numbers.

## Comparison of Neural Network Architecture Performance

Lara-Benítez et al. compared the performance of seven popular NN architectures, including MLPs, CNNs and LSTMs, in time-series forecasting tasks across twelve datasets (2021). These time series forecasting tasks were found to have a critical component in a plethora of research areas such as energy consumption, retail sales and, most importantly for us, weather.

For evaluating the performance, instead of using the Mean Absolute Error (MAE), the most commonly used metric for evaluation, they chose instead to use a weighted error (termed the Mean Absolute Percentage Error, MAPE) by dividing the MAE by the mean of the dataset. This allowed them to obtain an error that could be compared across multiple time series datasets.

After statistical analyses of the MAPE of each architecture in each dataset, they found that the MLPs generally performed the worst, and the LSTMs and CNNs performed the best (had the lowest MAPEs).

When considering efficiency of the architecture (length of time required for both training and inference), MLPs were found to perform the best, with CNNs following right after. This led them to conclude that CNNs resulted in the best speed/accuracy tradeoff.

They also suggested that CNNs generally performed best for time series forecasting with four layers, and no max-pooling layers. There was no conclusive evidence on the best number of filters to utilize. This provided a good starting point for our own implementation.

In the next section, we draw on all of the knowledge we reviewed thus far to set up our own experiment.

# **Materials and Methods**

For our own research, we set out to build a system capable of forecasting outdoor air temperature in a rural Caribbean location, using ML on a simple, low-power embedded system.

As illustrated below, we first set up several sensors to record and log ambient environmental data at the location, after which we preprocessed the data and set out building the full ML model.

Once the model was built, we pruned, quantized and evaluated the model, before deploying it on our embedded board. We then analyzed the performance of the deployed model while performing inference, and stepped back to ML model building to fine-tune the architecture and hyperparameters of the model as required to improve our results.



Figure X: The overall process that we used to guide our experiment

We dive into more detail of each step in the following sections.

## Sensor Data Collection

We utilized three sensors to collect the local environmental data that we needed for the initial training:

* A BME280: To measure temperature, humidity and pressure
* A VEML7700: To measure light intensity
* A LM393: To measure whether it rained or not

Besides these sensors, two other modules were required in order to build the dataset accurately:

* A DS3231 Realtime Clock (RTC) module: To maintain an accurate internal clock for timestamps
* A MicroSD Breakout Board and MicroSD card: To store the periodic measurements from the sensors

An STM32 F767 board was used as the core of the system. The following figure illustrates the physical connections between these modules and the board:

A diagram of a computer

AI-generated content may be incorrect.

Figure X: Physical connections between the modules discussed and the STM32 board

Several small breadboards and jumper wires were used to make the physical connections.

The board was configured through STM32 Cube IDE with the FreeRTOS operating system to coordinate tasks for each module.

A diagram of a diagram

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Figure X: Simplified View of the State Machines of Modules Coordinating Data Collection

The above figure illustrates a simplified version of the state machines configured for the main modules of the system. Each sensor ran in its own task (or thread), and put its valid results onto the measurement queue every ten minutes. The measurement task initialized the file system, directory and daily log file on the MicroSD card, then serviced the queue and flushed the recorded data (with their timestamps) to the daily csv log file.

A screen shot of a computer

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Figure X: An illustration of the csv data persisted to disk

The figure above shows a snippet of the data that was collected in a single CSV file. In the following section, we discuss how we aggregated, cleaned and normalized data from the entire collection of files.

## Sensor Data Preprocessing

## ML Model Building

Since our literature review showed that CNNs were a good choice for a balance between model forecasting performance and hardware resource constraints, we decided that it would be a suitable architecture for our use case.

A close-up of a diagram

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Figure X: The CNN architecture we created

Our input layer consisted of a feature vector of size [1x48x7], with the first dimension (1) representing a single batch of time sequences (in 30min batches), the second dimension (48) representing the historical size of the time slots in the window, and the third dimension (7) representing the number of features of our input data used in each time step.

The input layer feeds into the first convolution block, which consists of 2 convolution layers followed by a max pooling layer. The first convolution layer consists of 16 filters with a kernel size of 5 and a ReLU activation function, allowing the model to learn low-level temporal patterns across all the input features over a series of 5 time-steps, while keeping the length of the time-axis constant.

The second convolution layer consists of 16 filters with a kernel size of 3 and a ReLU activation function, enabling the model to learn higher-level patterns about our data by extending the first layer’s representations. The max pooling layer then computes the maximum across every 2 time-steps, reducing the temporal size of the feature maps produced by the convolution layers, thereby ensuring that the computational cost of the following layers remains manageable.

The separable convolution layer consists of 32 filters with a kernel of size 3 and another ReLU activation layer. This layer breaks standard convolution into depthwise and pointwise operations, examining features along each channel independently, then mixing the channels together in a linear combination. This allows us to create more channels for richer representations of patterns across the training data, while keeping the number of parameters and MACs lower than a standard convolution layer.

The data then flows to a global average pooling layer, which computes averages of each feature map learnt (i.e. the new channels learnt in the previous steps) across the temporal axis, while keeping the number of parameters comparatively low.

The dropout layer prevents overfitting (a phenomenon when models become too closely tied to their training data and perform poorly on other, newer data) by randomly assigning 10% of the weights in the model to 0.

Finally, the dense output layer produces our forecasted temperature values, with the number of output units equal to a variable called *forecast\_slots*. We purposefully left this as a variable for flexibility with testing. For our final set of tests, we set this value to be 24, so that we could forecast 12 hours of data (24 forecast slots of 30 min time intervals).

## ML Model Pruning, Quantization and Evaluation

## TinyML Model Deployment

# **Results and Discussion**

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# **Conclusion And Future Work**

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