

MR. ABHIJEET S. HONWALKAR
(Electronics and Telecommunication)
RMD Sinhgad college of Engineering Pune.

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Objective:

Looking for a challenging career which demands the best of my professional ability in terms of technical and analytical skills that will help me in broadening and enhancing my current skills and knowledge.

Educational Qualification:

B.E. (Electronics and Telecommunication)	- Pune University –2017	61.55%
Diploma (Electronics and Communication)	- MSBTE Board–2014	74.51%
S.S.C -	-Amravati Divisional Board –2011	84.73%

Skills Set:

Programming Languages: Core java, SQL.

Technical skills: MANUAL TESTING, AUTOMATION TESTING(selenium).

Experience: I have Completed & Certified SOFTWARE TESTING course from TechRel Institute.

Operating Systems: Windows OS, Linux.

Software known:4nec2, Proteus,DSCH3,Xilinx.

Academic Project Details:

Title: Vedikullach Implementation on FPGA.

Subject Area: VLSI.

Nature of Project: Hardware and Software

Software Used: DSCH3,XILLINX.

In this project, we implement a high speed Multiplier using algorithm mentioned in Indian ancient Vedic mathematics which is utilized for all case of multiplication and to improve the performance of multiplier.

In this project the Vedic multiplier is compared with the existing method such as booth and array multiplier. The Vedic multiplier is designed using Urdhva and Nikhilam sutras. This algorithm reduces the overall delay of the multiplier unit.

Co-Curricular Activities

- Actively Participated in Arduino Workshop.
- Participated in PCB Designing Workshop.
- Attended Raspberry Pi Workshop.
- Participated in event 'APP' Solute Designing.
- Student Coordinator e-WORTS association committee.
- Participated in basic of Linux, Raspberry pi & Arduino Workshop.
- Training of Basic PLC programming,(Indo-German initiative).
- Training of Advanced PLC programming (Allen Bradley).

Extra-Curricular

- Participated in sports (Football) at university level.
- Participated in cultural event in college.
- Active member of 'Paris Parv' social Organization.
- Winner of dance competition in school gathering.

Strength:

- Adaptable to new environment, with ability to get along with people.
- Willingness to learn & work in team.
- Confidence and Positive Attitude.

Field of Interest:

- Software Testing
- Hardware Testing
- VLSI field

PERSONAL DETAILS

Name	:	Abhijeet Sanjay Honwalkar
Date of Birth	:	1 July 1995
Gender	:	Male
Marital Status	:	Single
Nationality	:	Indian
Languages Known	:	English , Hindi and Marathi
Hobbies	:	Dancing, Cooking,Playing cricket.
Permanent Address	:	Bhatiya Layout,gopal nagar, khamgaon 444303,dist.Buldhana.

DECLARATION

I hereby declare that the particulars mentioned in this document are true to the best of my knowledge and belief.

Place:

Date:

[AbhijeetHonwalkar]