

Hardware Assignment Report

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Assignment :

Random Number generation using Shift Registers.

Components required :

1. 555 IC (Timer IC)-1
2. D flip-flops(7474)-2
3. XOR gate(7486) -1
4. Resistor(1M Ω)-1
5. Resistor(1K Ω)-1
6. Capacitor(100nF)-1
7. Capacitor(10nF)-1
8. Connecting wires-20
9. Breadboard -1
10. Seven-segment display(common anode)-1
11. decoder(7447)-1

Function of each Component:

1. Breadboard: It provides a platform for connecting and prototyping electronic components without the need for soldering.
2. Decoder: It decodes the binary inputs from the D-flip flops and activates the corresponding outputs based on the binary pattern.
3. D-flip flops: They are sequential logic circuits that store and transfer binary data, In this context, they act as part of the shift register, holding the binary values and shifting them based on the clock pulses.
4. Resistors: They are passive electronic components that control the flow of electric current in a circuit, In this context, resistors may be used to provide current limiting or voltage division.
5. Capacitors: They are passive components used to store and release electrical energy, In this circuit, capacitors may be used in conjunction with resistors to determine the frequency of the clock signal produced by the 555 IC.

6. Connecting wires: These are used to establish electrical connections between different components on the breadboard.
7. Seven-segment display: It is a display device that can show numerical digits and certain characters, In this circuit, it is used to display the generated random numbers.
8. XOR gate: It is a logic gate that outputs true (1) if the number of input signals that are true (1) is odd, In the context of the shift register, XOR gates introduce feedback loops that create the randomness in the system.
9. 555 IC: It is a popular integrated circuit that can be configured as an astable multivibrator, producing a continuous stream of clock pulses, In this circuit, the 555 IC is used to generate the clock signal for the shift register.

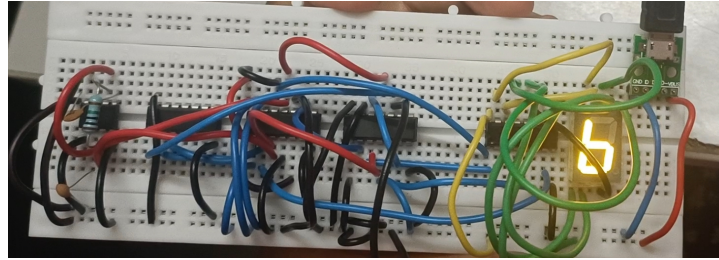


Figure 1: Random Number generation using Shift registers and XOR Gate

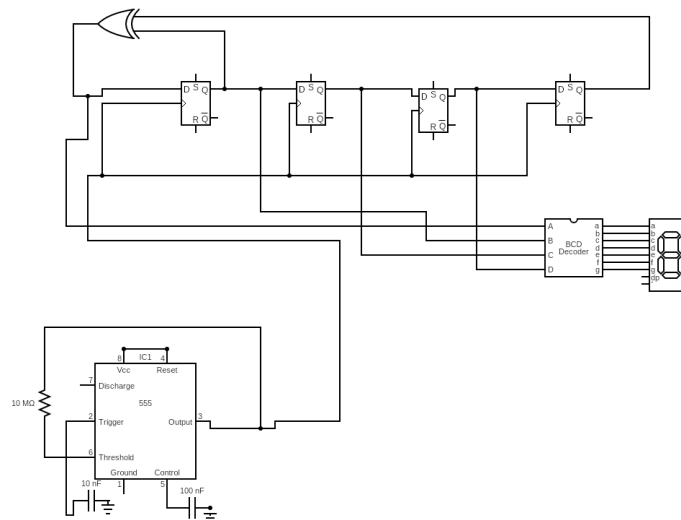


Figure 2: Block Diagram of the circuit