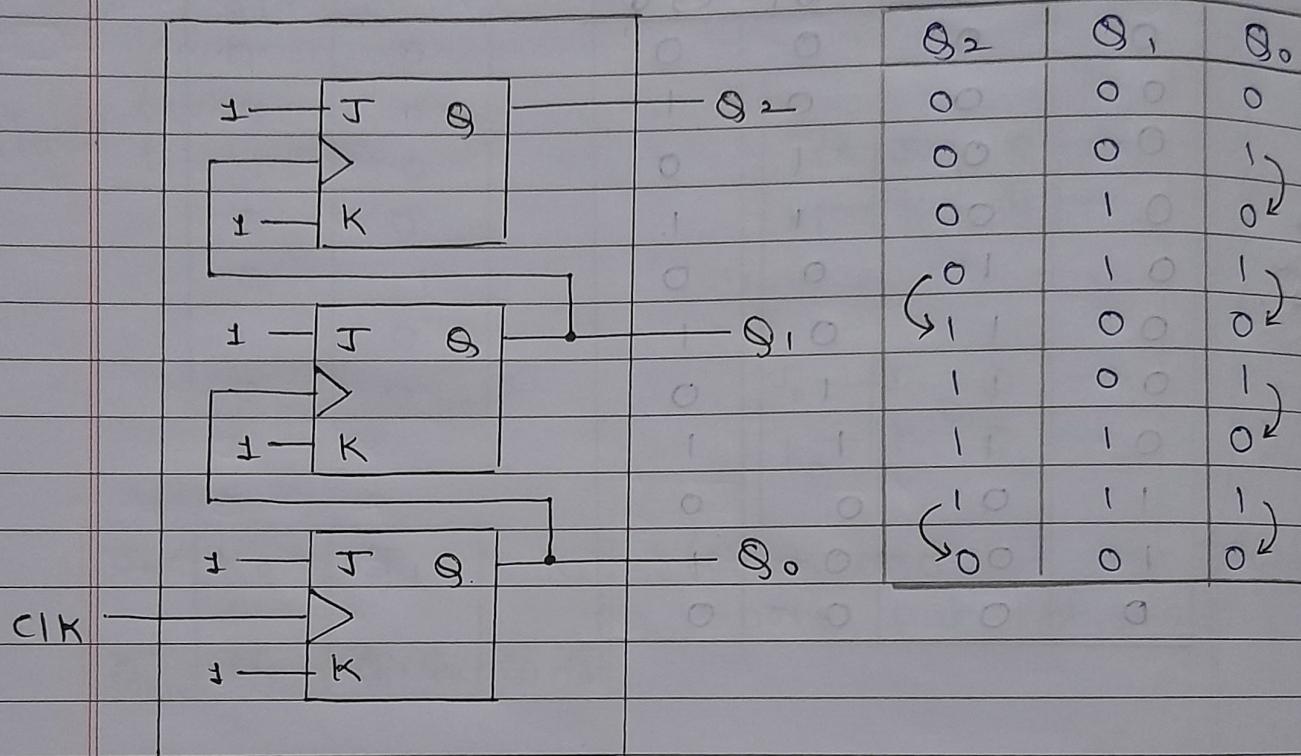


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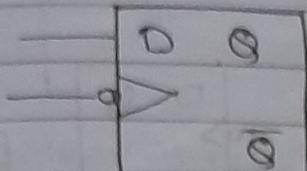
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- Asynchronous counter:-
clock is given only to the least significant flipflop, in an asynchronous counter.



$B(001,000) \rightarrow 0 \cdot 10 \quad (0,0,0,0) \cdot 0 \quad 1 \cdot 1$

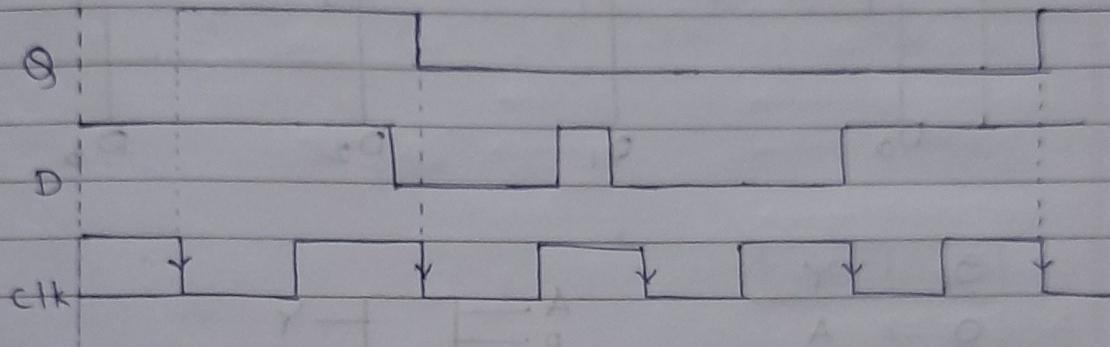
• Shift Registers :-



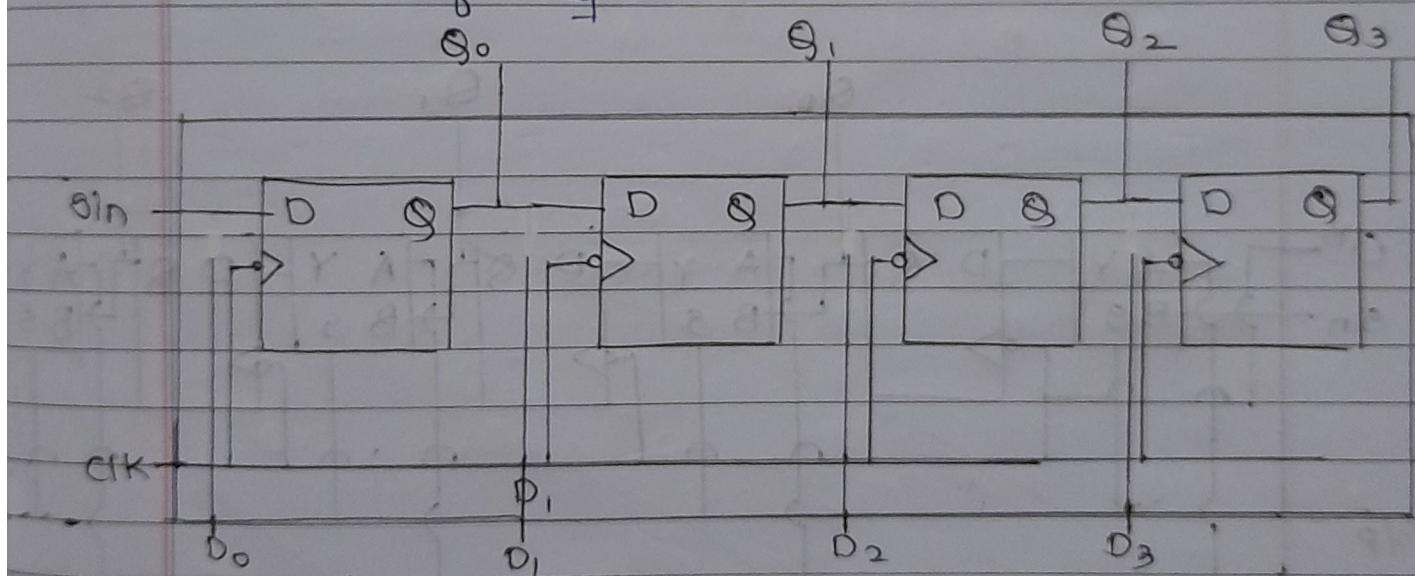
D Latch.

Truth Table :-

Clk	Q
0, 1, T	Lost state
↓	D.

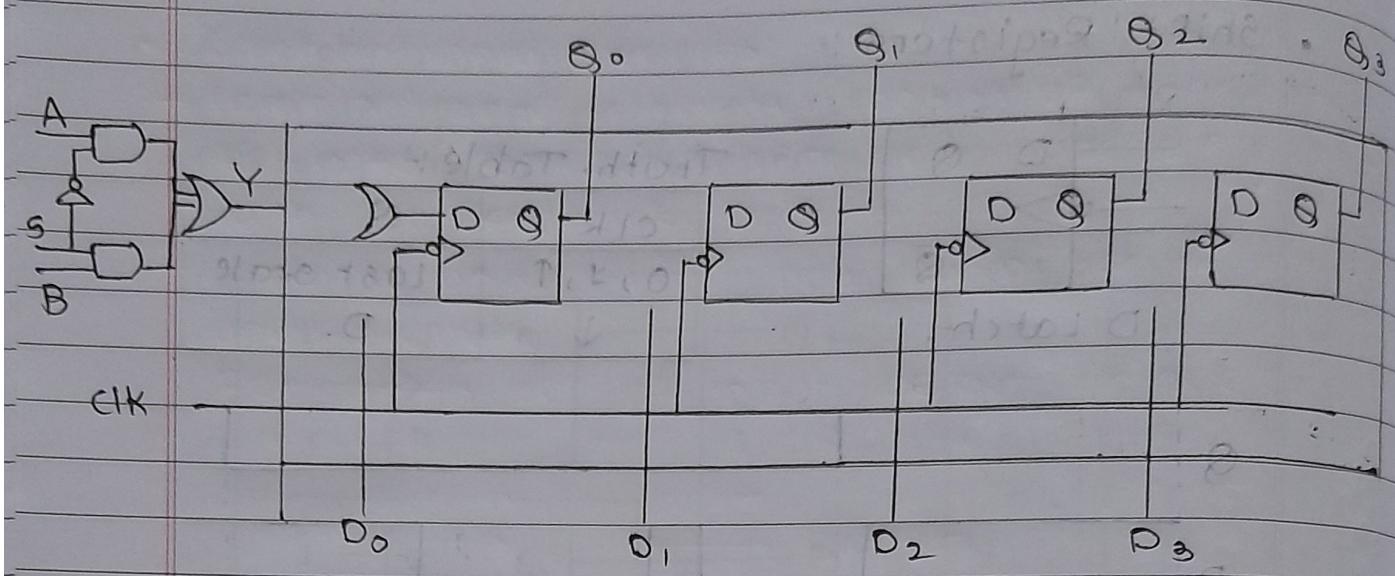


• 4 bit shift register

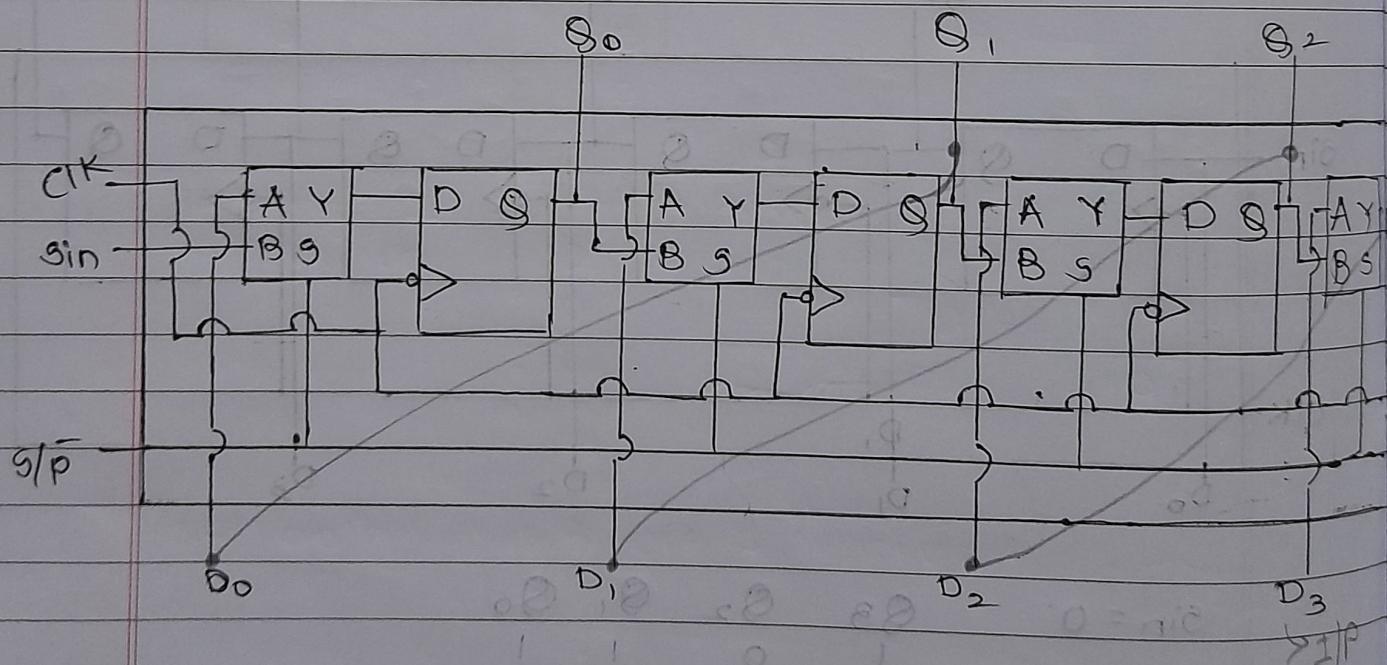
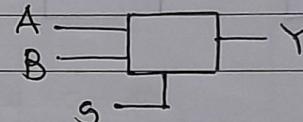


$$\begin{array}{l} \text{sin} = 0 \\ \quad \quad \quad Q_3 \quad Q_2 \quad Q_1 \quad Q_0 \\ \quad \quad \quad 1 \quad 0 \quad 1 \quad 1 \end{array}$$

$$\begin{array}{l} \quad \quad \quad 0 \quad 1 \quad 0 \quad 0 \end{array}$$



S.	Y
0	A
1	B



parallel $\leftarrow S/P = 0$ D_0 D_1 D_2 D_3
mode
serial $\leftarrow S/P = 1$ sin Q_0 Q_1 Q_2 Q_3

e.g. $Q_3 \ Q_2 \ Q_1 \ Q_0$ (initial condition)

$Q_3 \ Q_2 \ Q_1 \ Q_0$ (final condition)

Initial condition - $Q_3 = 0, Q_2 = 1, Q_1 = 0, Q_0 = 0$ (at stop)

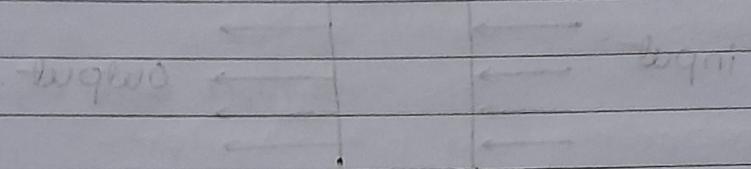
Final condition - $Q_3 = 1, Q_2 = 0, Q_1 = 1, Q_0 = 0$ (at start)

Reverse shifting \Rightarrow $Q_3 \ Q_2 \ Q_1 \ Q_0$ (initial condition)

$$S/P = 0 \quad Q_3 \ Q_2 \ Q_1 \ Q_0$$

$$Q_3 \ Q_2 \ Q_1 \ Q_0$$

$$0 \quad 1 \quad 0 \quad 1 \quad x$$



→ $Q_3 \ Q_2 \ Q_1 \ Q_0$

Initial condition

Final condition

		Q3	Q2	Q1	Q0
0		0	0	0	0
1		1	0	1	0
2		1	0	0	1
3		0	1	1	1

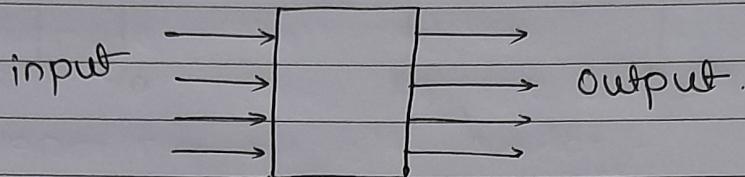
DQ

	Q3	Q2	Q1	Q0
1	1	0	0	0
2	0	1	0	0
3	0	0	1	0

$$L = \mu \cdot I^2 \cdot A \quad \text{for } L = \mu \cdot I^2 \cdot A$$

- Combinational circuits:
 - combinational circuits consist of interconnection of gates to each other and cascading them with no feedback.
 - combinational circuit has given set of input and outputs.

For given set of input we get unique output.



- Adder \Rightarrow

In Binary Arithmetic.

A	B	carry	sum
0	0	0	0
0	1	0	1
1	0	0	1
1	1	1	0

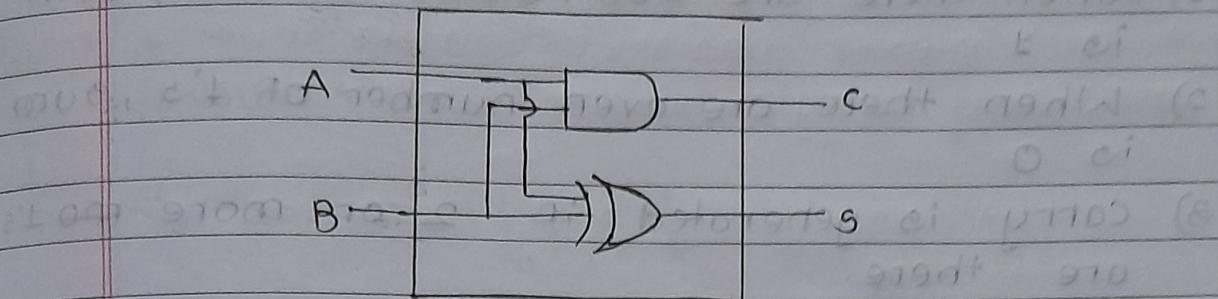


Adding 2, 1 bit number

explanation:

$$\begin{array}{r}
 & 0 & 0 & 1 & 1 \\
 + & 0 & 1 & 0 & 1 \\
 \hline
 0 & 0 & 0 & 1 & 0 \\
 \uparrow & \uparrow & & & \\
 c & s & & &
 \end{array}$$

IF both A & B are 1, then carry = 1



Half Adder.

- Adding 2, 2 bits number.

e.g.

A_1	$-A_0$	B_1	B_0	C_1	C_0	S
1	0	1	0	0	1	1
$A \rightarrow$			$B \rightarrow$			Cin

- Addition of 2 Bit number also a bit number.

- Here we need full adder.

Truth table of Full adder \Rightarrow

cin	A	B	cout	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

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- 4) When there are odd number of 1's, sum is 1
- 5) When there are even number of 1's, sum is 0
- 6) carry is generated if 2 or more 1's are there.

- 2 Half Adder and 1 OR gate is used to prepare Full Adder.

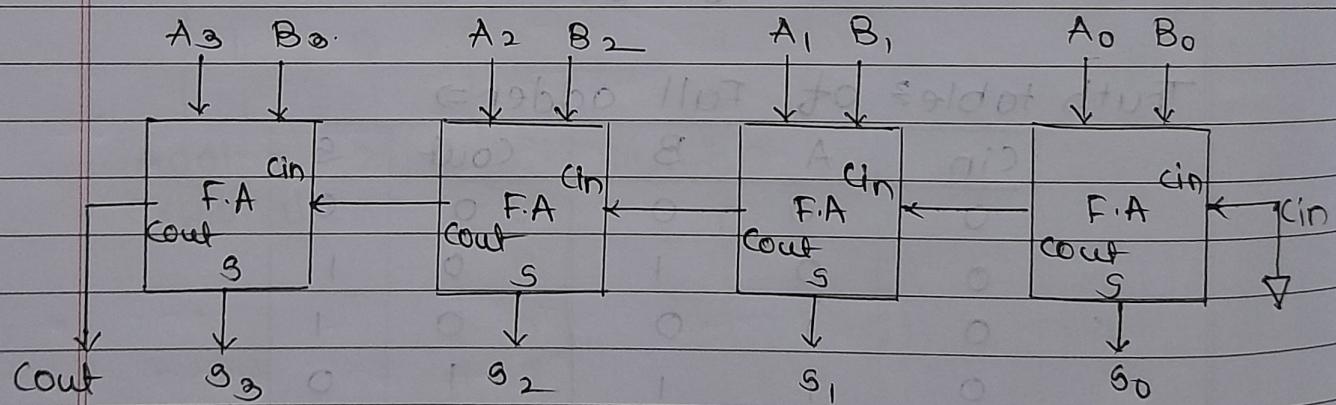
• preparing ^{Full} 4 bit Adder:-

Adding 2 numbers of 4 bits.

$$\begin{array}{r} 0101 \leftarrow A \\ + 1101 \leftarrow B \\ \hline 1101 \text{ Cin} \end{array}$$

$$\begin{array}{r} 0101 \leftarrow A \\ + 1101 \leftarrow B \\ \hline 1101 \text{ Cin} \end{array}$$

$$\begin{array}{r} 0101 \leftarrow A \\ + 1101 \leftarrow B \\ \hline 1101 \text{ Cin} \end{array}$$



$$\begin{array}{r}
 A_3 \quad A_2 \quad A_1 \quad A_0 \\
 + B_3 \quad B_2 \quad B_1 \quad B_0 \\
 \hline
 \text{Cout} \quad S_3 \quad S_2 \quad S_1 \quad S_0
 \end{array}$$

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A₃ A₂ A₁ A₀ B₃ B₂ B₁ B₀

↓ ↓ ↓ ↓ ↓ ↓

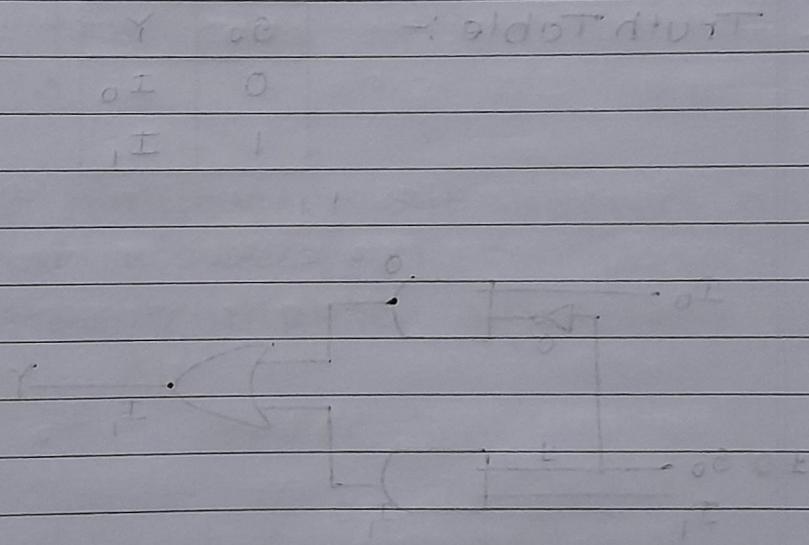
cout ←

↓ ↓ ↓ ↓

g₃ g₂ g₁ g₀

cin pao bar

A 4 bit full Adder comprises of 1 carry in,
1 carry out, 2 4 bit inputs A and B,
1 4 bit output sum.



19x2lithium 1: 90 107 .

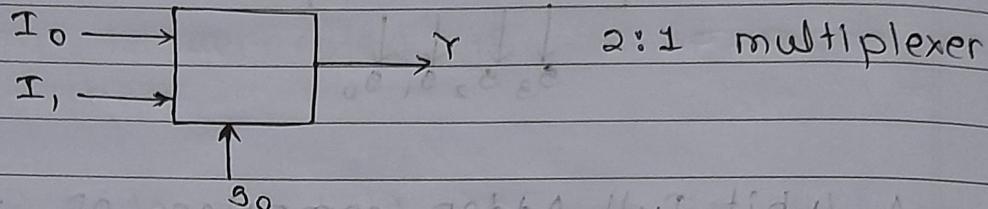
a₀ ← enil buan

b ← enil tuquo

c ← enil b90a

- Multiplexer \Rightarrow (Many in to 1)

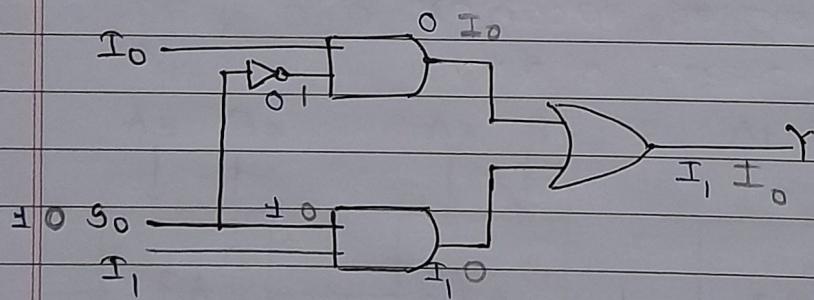
- A multiplexer consist of more than 2 inputs and only single output.



- S_0 is select line that decides which input should come on the output.

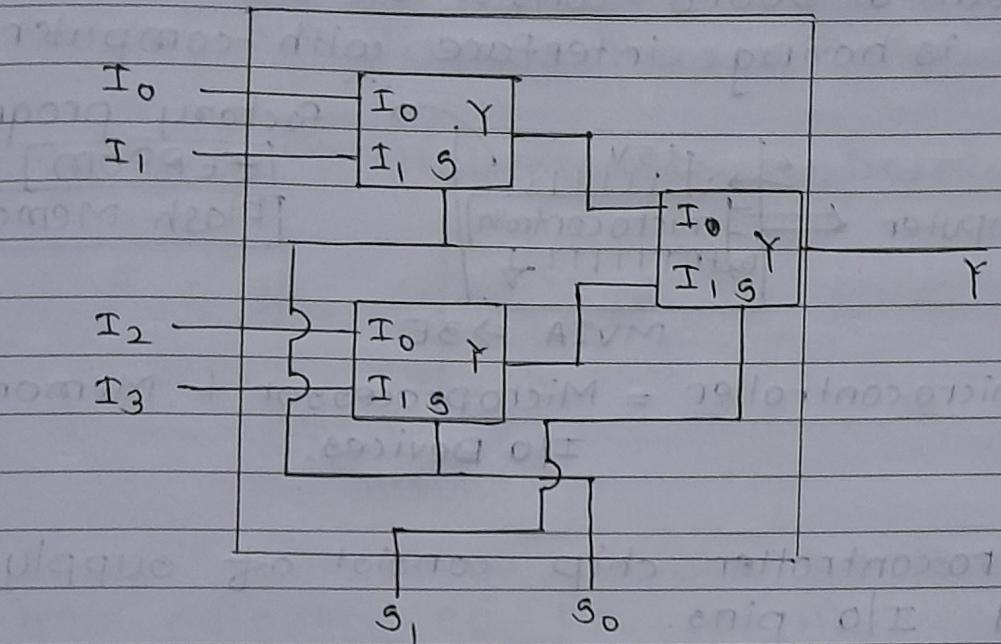
Truth Table :-

S_0	Y
0	I_0
1	I_1



- For $2^n : 1$ multiplexer,
input line $\Rightarrow 2^n$
output line $\Rightarrow 1$
select line $\Rightarrow n$.

• 4:1 Multiplexer \Rightarrow



1) $S_1 = 0$, Top multiplexer o/p appears on final o/p.

2) $S_1 = 1$, Bottom multiplexer o/p appears on final o/p.

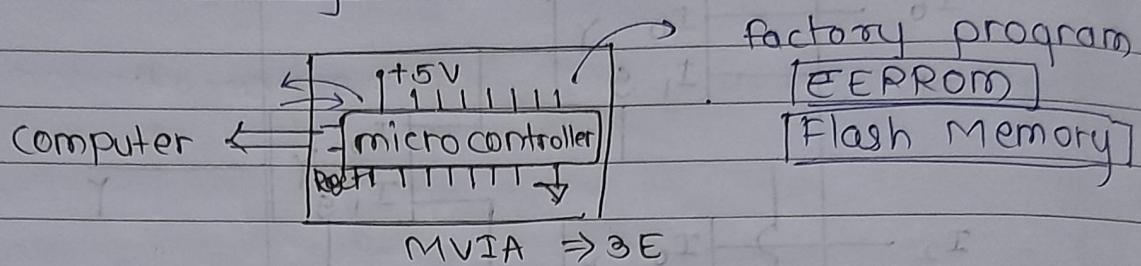
Truth table:-

	S_1	S_0	Y
	0	0	I_0
	0	1	I_1
	1	0	I_2
	1	1	I_3

S_0 decides which output should appear on 1st layer multiplexer.

S_1 decides final o/p.

- Arduino Board \Rightarrow Arduino Board consist of micro controller chip, it is having interface with computer.

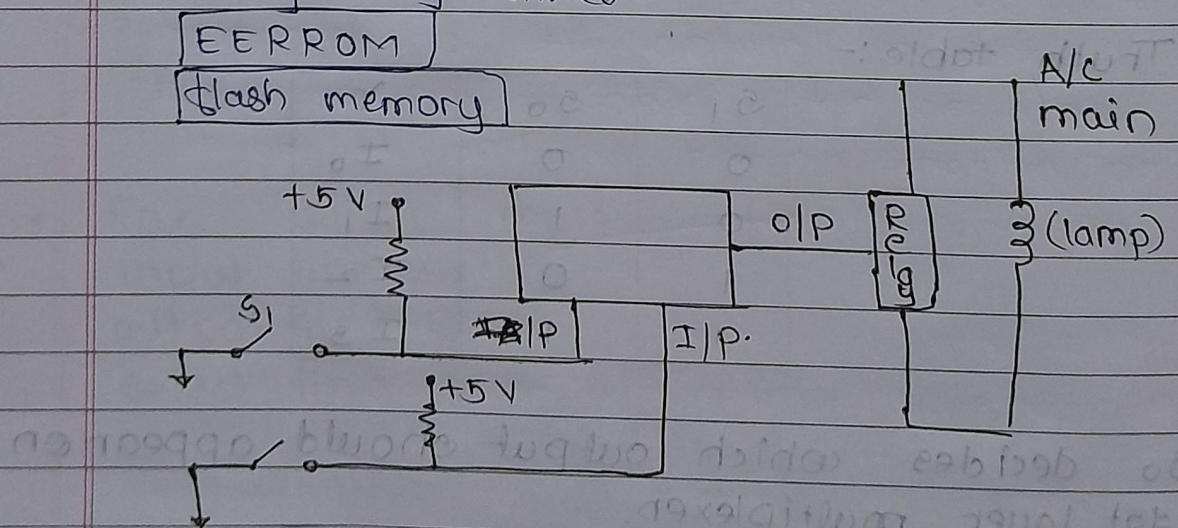


micro controller = Microprocessor + Memory + I/O Devices.

- Micro controller chip consist of supply pins and I/O pins.

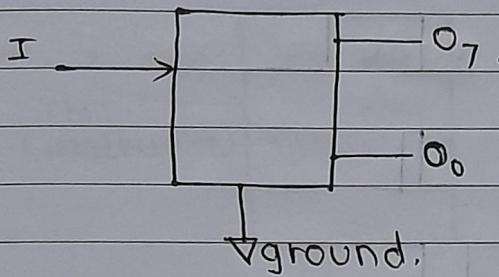
- Power on Reset circuit \Rightarrow Resets the circuit and Micro processor starts program from Address 0, which is present in memory.

factory programmed.



$$y = S_1 \oplus S_2$$

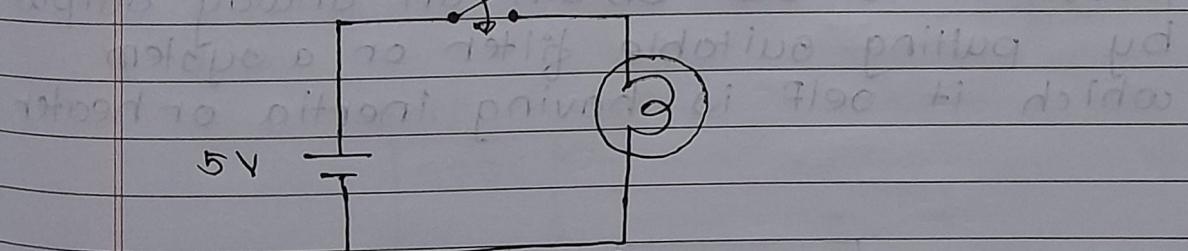
- Arduino Board Pins \Rightarrow
Digital I/P Pin \rightarrow user controls & microcontroller Read
- Digital O/P Pin \rightarrow Microcontroller controls it.
- Analog to Digital converter (ADC) \Rightarrow



Input into Range of 0 to 5 V is converted into output 00 - FF (80 H).

$$\frac{5}{255} \Rightarrow 1 \text{ UNIT.}$$

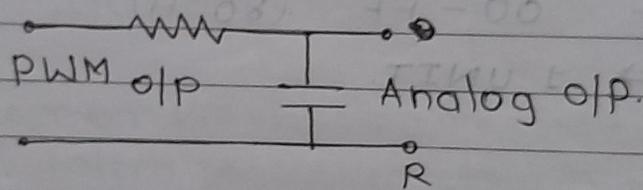
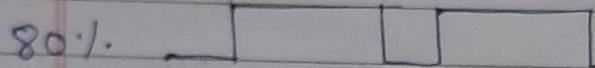
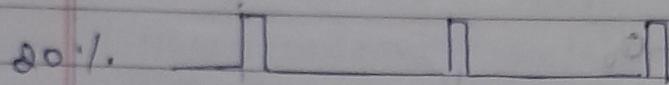
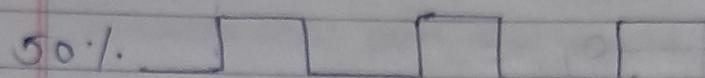
- Analog o/p pin is not available, but output is generated in another form i.e. pulse width modulated format (PWM).



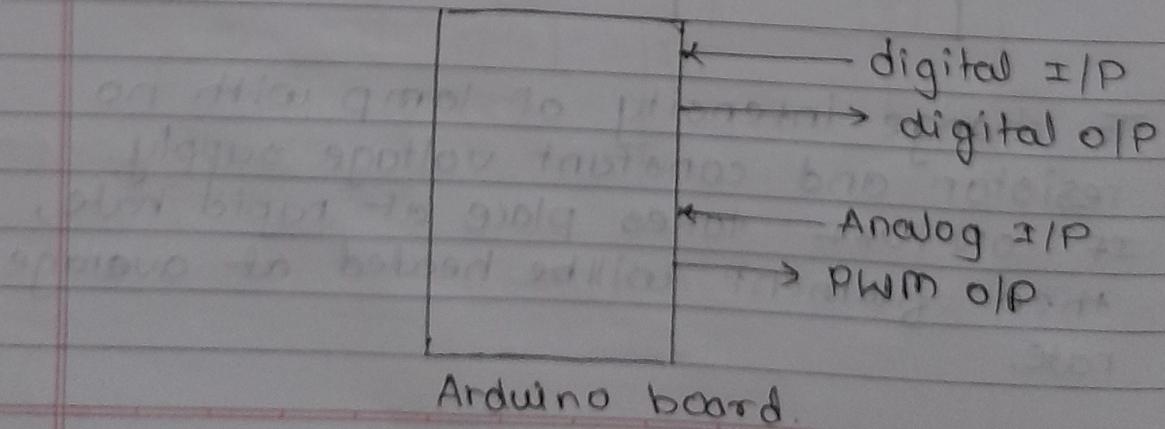
- To change intensity of lamp with no resistor and constant voltage supply.
If ON/OFF takes place at rapid rate, then filament will be heated at average rate.

- This is called pulse width modulation o/p generates a square wave where Duty cycle can be from 0% to 100%.

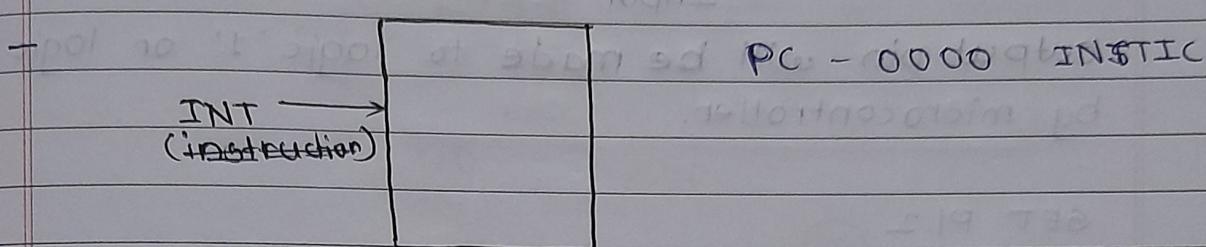
Square wave \Rightarrow no loss of power -



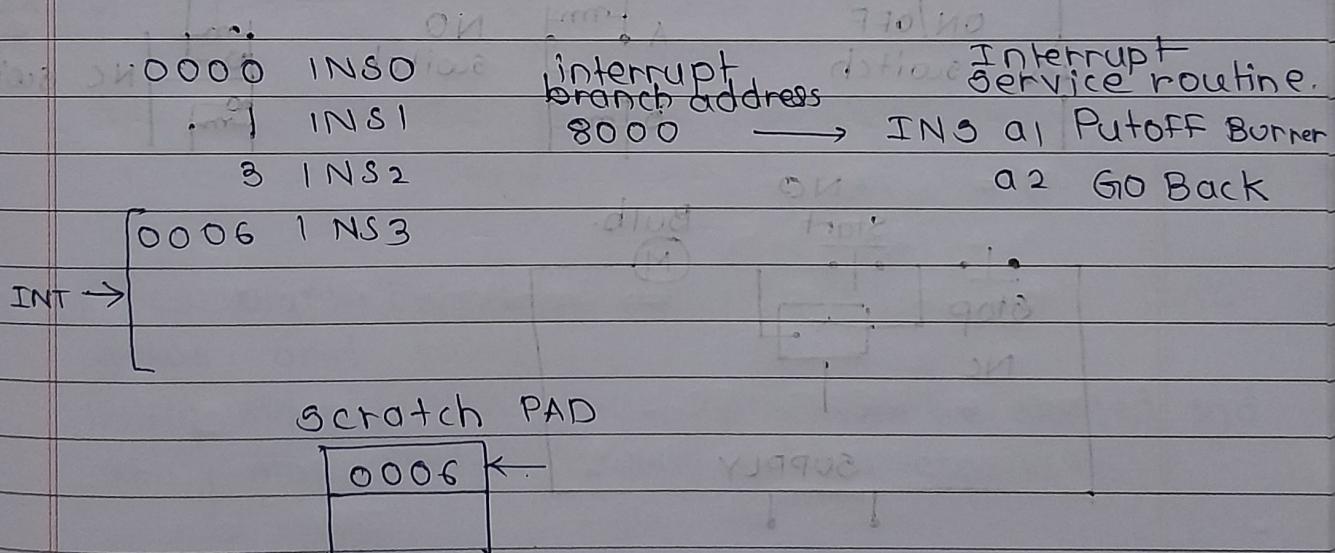
- PWM output is a digital output pin in which by controlling its duty cycle it is possible to obtain an analog output by putting suitable filter or a system which it self is having inertia or heater



- Interrupt Inputs \Rightarrow A program on arduino board consist of sequence of instruction, which are executed sequentially and then goes back to starting step.



- Interrupt is an input pin which is checked after instruction, automatically.
 - It doesn't consume anytime.



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• Arduino Programming

General purpose Digital I/O pins.

I/O pin → switch ↔ Manual switch

I/O pin → Read P0.0
Input P1.1.

Output pin will be made to logic '1' or logic '0'
by microcontroller.

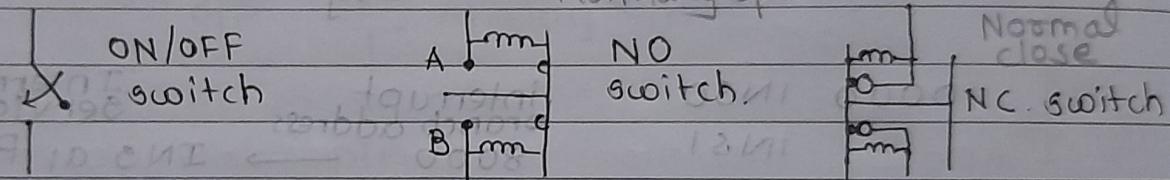
SET P1.2

RESET P2.1

WRITE(P1.3, FLAG)

OUTPUT(P3.2, 0)

Normally open switch



NO

Start

Bulb.

(M)

Stop

Nc

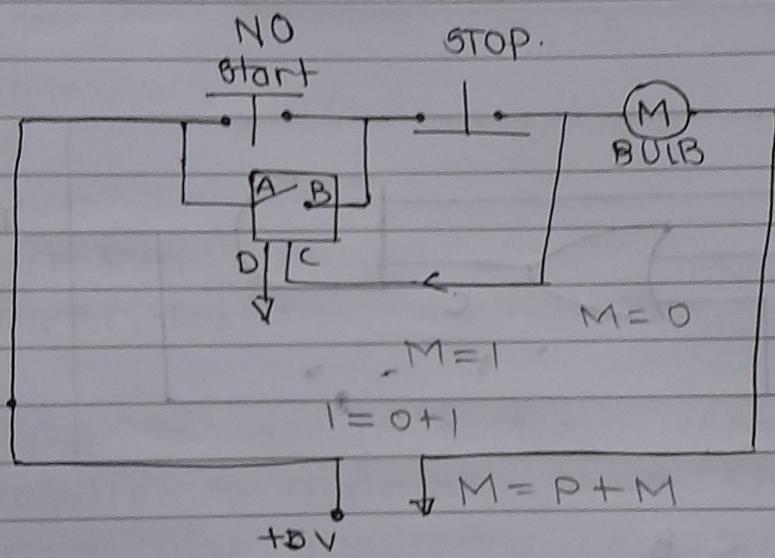
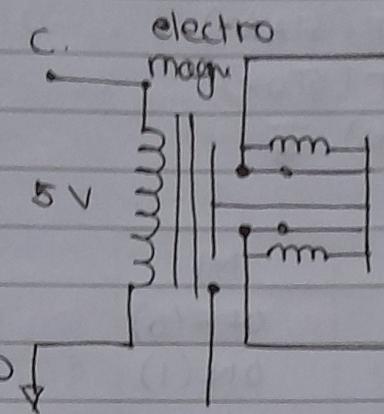
SUPPLY

1) Press START

MOTOR START, and continues to run even if the Start button is released.

2) Press STOP

Motor stops and even if you release stop button, motor doesn't stop.



When start button is pushed, motor starts, bulb glows and relay

using start button, bulb motor can be started and using stop button motor can be stopped.

READ START

READ STOP

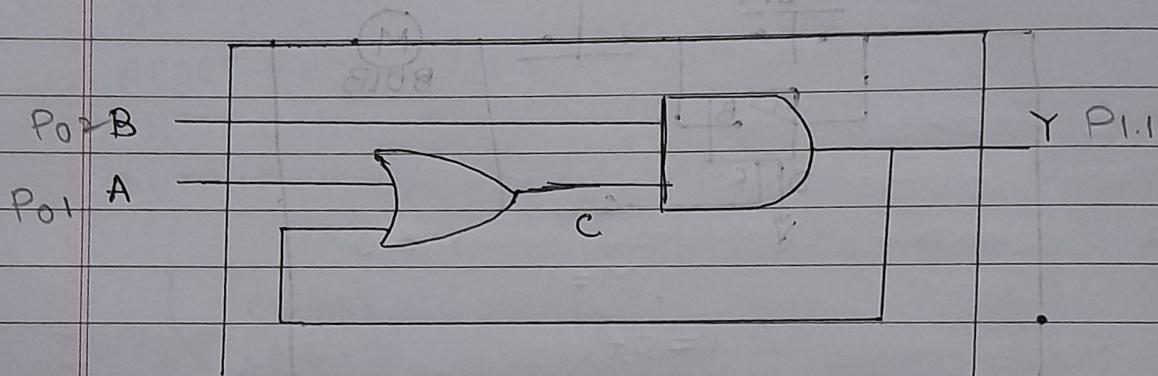
$$M = (P+M) \cdot \bar{Q}$$

label $P = \text{INPUT(START)}$

$Q = \text{INPUT(STOP)}$

$M = (P+M) \cdot \bar{Q}$

STOP	START	M
1	X	OFF(0)
0	1	ON(1)
0	0	0/1



$$Y = C \cdot B$$

$$Y = (A+Y) \cdot B$$

$B_0, B_1, B_2 \Rightarrow \text{internal Registers}$

A B Y

$B_0 = \text{READ } P0.1 \text{ AND } B_2, B_1$

BEGIN: MOV B0, P0.1 MOV P11, B2

MOV B1, P0.2

JMP BEGIN

OR B0, B2

Microcontroller instructions are either arithmetic logic instruction or input/output operation instruction.

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- Analog I/P on, PWM o/P.

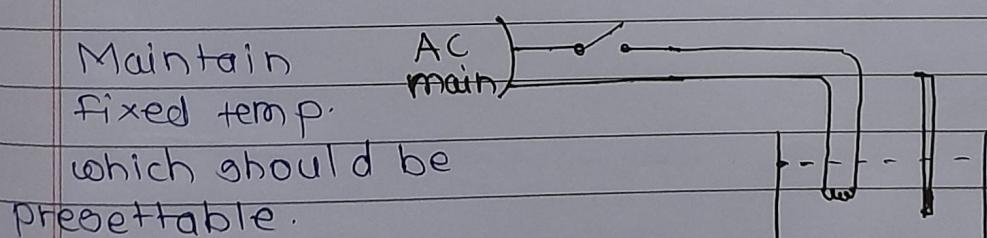
Analog I/P is there are ^{in built} analog to digital converters and it is possible to read that pin.

Analog		8 Bits
5V	→ 2550	P2.0 (00)
51	→ 255	P2.1
		P2.3
<u>5000 mV = 20 mV</u>		:
250		P2.7 FF

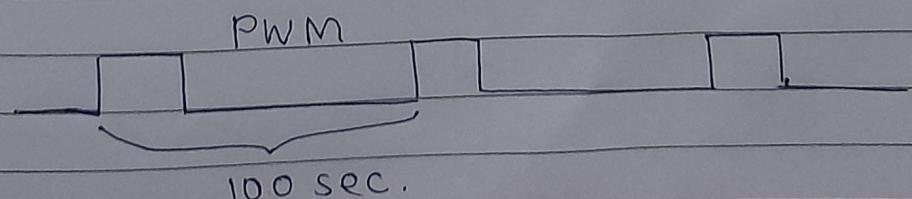
INPUT P2.0

MOV (R1, P2.0)

- Suppose to design a temp controller system. It requires to maintain some fixed temp for particular application. e.g water heating



8 switches
00 - FF



(S.P)

User sets Setpoint → Read by Arduino
System changes Process (p.v) → Read by Arduino.

SWITCH

O/P pin controlled by Arduino.

(READ)

(READ) 0.59

8.99

77 5.9

ACI TUNISI

(559, 19) ROM

