Number Systems

This document covers

[Positional Number Systems](#_Positional_Number_Systems)

[Computer Represention of Numbers](#_Computer_Representation_of)

## Positional Number Systems

### Overview

A positional number system represents any real number as a polynomial in the base of the number system.

When writing polynomial representations of numbers we use a radix point to separate the whole and fractional parts. We can then drop the powers of the base as the exponent is implicit in the position of the digit. If a particular power has no value we still need to mark it with a co-efficient of zero. Our form becomes.

The following are some examples

If we drop the fractional part of the representation we restrict ourselves to the set of integers . In programming languages these are known as the ‘signed integer’ as they can be both positive and negative. If we restrict ourselves to only the positive whole numbers we have what programming languages call the ‘unsigned integers’

Note:

Mathematicials usually assume the natural numbers exclude zero. We use the standard computer science convention that the set includes zero.

#### Normalized Scientific notation

Real numbers from the set form the basis of most scientific calculations. Any real number can be written in normalized scientific form.

The mantissa is a real number whose value is greater than or equal to 1.0 and less that The exponent is an integer. An example of a number in this form is

Given an infinite number of decimal places in the fractional part of the mantissa any decimal number can be represented in this general form.

#### Restricting the representation size

Often we do not have the luxury of an infinite number of decimal places and hence it is common to use a more restricted representation. In full generality, if we use a baseand have digits of precision in the mantissa we can **represent** a real number using the representation.

This is the same as the following.

W also need to restrict the values of the exponent in such that

Consider the case where we use the familiar base with a mantissa of precision . Furthermore, we will use two signed decimal digits in the exponent. Our representation becomes.

How many different numbers can this form represent? One key point of the standard form is that that the digit before the decimal point must be in the set where the digits can be zero. In our representation we can calculate the total number of different representable values as the product of

* 9 values of the integer part of the mantissa
* 10x10x10 values of the fractional part of the mantissa
* 10x10 values of the exponent
* 2 positive and negative values of the exponent
* 2 positive and negative values of the mantissa

This gives a total of different representable values. The largest value representable becomes and the smallest representable value becomes whereas the smallest non-zero positive value is.

If we use base with 4 digits for the mantissa and 2 digits for the exponent our finite normalized scientific representation becomes

The leading integer digit of the mantissa must be non-zero in normalized notation and such a binary digit is in the setthe only valid value it can take is 1. All the other digits in the mantissa and exponent can be either 0 or 1 giving us a total number of representable values as the product of

* values of the integer part of the mantissa
* values of the fractional part of the mantissa
* values of the exponent
* 2 positive and negative values of the exponent
* 2 positive and negative values of the mantissa

Giving a total number of representable values of

We note an important point here. We used 4 bits for the mantissa and 2 bits for the exponent, one bit for the sign of the mantissa and one bit for the sign of the exponent coming to a total of 8 bits. However the total number of representable values is only . This is because the leading integer digit of the mantissa has to be one. (remember in normalized scientific notation the integer digit must be greater than or equal to one and less than . If is 2 then only the integer digit 1 meets this criteria). We need one bit less in the representation. When we look at computer representation of floating point numbers later we will meet this again.

#### Properties of the finite representation

We now consider the important properties of a finite representation

The total number of different values representable is given be the expression.

Sign

Integral

Digits

Different Values

Exponent

Fractional

Digits

Properties of finite real number representations

* Possible different values
* Smallest non-zero positive value 
* Largest representable value
* Smallest representable value
* Difference between nearest 2 values

Example 1 base

* Smallest non-zero positive value
* Largest representable value
* Smallest representable value
* Difference between nearest 2 values

Example2 base

* Smallest non-zero positive value
* Largestr representable value
* Smallest representable value
* Difference between nearest 2 values

-3.0, -2.0,-1.5,-1.0,-0.5, 0.5, 1.0, 1.5, 2.0, 3.0

The distance between two nearest values in our representation depends on the particular value of the exponent and the smallest non-zero positive value. In our particular case in this example if the exponent is then our number line becomes

1.125

1.250

1.375

1.500

1.625

1.75

1.875

And the distance between the two nearest values is

But if we increase the exponent to our number line becomes

1.125

2.5

1.375

1.500

1.625

1.75

2.00

2.250

2.75

1.875

1.250

And the distance between the two nearest values becomes and in general for a given expone and a given number of binary digits p in the mantissa the distance between the nearest two points in our representation is

In our general form the distance between the two nearest numbers where the representation has base and p digits in the mantissa becomes

### Representation error

#### Absolute error

Most real numbers can only be approximated by a finite representation. As such we need to be able to measure the error in approximation. If we are approximating some real number x with a floating point representation float(x) the absolute error in the approximation is given by.

Absolute error = float(x) – x

#### Relative error

One problem with absolute error is that it does not take into account the scale of the number being approximated. Relative error includes the magnitude of the value we are approximating.

In the previous section we showed that the distance between the nearest representable values in a representation with p digits in the mantissa is for a particular value of the exponent e.

#### Units of the last place (ulps)

Often we are interested in the absolute error in terms of the precision of the mantissa, ignoring the exponent part. We often talk of the error in “units of the last place” which mean the error in units of the last place of the mantissa. So if our mantissa has 3 decimal digits in the fractional part then if our floating point representation of 5.413298 is given by 5.413 then the error in units of the last place would be .298. The unit of the last place itself has value

In our general form where we approximate a number x by a floating point number float(x) with base and p digits the error in units in the last place becomes

Of course given a number in units of the last place we simply multiply by the exponent term to get the absolute error

If we have procedure that guarantees that the floating point number chosen to approximate our real number x is the closest floating point number then the error in terms of “units of the last place” can be at most ½ times the value of the unit of the last place

#### From units of the last place to relative error

For any chosen value of e a number of the form can vary in value from all the way to As such for any chosen value of e, where the error in terms of ulps is fixed the relative error will vary from up to If we have chosen the nearest floating point value to the real value then we can say that the error measured in ulps is 0.5 then our relative error will vary from up to

Put another way. For a fixed value of error in ulps the relative error can vary by a factor of due to the fact that the mantissa can vary from 1.0 up to just under .

We now consider a numerical example to cement these formulas. Consider the special case where we use a base of 10 and mantissa of 4 digits. Assuming we always choose the correct nearest floating point number then the error in ulps will be 0.5. In our case the last place has value . Our chosen value of e is 3 so from our ulp error to absolute error we multiply by giving us an absolute error of 0.5 units. If we consider the value our relative error becomes On the other hand if we consider the value and our relative error becomes Both of these confirm what we expect. A fixed absolute ulp for a given exponent e gives a relative error that varies by a factor of B depending on the value of the mantissa

### Converting between bases

Give a number N in base we want to convert it to a new base . That is to say given

We want to find the coefficients  and such that



When doing the conversion we consider the intergral and fractional part separately.

#### Integral part

Looking first at the integral part we have a number N

We want to convert it to base such that

We can rewrite this as

If we divide it by then the remainder is clearly and the quotient is

If we repeat this until the quotient is zero we can read off the value of to giving us the required number in the new base

Let us consider the scenario where we want to convert the decimal number 2748 to hexadecimal. We first divide our decimal number by the new base 16

2748

16

171

1600

112

1148

28

16

12

So after this first division we know that

1. 

We can’t represent 171 as we only have sixteen symbols so we to divide 171 by 16

171

16

10

160

11

So now we know that

1. 

Inserting ii) into i) we get

1. 

Which we know is a positional number 

Similarly we can do the same for base 2

#### Fractional part

Consider the situation where we have a fraction part in some base and we want to find the digits in the representation

We first note that

So if we take our fractional part and multiply it by then the resulting integral component is the we can similarly repeate the process to find the digits

Example 1 Convert  to base 8

**i)** 









## Computer Representation of numbers

### Unsigned integers – the whole numbers

#### Introduction

Consider our positional number system

If we only need to represent whole numbers, that is to say unsigned integers we can use an n-bit binary representation. We don’t need any bits to represent fractions.

Such a representation can distinguish between different values. It is often useful to visualize the representation as a circle mod . In the special case where n = 3 we get.

000

100

001

010

011

1100

101

1110

If we use our different values to represent positive integers in the range we get the following.

#### Unsigned binary integer addition

001

000

100

010

011

1100

101

1110

1

2

3

0

7

6

5

4

In this representation addition is simply moving clockwise around the circle. It can easily be achieved naturally using binary addition in hardware. We can easily simulate unsigned addition in code via the bitwise shift and logic operators.

101 (5)

011 (3)

010 (2)

C++ Code to perform binary addition on unsigned integers

short binaryAdd( short x, short y )

{

short result = 0.0;

short carry = 0.0;

int numberOfBits = sizeof(x) \* 8.0;

for ( int bitNumber = 0; bitNumber < numberOfBits; bitNumber++ )

{

// We deal with one bit at at time. By right shifting

// x and y bitNumber times we can set the bit we

// want into the least significant bit

// of the twos complement representation.

short shiftedX = x >> bitNumber;

short shiftedY = y >> bitNumber;

// Now we make use of the fact that the number 1 in

// twos complement has (numberOfbits -1 ) zeros

// followed by a solitaty 1 in the least significant

// digit. We can then take our shifted values and bitwise

// and them with 1 to make sure the only digit in the

// shifted number is the one we want to deal with

short xDigit = shiftedX & 1;

short yDigit = shiftedY & 1;

// We have three values that feed into the current digit

// {the x digit, the y digit, the carry digit}. If

// one or all three of these are zero then the

// digit value will be one, otherwise it will be zero

short digitValue = ( xDigit ^ yDigit ) ^ carry;

// We now shift the digit back into its correct

// location and add it to the result we are building up

result |= ( digitValue << bitNumber );

// Finally calculate the carry for the next round

carry = ( xDigit & yDigit ) | ( xDigit & carry ) | ( yDigit & carry );

}

return result ;

}

#### Unsigned binary integer subtractions

We have quite easily implemented code to perform addition of unsigned integers using bit operators. We now consider subtraction of unsigned integers. Unsigned integer subtraction makes use of some interesting properties of our binary number system.

1. in an n bit unsigned representation

Essentially we are just moving one complete revolution back to the same number.



100

001

010

011

1100

101

1110

1

2

0

7

6

5

4

3

The second result we need is that in modular arithmetic ( see textbox for proof)

* 1. From property 1)

* 1. Rearranging i)

* 1. Subst rhs ii) into rhs of i)

* 1. Basic arithmetic
  2. Sub iv)into rhs of iii)
  3. Take 1 outside the brackets

The final clever step to performing subtraction of unsigned integers is to note that in an n bit unsigned representation where the bitwise complement operator is ~.

**0**

**1**

**1**

**1**

**0**

**0**

**1**

**1**

**1**

**w**

**~w**

**~w + w**



**Proof**

We can then substitute this into 2)

This is very powerful. It means that if we couple the code we already wrote to do unsigned addition with the bitwise complement operator we can then do unsigned subtraction as well.

Figure 1 Addition of negative unsigned integers

short binaryNegate( short x )

{

short neg = binaryAdd(~x, 1);

return neg;

}

short binarySubtract( short x, short y )

{

short minusY = binaryNegate(y);

return binaryAdd( x, minusY);

}

### Signed Integers

#### Twos complement

Twos complement is a way of encoding negative numbers into ordinary binary such that addition still works. In the previous section we discussed how a polynomial can be be represented as

Specifically letting R be 2 we can represent an N bit binary number as

Using summation notation we have

So a three bit number is this representation would equal 7

In a twos complement signed representation we change the most significant digits weighting to giving us

So in a three bit 2’s complement notation the number would equal and indeed for any value n where all the coefficients are set to 1 the value is equal to -1.

000

100

001

010

011

1100

101

1110

1

2

3

0

-1

-2

-3

-4

**0**

**1**

**1**

**0**

**0**

**1**

**0**

**1**

**0**

**0**

**0**

**0**

**3**

**2**

**1**

**0**

**1**

**1**

**1**

**1**

**0**

**1**

**1**

**1**

**0**

**1**

**0**

**0**

**-1**

**-2**

**-3**

**-4**



Remember in the previous section on unsigned integers we saw that the maximum value than can be represented in an n-bit unsigned integer is .We also proved that subtracting a value from the binary representation with the value one is every bit is the same as flipping the bits

In our twos complement notation the binary value with a one in every bit is no longer but instead is -1. Our equation then becomes

And so

So given a positive twos complement number we need to flip it bits and add one.

#### Why twos complement is powerful

The most powerful aspect of the two complement notation is that we can add positive and negative numbers. If we have n bits we can represent values and hence due to overflow if we move points around our modular system we get back to the same number.

The algorithm to multiply a twos complement number by -1 is to flip all its bits using the logical negation operator ~ and then add one. This is beautiful because we can use the same bitwise addition to perform addition and subtraction. To do subtraction just form the ones complement and then do normal addition

Figure 2 Binary negation

short binaryNegate( short x )

{

short neg = binaryAdd(~x, 1);

return neg;

}

#### Summary

* The most significant bit represents the sign
* Negating a value requires switching all its bits and then adding one
* 1 is represented by 001 and -1 is represented by 111
* N-bit implementation can represent numbers from to

### Floating Point representations

“The exact meaning of single-, double-, and extended-precision is implementation-defined. Choosing the right precision for a problem where the choice matters requires significant understanding of floating point computation. If you don’t have that understanding, get advice, take the time to learn, or use double and hope for the best”

Bjarne Stroustrup – The C++ Programming Language

#### Introduction

In the above section we saw that we can represent any non-zero real number using the notation

Internally real numbers are stored in binary representation, i.e. our base is 2.

The key aspects of this representation are

1. The number of digits in the mantissa p
2. The max and minimum exponents and
3. The base

All floating point numbers are rational numbers which means they have a terminating expansion in the relevant base. As such most real numbers cannot be expressed exactly. Any number with an infinite expansion cannot be represented.

Also a number which has a finite expansion in one base can have non-finite expansion in another base. If the base is 2, as in binary floating point only **rational** numbers whose denominators are powers of 2 can be represented.

Converting a base 10 fraction such as 0.1 to binary floating point will result in an infinite expansion which can only be approximated with a finite number of digits. The following section discusses how to measure the error in any approximation

#### Single precision IEE

If we consider single precision number the valid values the 32 bits are allocated as

* 1 bit represents the sign of the number
* 23 bits for the fractional part of the mantissa
* 8 bit signed number for the exponent

The storage however is a little peculiar. We might expect that using 8 bits for the exponent would allow use to have 256 different values. However four values are reserved for special values such as plus and minus zero and plus and minus infinity.

The representation is where (0 and 255 are used for special values) and The largest possible value representable is hence . The smallest positive number becomes

The binary machine number is the machine epsilon and is hence the smallest positive value such that. Because we can infer that single precision floating point has accuracy to six significant decimal figures.

So the mantissa can represent from 1 to in increments of which in decimal in approximately from 1 to in increments of so since any single precision mantissa representation can be up to from the real number. As such the precision of the mantissa is 6 significant figures.

Questions

What is the precision of a single precision point floating point number and why?

Six significant figures

The binary machine number is the machine epsilon and is hence the smallest positive value such that. Because which if we write it out we see

If we see this value what it really means is that the value is

So only the sixth significant figure is accurate.

What is the range of a single precision floating point and why?

From to which is approximately from to

The reason being that the largest absolute value representable in single precision is given by as the mantissa has 23 bits and the exponent has 8 bits.

What is the precision of a double precision point floating point number and why?

The binary machine number is the machine epsilon and is hence the smallest positive value such that. Because so only to the 15 significant figure is correct.

Given an operator that does addition how can one add subtract one unsigned integer from the other without using the subtraction operator?

x – b = x + ~b + 1

Why does this work?

If we use n bits to represent unsigned integers addition is modulo  giving a maximum value of

But consists of n ones 1111…11 Subtracting from n ones is equivalent to flipping the bits because 1 – 0 = 1 and 1 -1 = 0. So we can replace Substituting back in we get

What is the result of the following C code and why?

int fraction = 5 / 9 ;

The result is that the value 0 is assigned to fraction because in C integer division truncates.