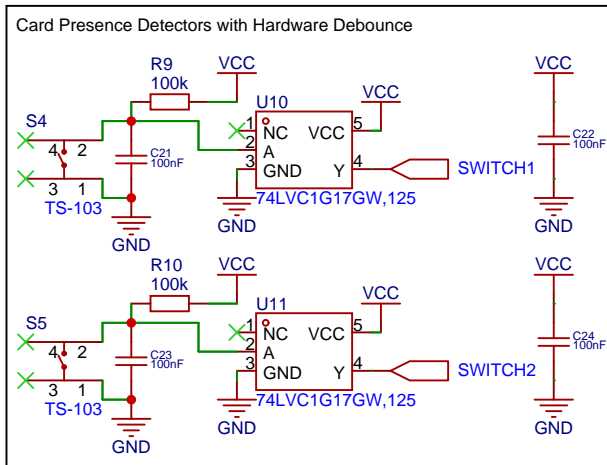
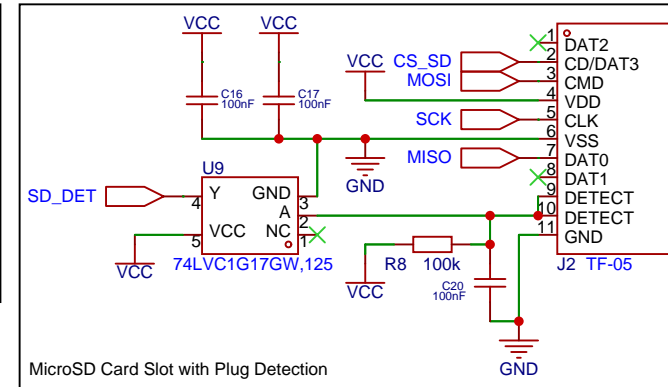
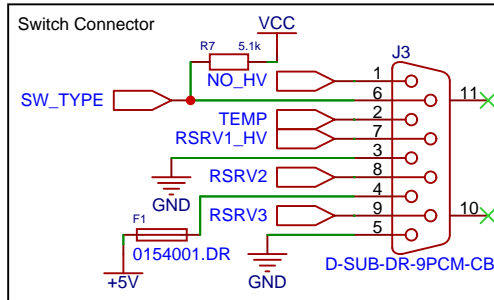
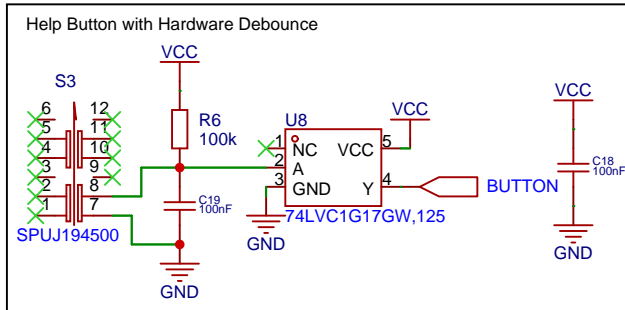
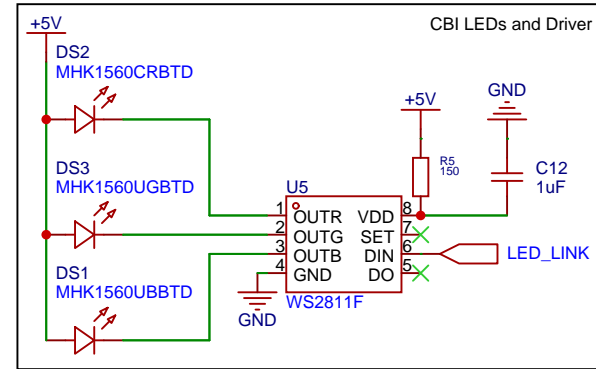
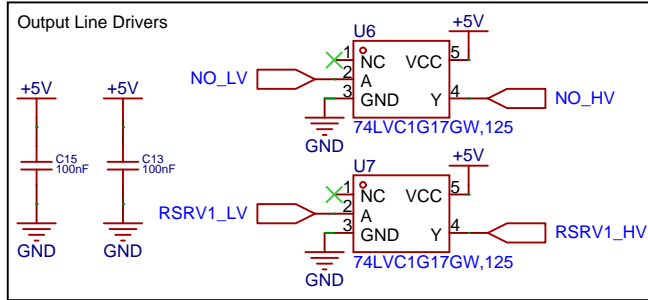
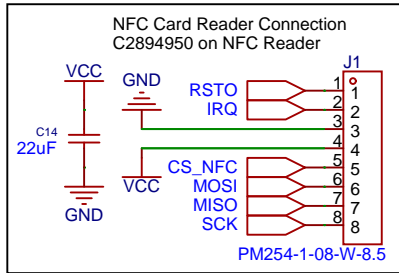
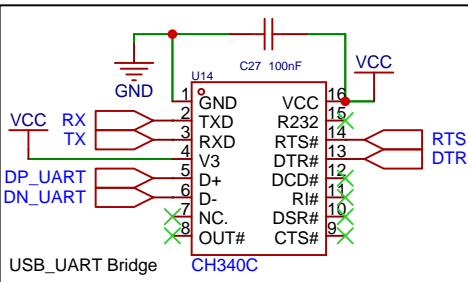
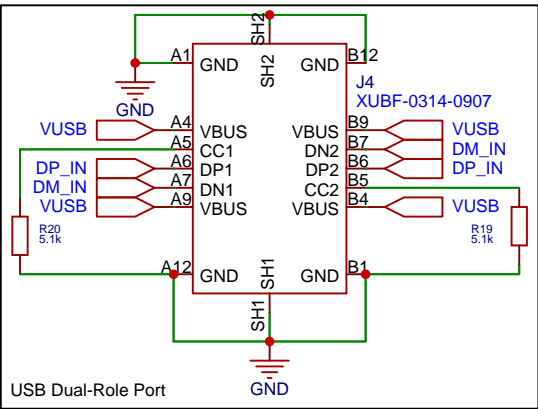
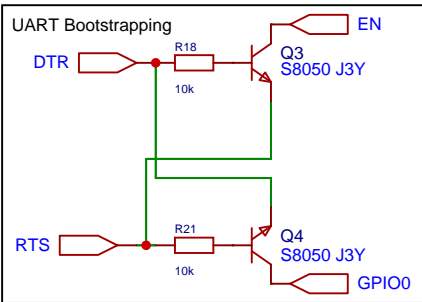
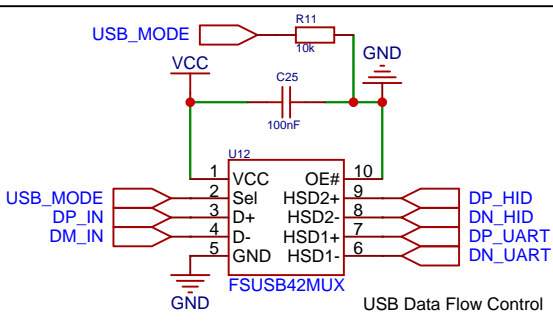
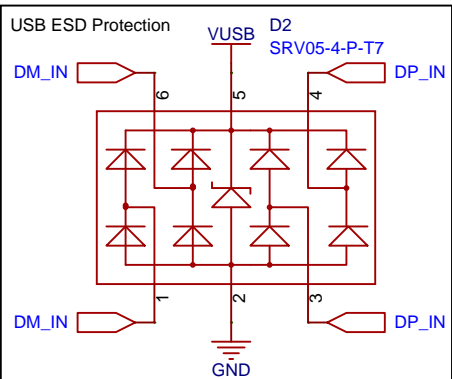
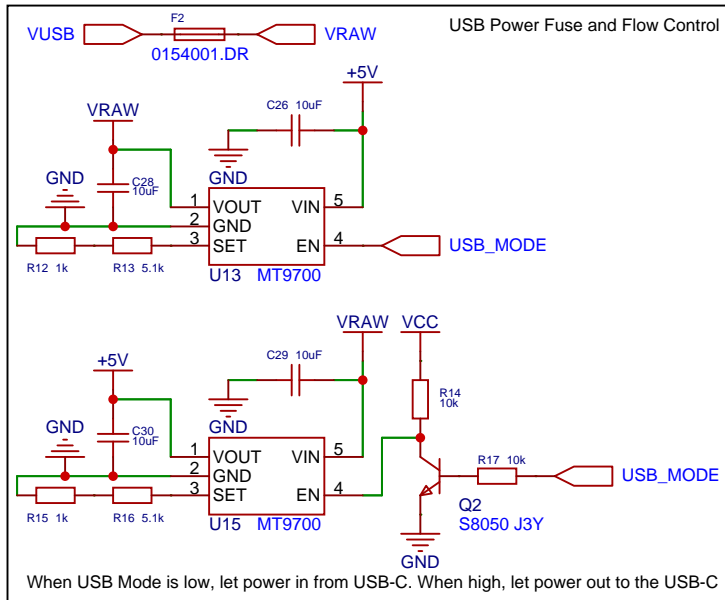


Schematic	V2.1.1 Core Schematic			Update Date	2024-05-22
				Create Date	2024-05-16
Page	Microcontroller.V2.1.V2.1.1 Core Schematic			Part Number	N/A
Drawn	Jim Heaney	Access Control V2.1			
Reviewed	Jim Heaney				
		VER	SIZE	PAGE	1 OF 3
RIT		V2.1.0	A4	RIT SHED MAKERSPACE	



Schematic	V2.1.1 Core Schematic		Update Date	2024-05-22
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Reviewed	Jim Heaney			
		VER	SIZE	PAGE 2 OF 3
RIT		V2.1.0	A4	RIT SHED MAKERSPACE



Schematic	V2.1.1 Core Schematic		Update Date	2024-05-22
			Create Date	2024-05-17
Page	USB.V2.1.V2.1.1 Core Schematic		Part Number	N/A
Drawn	Jim Heaney	Access Control V2.1		
Reviewed	Jim Heaney			
		VER	SIZE	PAGE 3 OF 3
RIT		V2.1.0	A4	RIT SHED MAKERSPACE