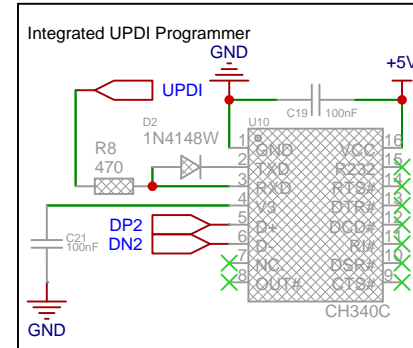
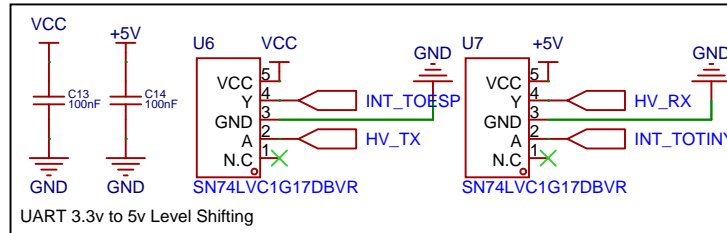
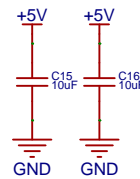
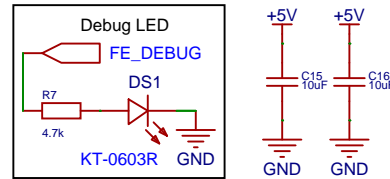
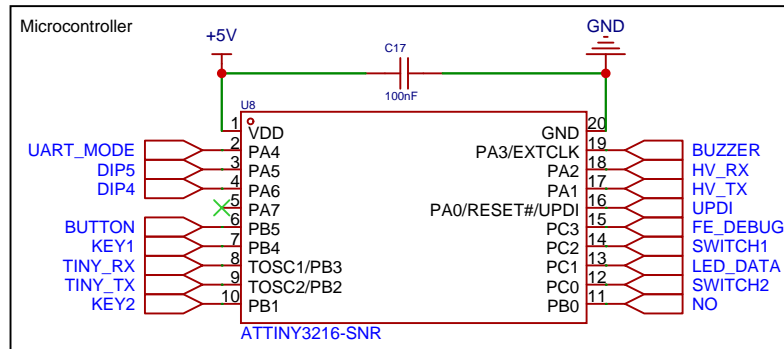


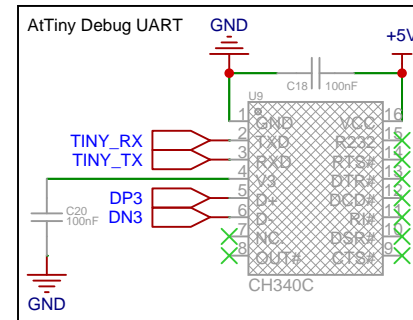
Schematic Page	Front End		Page Number	1
Source Link	https://github.com/rit-construct-makerspace/access-control-hardware		Total Pages	6
Last Updated	2025-06-26	<div>Access Control Core V2.3.2 Schematic</div> <div>Licensed Under CERN-OHL-S 2.0</div>		
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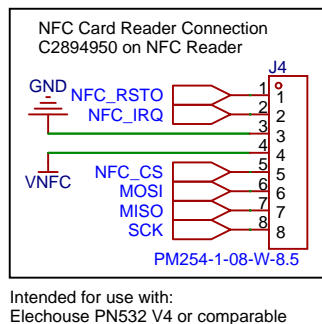
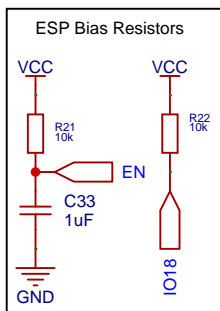
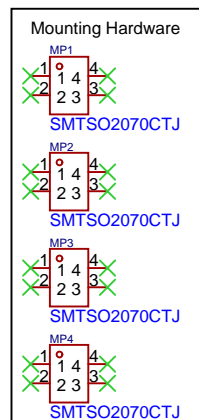
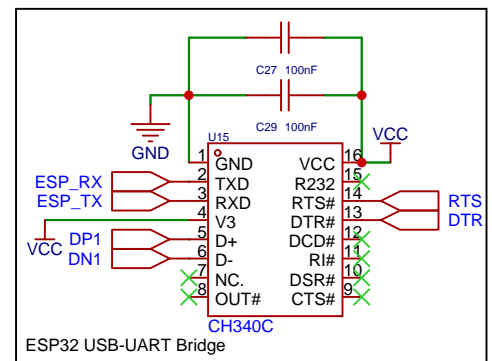
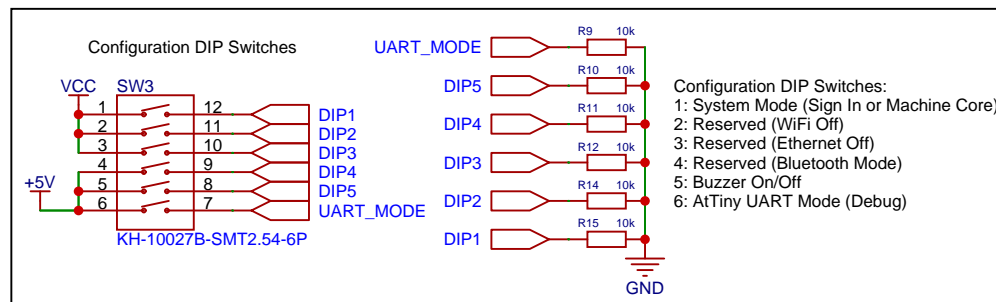
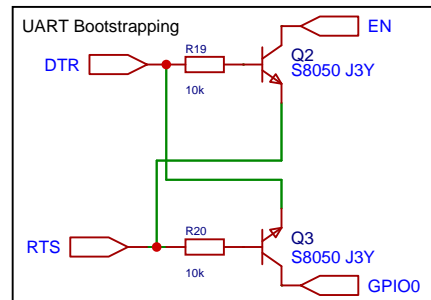
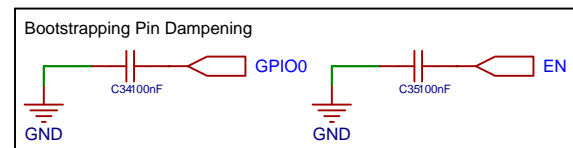
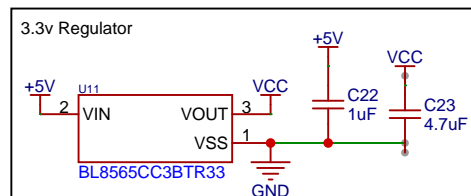
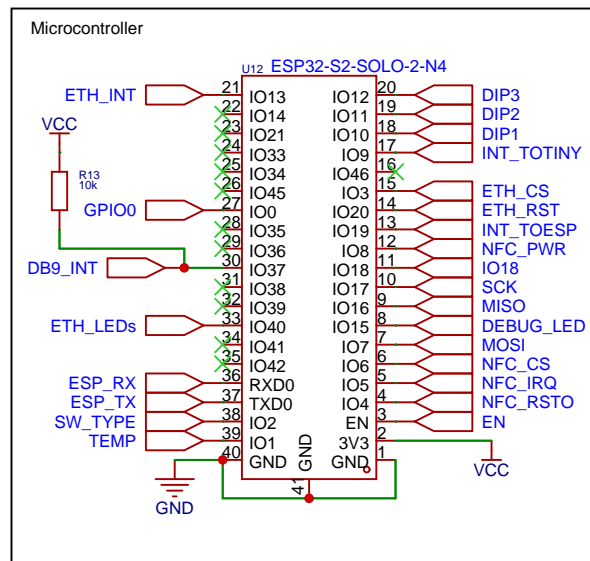


If UART_MODE is high, AtTiny re-assigns location of RX/TX to communicate with ESP32.
By default and/or with no special code running, UART goes to USB

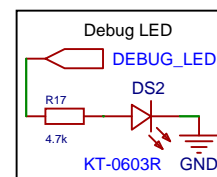
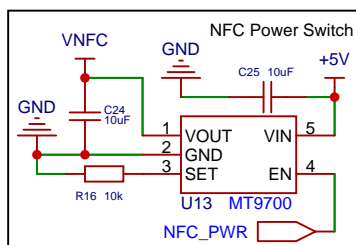
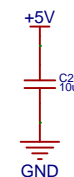
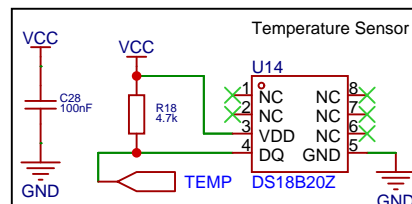
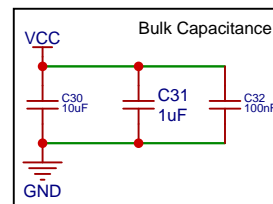


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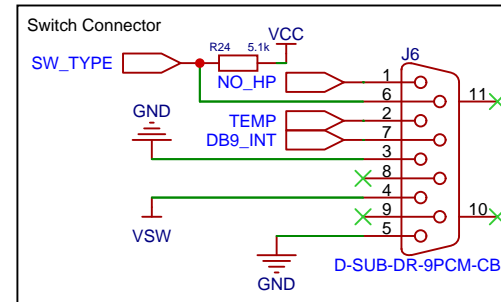
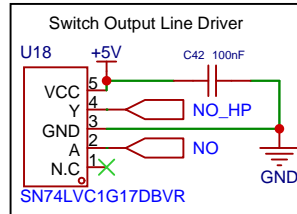
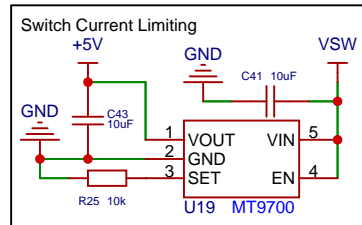
Schematic Page	Front End MCU		Page Number	2
Source Link	https://github.com/rit-construct-makerspace/access-control-hardware		Total Pages	6
Last Updated	2025-06-26	<div>Access Control Core V2.3.2 Schematic</div> <div>Licensed Under CERN-OHL-S 2.0</div>		
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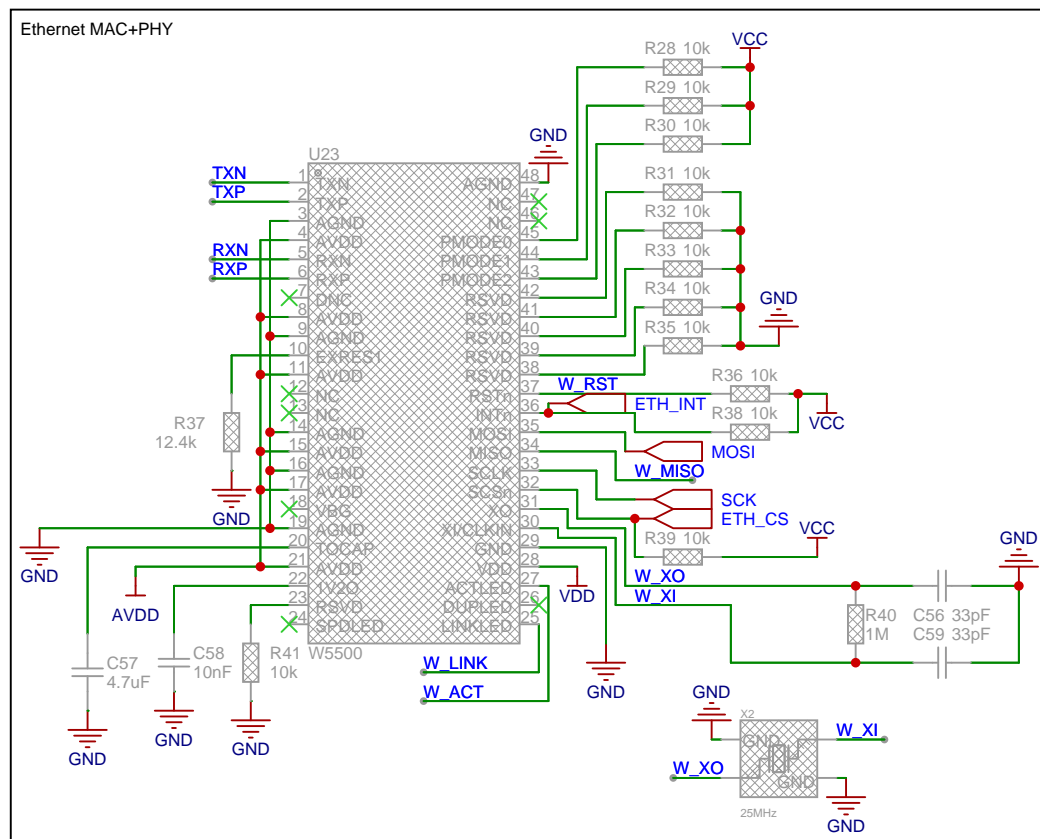
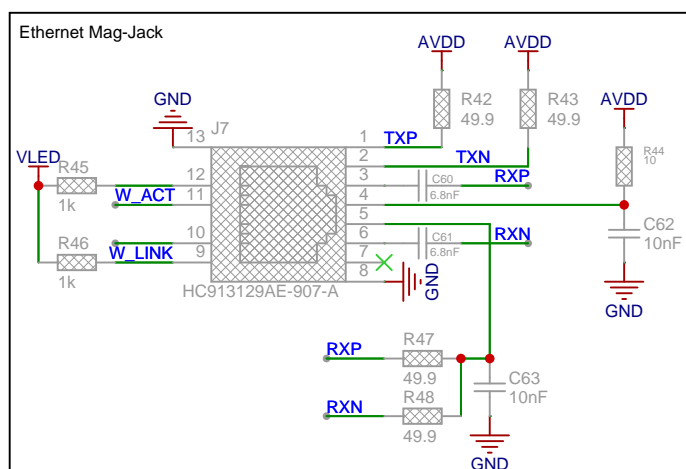
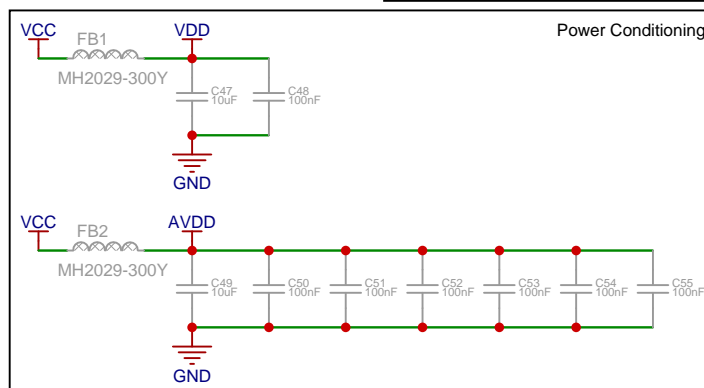
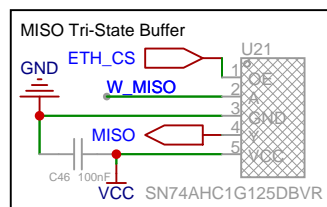
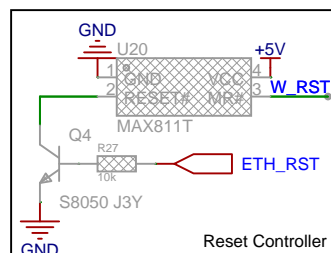
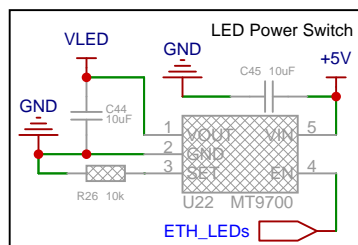
Intended for use with:
Elechouse PN532 V4 or comparable




Schematic Page	Network MCU		Page Number	3
Source Link	https://github.com/rit-construct-makerspace/access-control-hardware		Total Pages	6
Last Updated	2025-06-26	Access Control Core V2.3.2 Schematic		
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Schematic Page	Switch Interface		Page Number	5
Source Link	https://github.com/rit-construct-makerspace/access-control-hardware		Total Pages	6
Last Updated	2025-06-26	<div>Access Control Core V2.3.2 Schematic</div> <div>Licensed Under CERN-OHL-S 2.0</div>		
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Ethernet hardware is not present in the Lite edition ACS Core

Schematic Page	Ethernet		Page Number	6
Source Link	https://github.com/rit-construct-makerspace/access-control-hardware		Total Pages	6
Last Updated	2025-06-26	<div> <div>  <div> <div>SHED</div> <div>Access Control Core V2.3.2 Schematic</div> </div> </div> <div>Licensed Under CERN-OHL-S 2.0</div> </div>		