

**Formal Methods**



**Spectification (Interence)**

• **MMCAI'24) MIT Learning**

**•JOSCARLET(JOSS'24)**

• **(TACAS'22) LTL Learning**



# Verification

**`Ensure controller satisfies the  
goal'**



Design correct-by-construction

**Reactive Synthesis**

controlier,

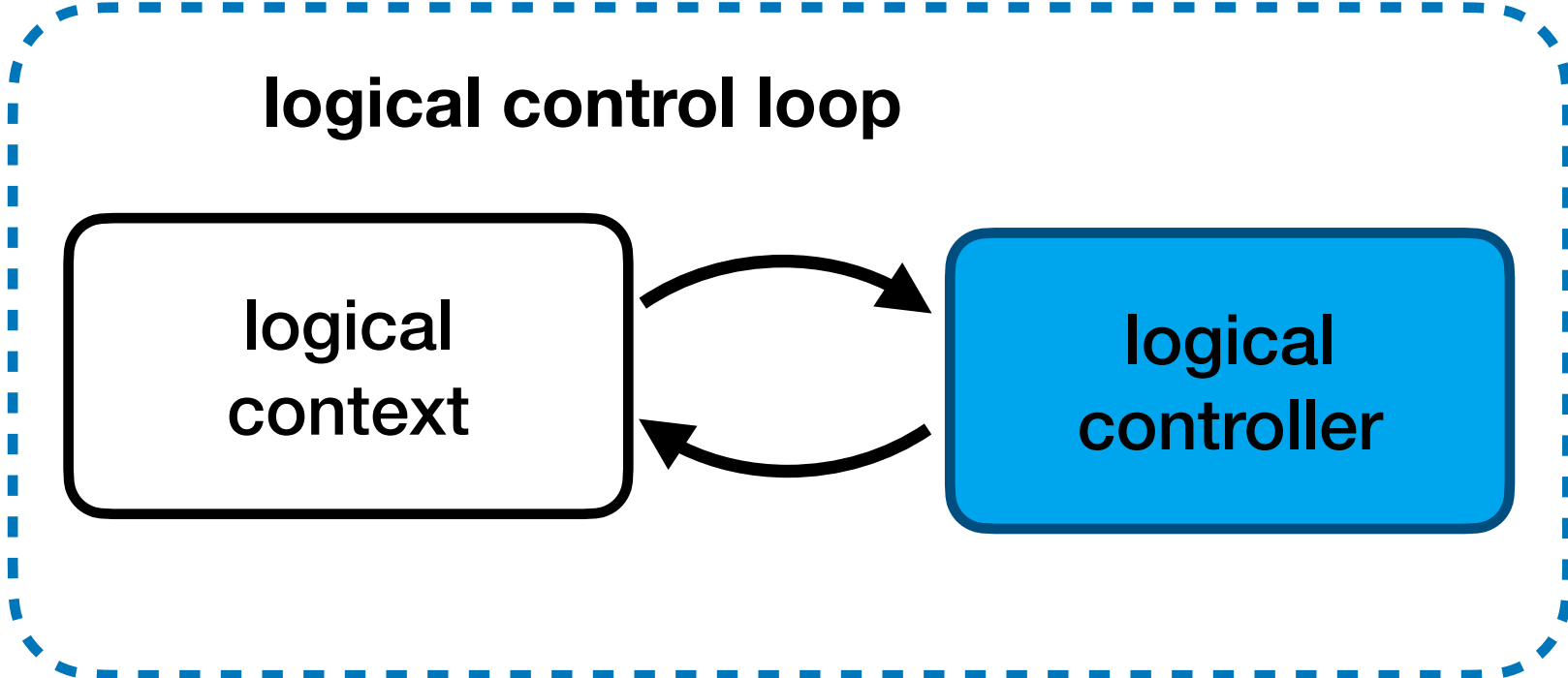
# Higher Logical Layer

logical control loop

logical  
context

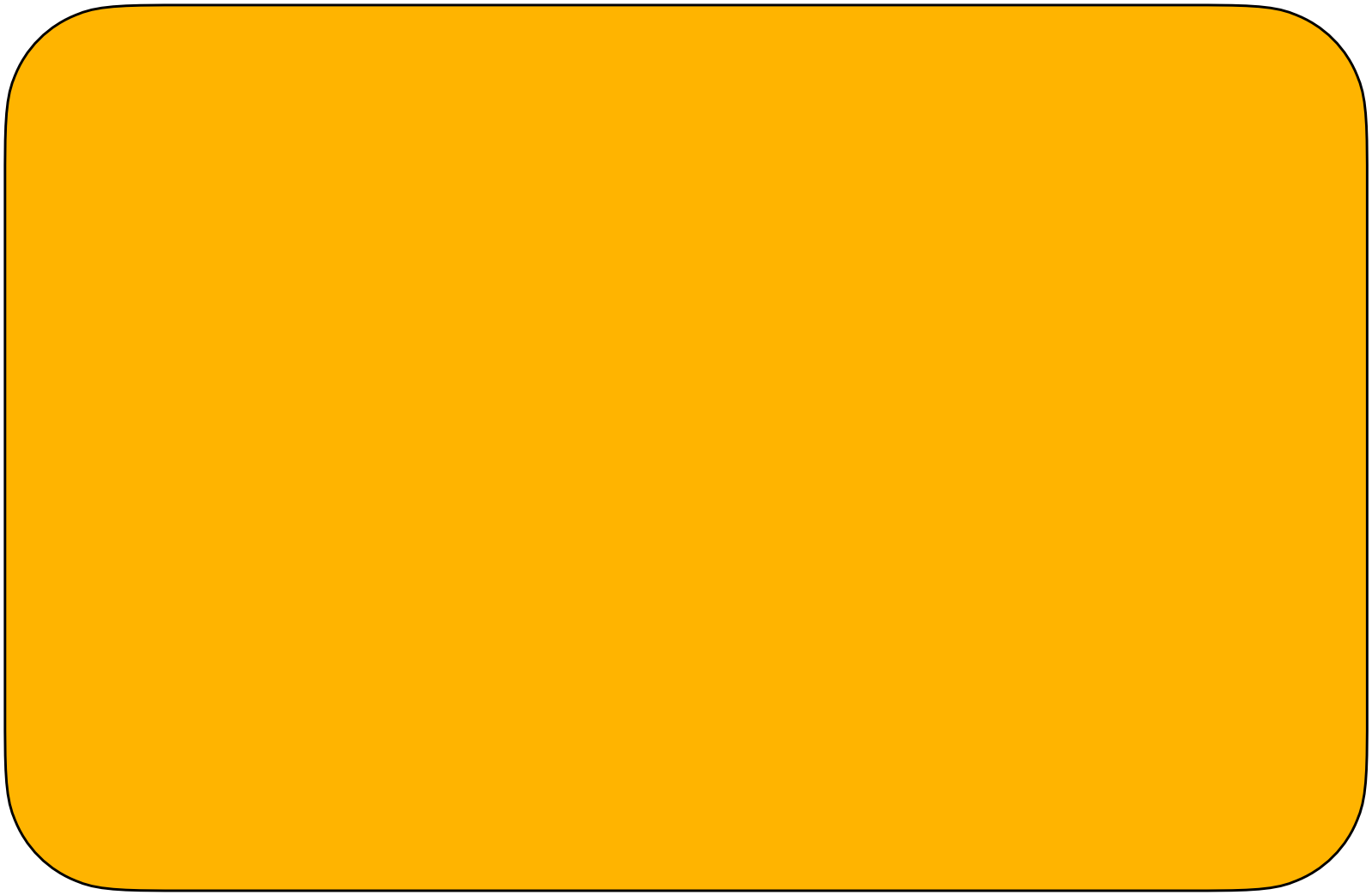
logical  
controller

$\models$  specification



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# Higher Logical Layer

logical control loop

logical  
context

logical  
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