

Reactive Synthesis

`Design correct-by-construction
controller'



• **(TACAS'22) LTLLearning**

• (VNCAL'24) MIT Learning

Spectification (Interence)

•JOSCARLET(JOSS'24)

Formal Methods



***verificati*on**

Ensures that the

goals;

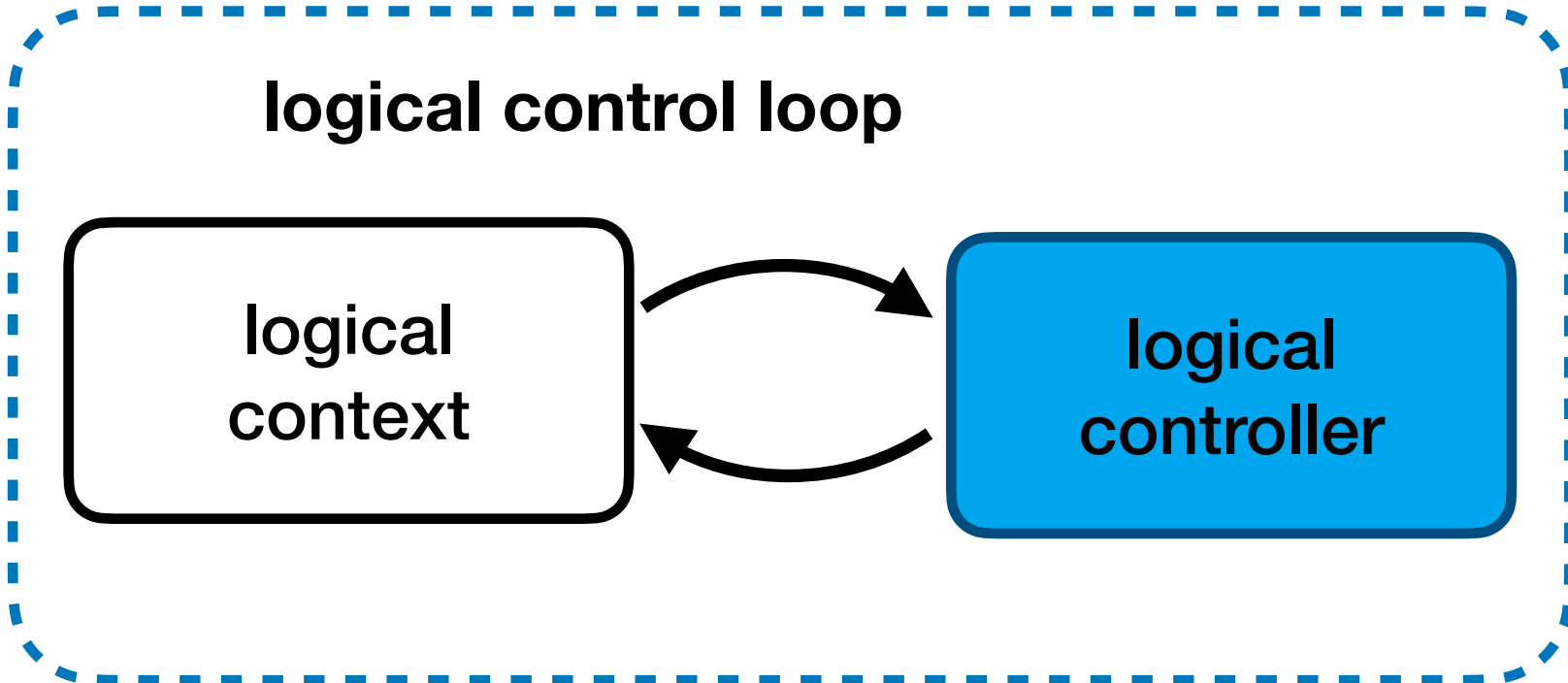
Higher Logical Layer

logical control loop

logical
context

logical
controller

\models specification





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