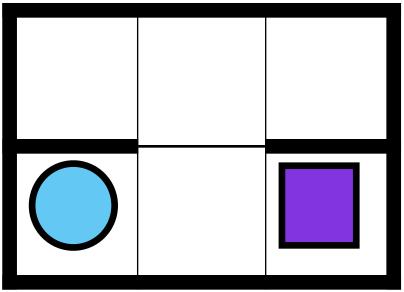
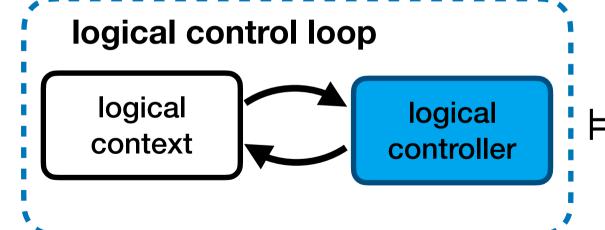
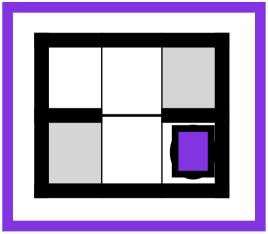
Example

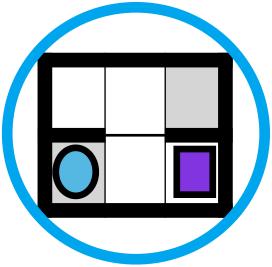


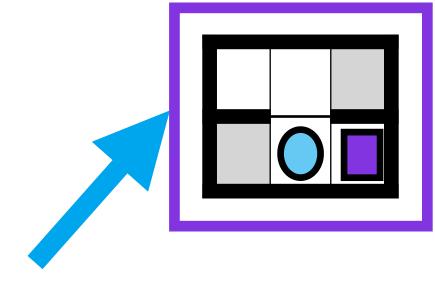
Higher Logical Layer

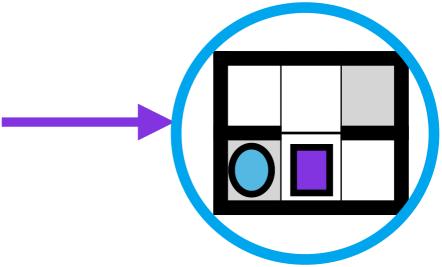


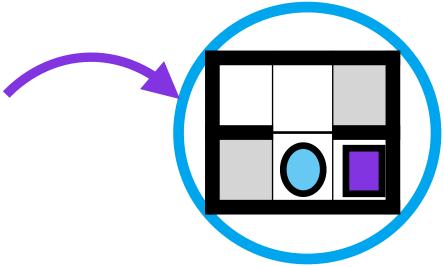
F specification





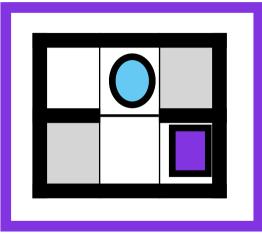




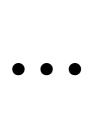


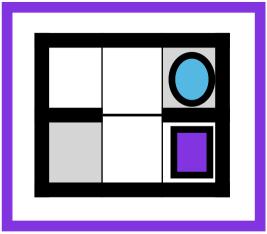


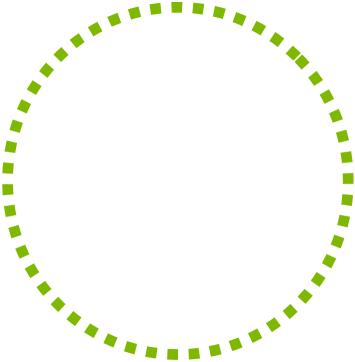


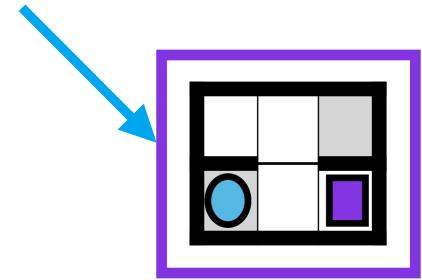




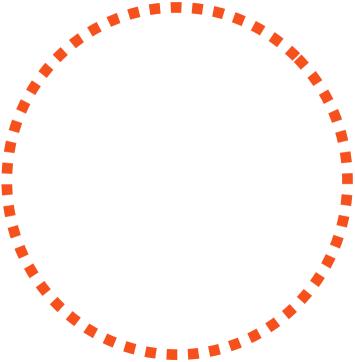
















disturbance

logical