

Ritchie Xia

Third Year Computer Engineering Student | University of British Columbia

+1 (604) 352-1531 | rxia@student.ubc.ca | ritchiexia.me | github.com/ritchiexia | Vancouver, BC, Canada

TECHNICAL SKILLS

Languages: Java, C, HTML, CSS, JavaScript (React), Python, OpenCV, SystemVerilog, Verilog, ARMv7, x86-64, PicoBlaze

Software Tools: Git, MATLAB, Intel Quartus, ModelSim, Node

PROJECTS

Find Free Space

nwHacks Project (<https://github.com/ritchiexia/FindFreeSpace>)

Jan 2021

- Created a web app using **Python** and **OpenCV** for upper body detection from a camera input and returns a count to a **MongoDB** database to inform users of a room's occupancy level
- Won Best Domain Registered with Domain.com award

Discord Bot

Personal Project (<https://github.com/ritchiexia/DiscordBot>)

May – Jun 2020

- Developed a Discord bot using **Discord.js** and **Node** for minor tasks and interactions with specific phrases
- Implemented 'poke' command allowing users to alert other inactive users via sending personal messages; other members can increase the number of messages sent by reacting to the original command message

ENGINEERING STUDENT TEAMS

UBC Rocket

Internals Subteam Member

Sep 2019 – Apr 2020

- Integrated core components of rocket into an efficient, functional design
- Used **SolidWorks** to design and perform force analyses on mid-assembly of rocket including main and drogue parachute bays, spoked plates, and threaded rods providing room for avionics board to be housed

OTHER WORK EXPERIENCE

Trek Bicycle Corporation

Sales Associate, Burnaby, BC

Jul – Oct 2020

- Provided a personal shopping experience by qualifying customer needs and finding ideal products

SFU Camps

General Instructor of Computers, Burnaby, BC

Jun – Aug 2019

- Supervised, formed lesson plans, and taught basics of computer programs such as **Scratch**, **HTML**, the Adobe suite (**Photoshop**, **After Effects**), and **SketchUp** to groups of about 25 campers aged 10-12

EDUCATION

University of British Columbia, BAsC Computer Engineering

Expected Graduation: April 2024

Relevant Courses: Operating Systems (**C**), Principles of Software (**Java**), Digital Systems Design (**SystemVerilog**)