

I2C Lab Final

The address 0x80 was chosen for the slave device. The following screenshot shows the address being detected. The screenshot also shows some data being written at offset 0x4 to the slave device and the same data is also read from the same offset.

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I2C slave detected at address: 0x38
I2C slave detected at address: 0x64
I2C slave detected at address: 0x72
I2C slave detected at address: 0x80

entry_point(): Entering main()
Starting RTOS

List of commands (use help <name> to get full help if you see ...):
    crash : Deliberately crashes the system to demonstrate how ...
    i2c    : i2c read 0xDD 0xRR <n>...
    tasklist : Outputs list of RTOS tasks, CPU and stack usage....
    uart3   : Send a string to UART3
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i2c write 0x80 0x04 1 2 3 4
Wrote 4 bytes to slave 0x80
[  4] = 0x01 (1)
[  5] = 0x02 (2)
[  6] = 0x03 (3)
[  7] = 0x04 (4)
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i2c read 0x81 0x04 4
I2C Read of Slave 0x81
0x04: 0x01 (1)
0x05: 0x02 (2)
0x06: 0x03 (3)
0x07: 0x04 (4)
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```