Time CLOCK instruction[15:0] 00 alu_op[4:0] 00 reg_write c_imm c_carry c_imm c_jump c_load_store c_mov c_store c_sub dm_read dm_write raddr1[3:0] 0 raddr2[3:0] 0	D55A	057A 04	A5FF 09	S	\$\$	IS	05DA 14	454F	40CA 1E
instruction[15:0] 00 alu_op[4:0] 00 reg_write c_imm c_c_carry c_imm c_ljump c_load_store c_mov c_store c_sub dm_read dm_write raddr1[3:0] 00	055A							454F 1C	40CA 1E
alu_op[4:0]								10	18
eg_write c_imm c_carry c_imm c_jump c_load_store c_mov c_store c_store d_mread dm_read dm_write c_addr1[3:0] 0									
imm									
c_carry c_imm c_jump c_load_store c_mov c_store c_sub dm_read dm_write c_addr1[3:0] 0									
2_imm									
load_store									
:_load_store									
_storesub m_read m_write addr1[3:0] 0									
_sub m_read m_write addr1[3:0] 0									
m_read m_write addr1[3:0] 0									
m_write addr1[3:0] 0									
addr1[3:0]									
addr1[3:0] 0 addr2[3:0] 0									
addr2[3:0] 0	 A		F				A	F	A
									0