



DIGITAL LOGIC

Combinational Circuit

Lecture No. 5



By- CHANDAN SIR

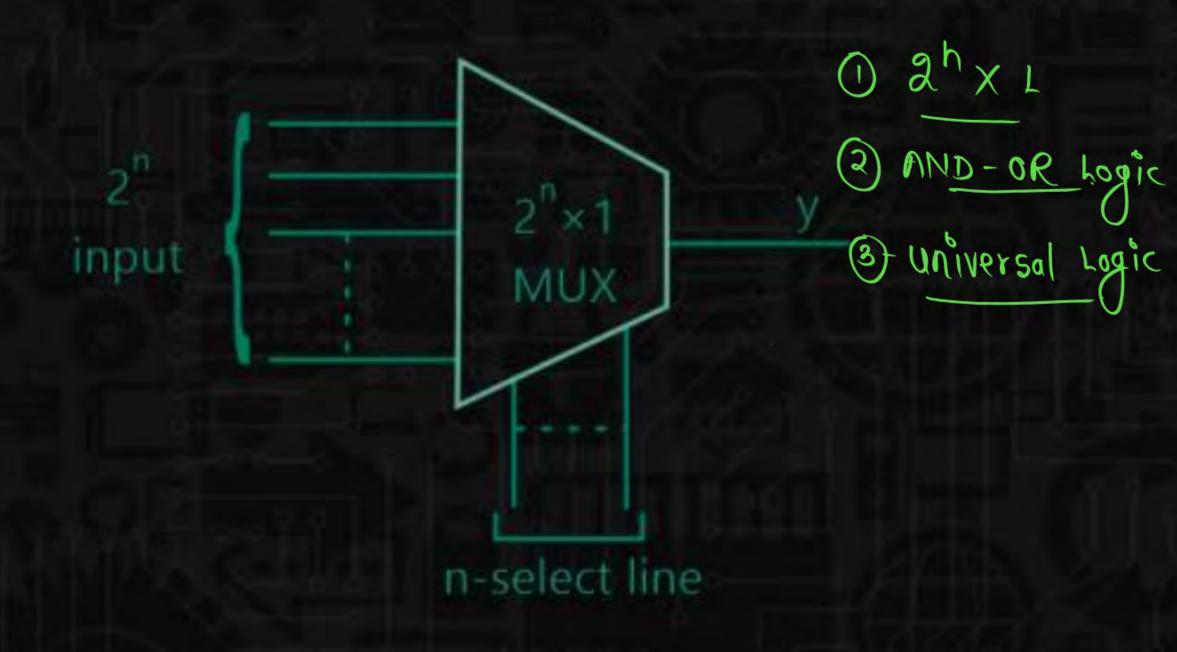


TOPICS TO BE COVERED **01** MULTIPLEXER

02 QUESTION PRACTICE

03 DISCUSSION





Recape



Design a 2 × 1 MUX

✓Design a 4 × 1 MUX





Q.1

Design 4×1 MUX using 2×1 MUX.

$$\frac{4}{2} + \frac{2}{3}$$

$$\frac{4}{2} + \frac{1}{3}$$

$$\frac{3}{2} + \frac{1}{3} = 3$$

$$\frac{4}{3} + \frac{1}{3} = 3$$



Q.2

Design 64 × 1 MUX using 8 × 1 MUX.

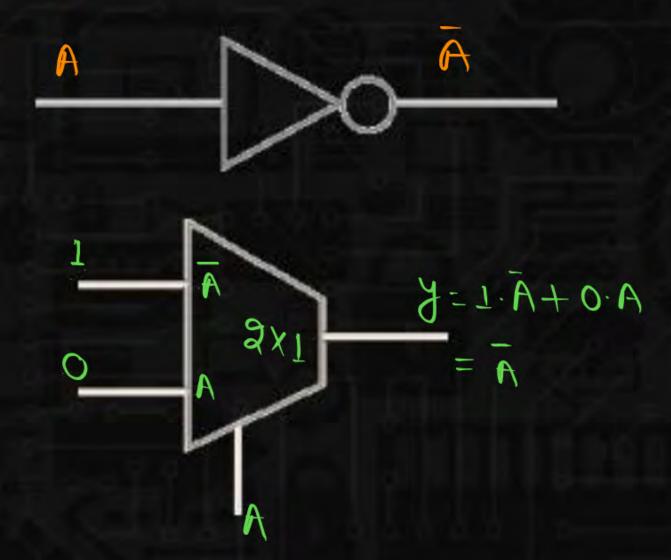
$$8\times 1 \xrightarrow{\frac{64}{8} + \frac{6}{8}} 64\times 1$$

$$8+1=9$$

Type 2- Mux as a Universal Logic

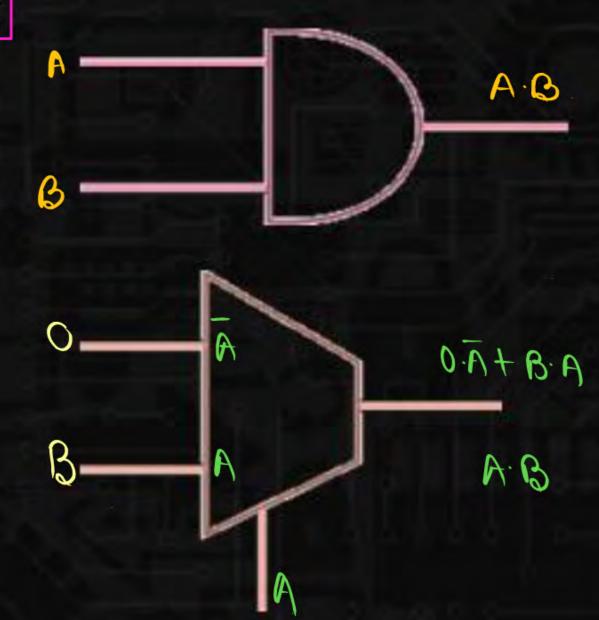


Not Gate



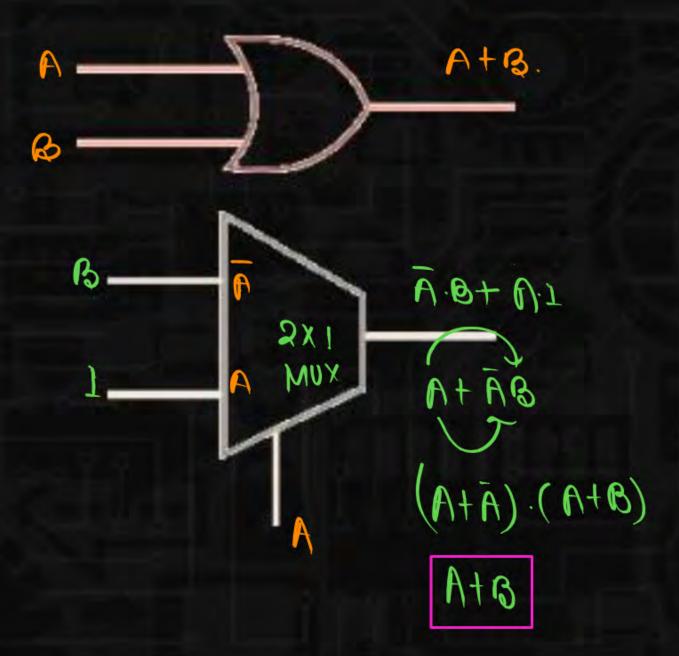


2. AND GATE





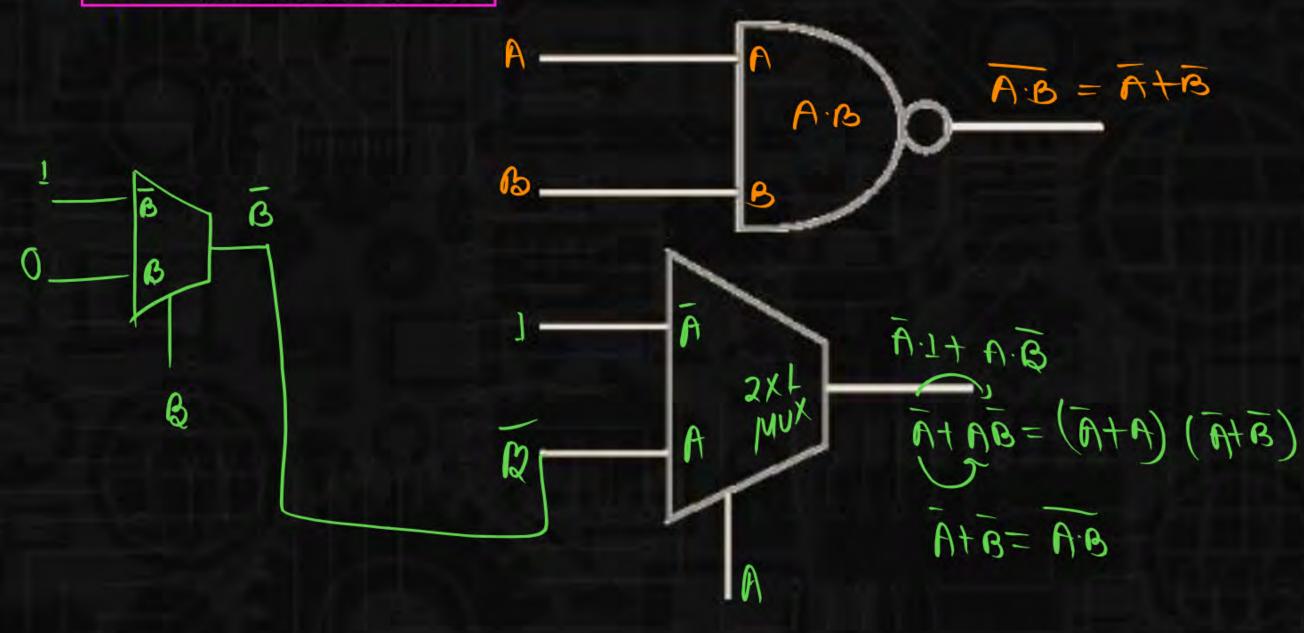
3. OR GATE





Two gx! are required

4. NAND GATE





-> Two axi MUX are required

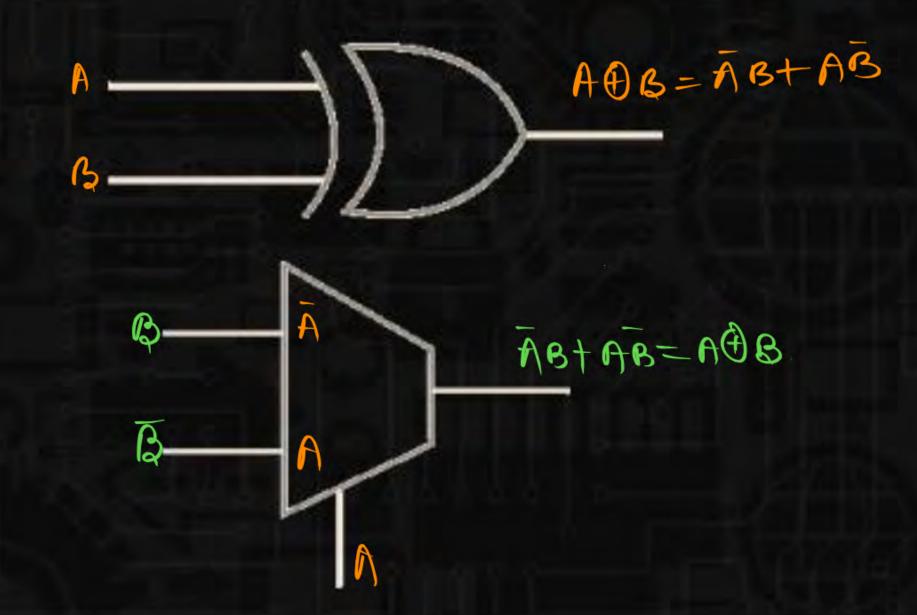
5. NOR GATE

$$A = A + B = A \cdot B$$

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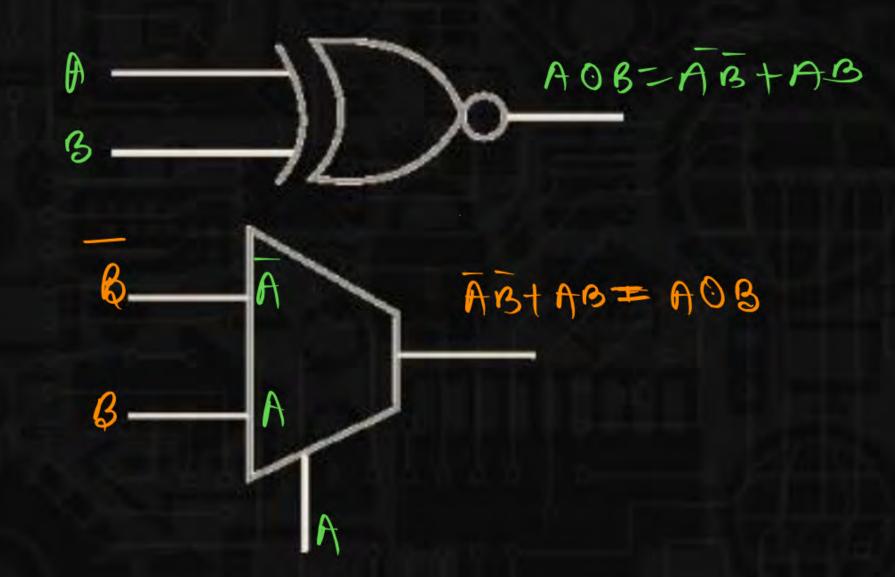


6. X-OR GATE





7. X-NOR GATE

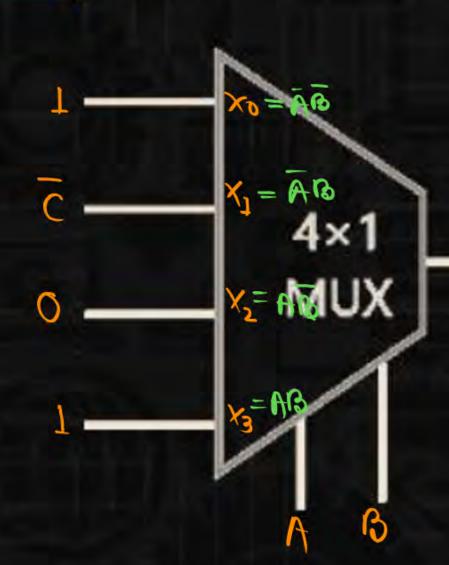


Type 3- Minimization



Q.

Find the output f.



$$J = \overline{AB} \cdot 1 + \overline{ABC} + \overline{AB} \cdot 0 + \overline{ABC} \cdot 1$$

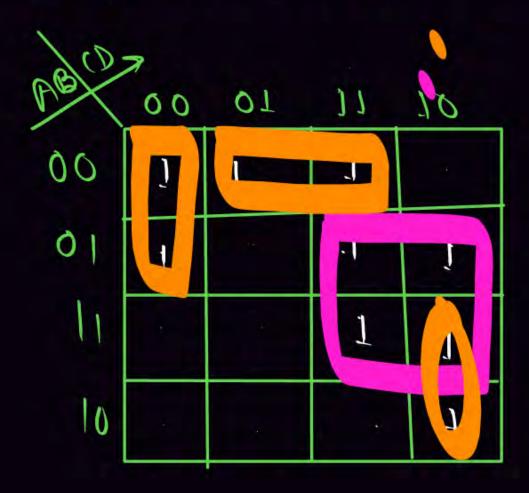
$$J = \overline{AB} + \overline{ABC} + \overline{AB}$$

$$J = \overline{AB} \cdot (\overline{C} + C) + \overline{ABC} + \overline{ABC$$





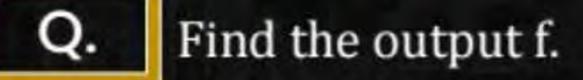


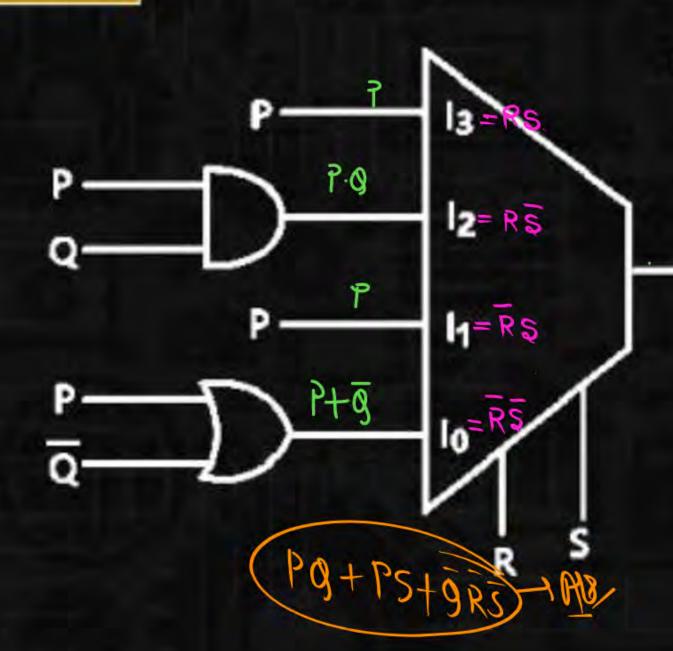


BC+ACD+ABD +ACD

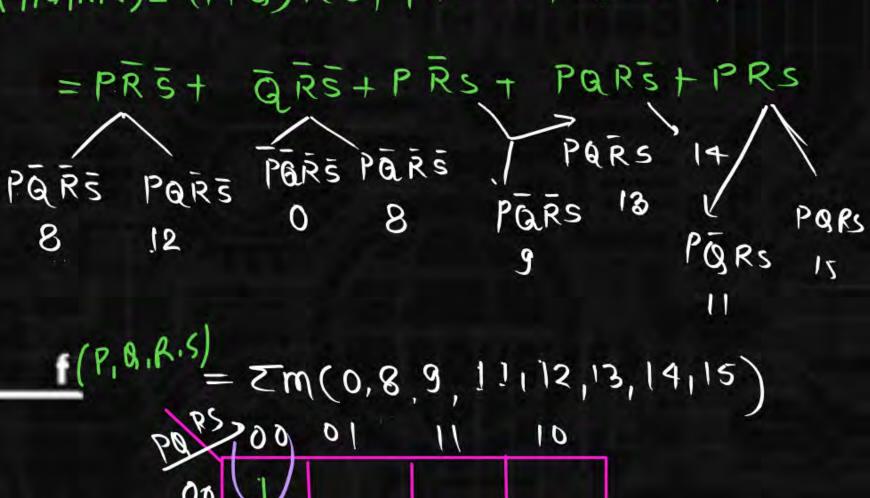
PYB







01





Type-4 cascoding
$$A + \overline{A}B = (A + \overline{A})(A + B) = (A + B)$$

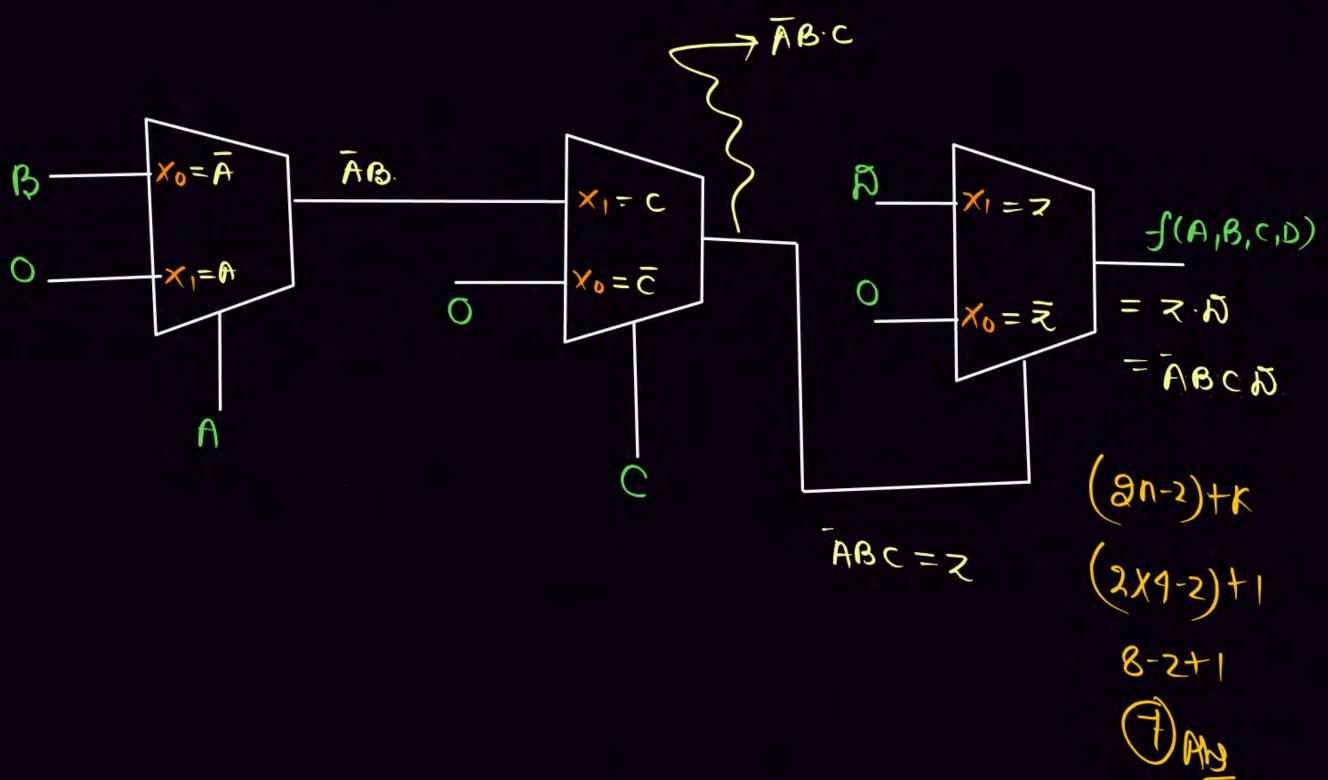
$$Z + (A + B)$$

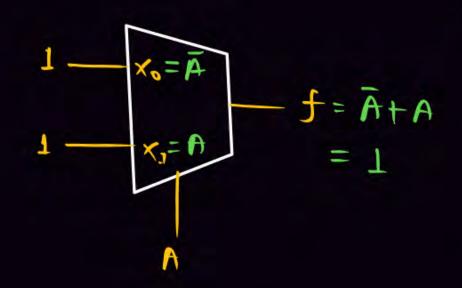
$$Z = \overline{A} + B$$

$$Z = \overline{$$

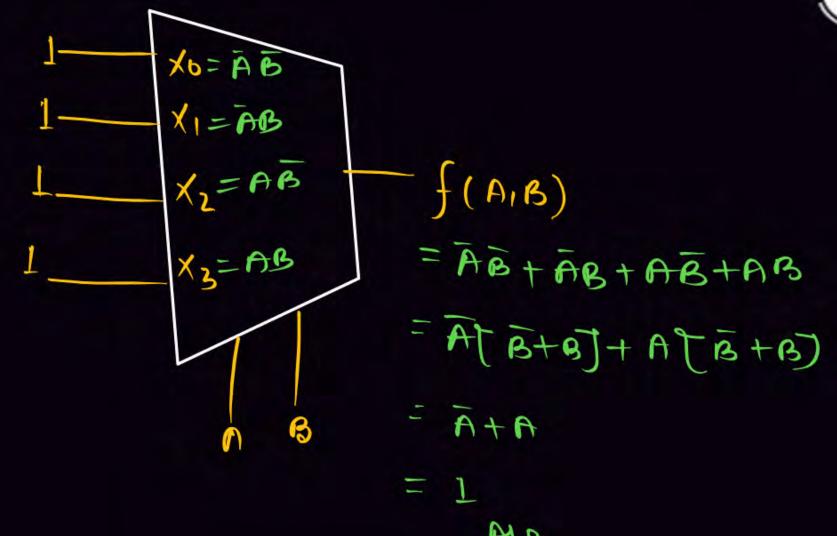


Ex.





Q

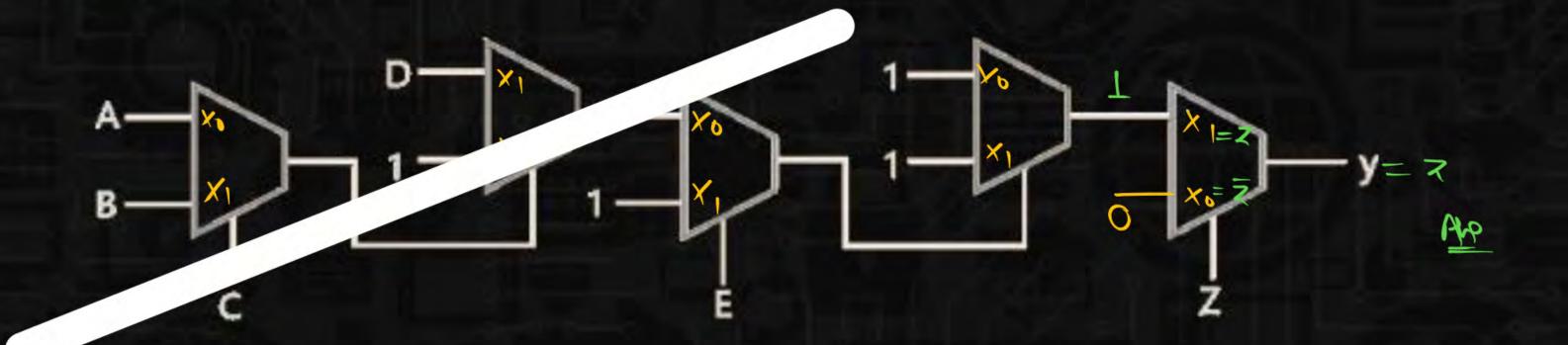


Type 4- Cascading



Q

Find the output f.





Type 5 :> Implementation of function.

$$Ex. f(A_1B_1C) = \overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC}$$

$$= \sum m(0, 1, 4, 7)$$

$$(ase(1) By 8x1 Mux.$$

8XI MUX

