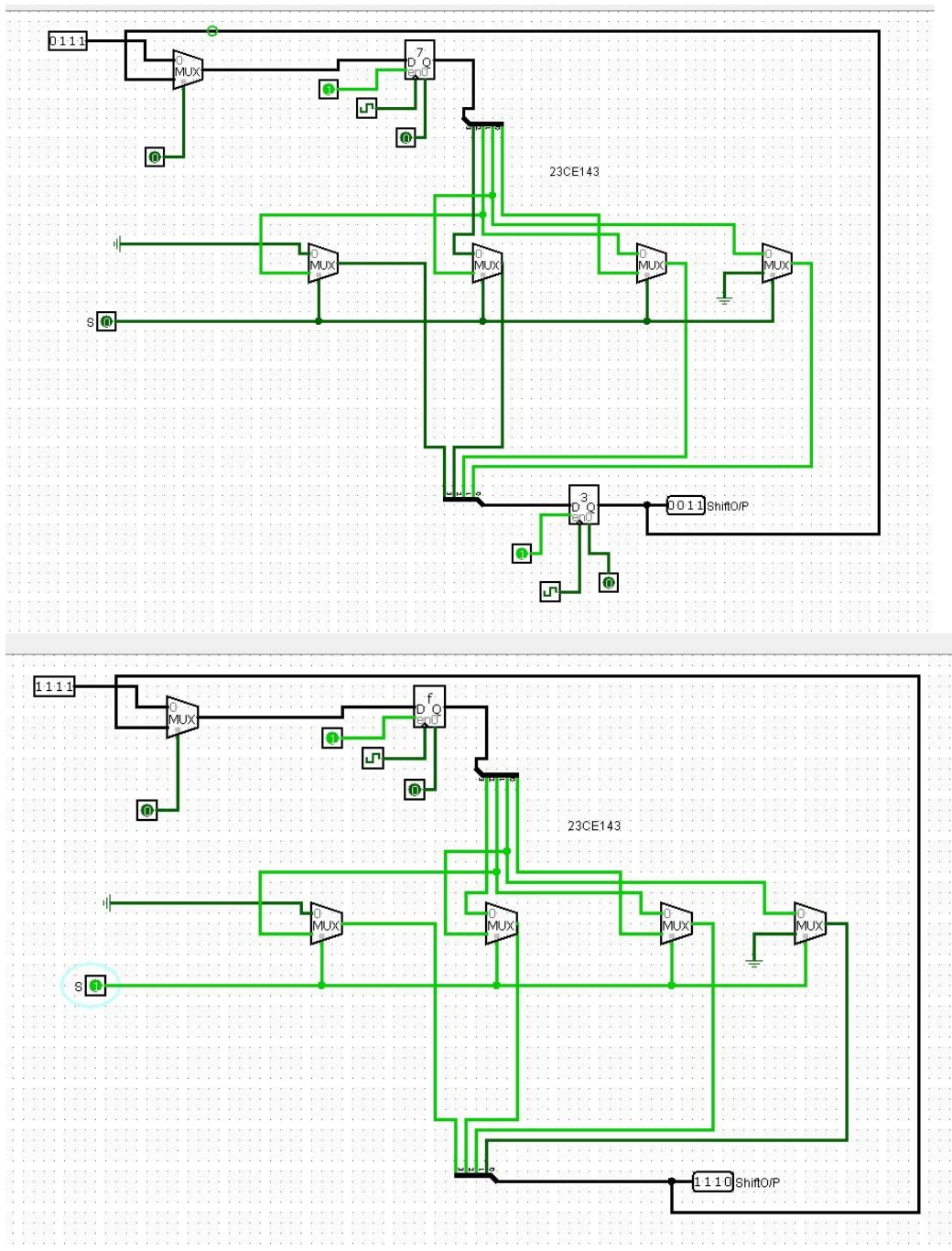
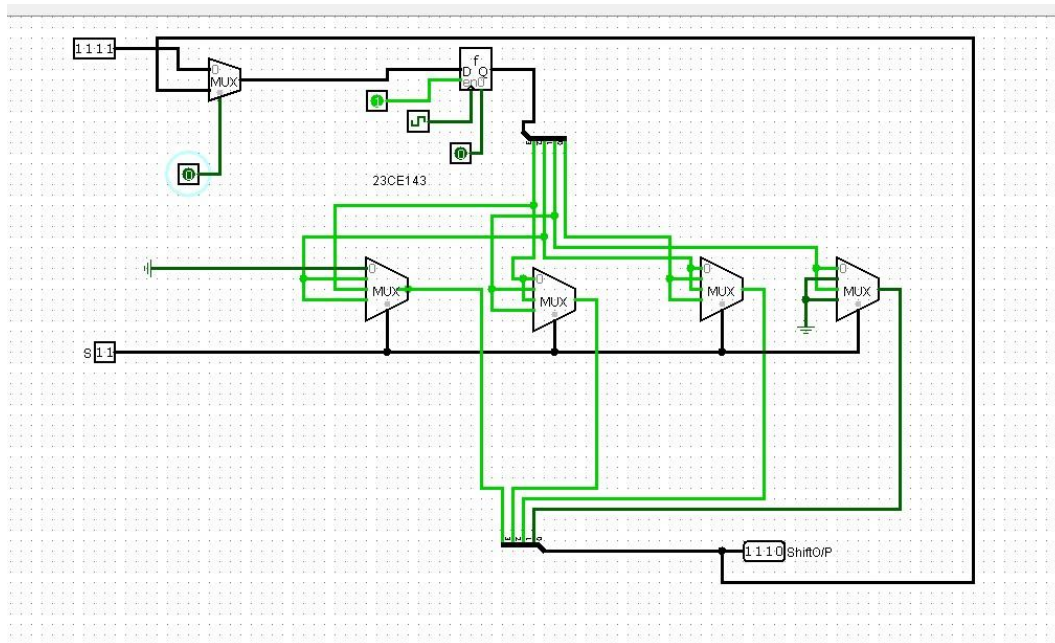


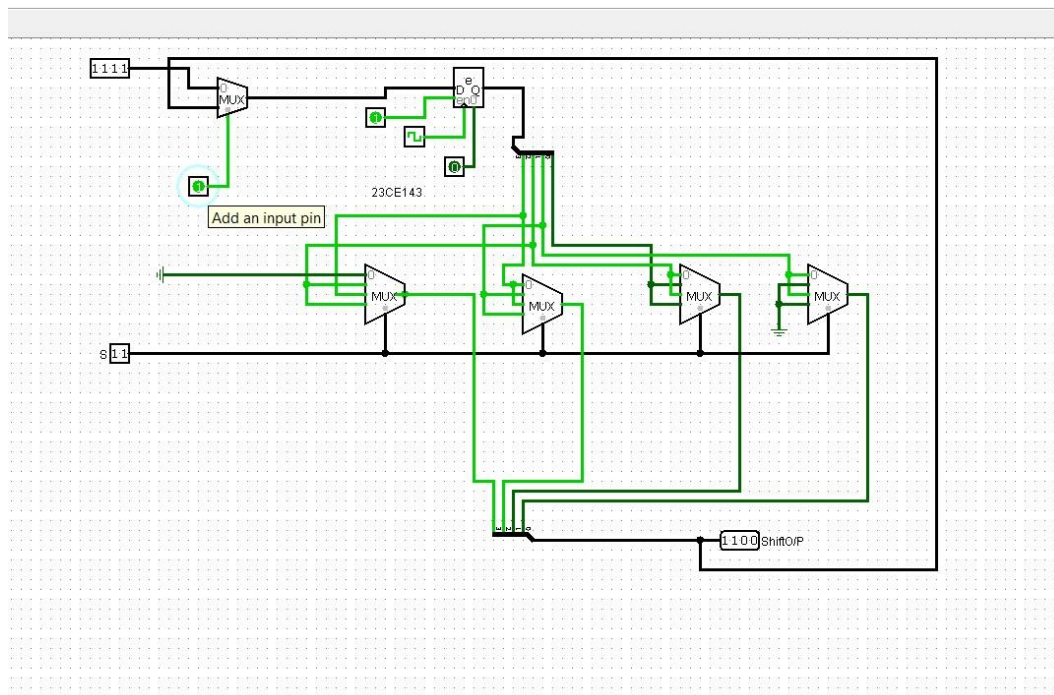
DATE:

EXPERIMENT NO. 6

AIM: Design and Implement 1-bit shifter circuit using logisim which can perform logical left, logical right, arithmetic left and arithmetic right shift.

OUTPUTS:





□ **CONCLUSION:**