SAAHITHYA MANDELA

PROFILE

Seeking a position, where I can utilize my skills and abilities to its best, that helps in my overall professional and personal growth.

EDUCATION

- **B.TECH in Electronics and Communication Engineering**
  CGPA – 7.9 / 10
  2019 – 2023

Amrita School of Engineering,
Amrita Vishwa Vidyapeetham, Bengaluru

- **Class 12** – 97.5%   **2019**

Institution: Sri Sarada Educational Institution

PROJECTS

- **Automated Teller Machine(ATM) using Verilog**
  February 2022 – June 2022

  The ATM machine is coded using Verilog hardware description language instead of the conventional coding languages C/C++ which gives us an edge over the others in terms of simplicity in coding and security as the code could not be easily hacked or changed due to strong encryption. The coding is done by employing the EDA Playground compiler.

- **Optimization of a Leading Zero Count Structure for Arithmetic Computations**
  August 2022 - May2023 (Ongoing)

  Understanding and designing a structure for leading zero count operation using Verilog. Implementing the design on FPGA and analyzing the design in terms of area, power and delay. The coding is done using Xilinx Vivado tool. The synthesis is done using Cadence genus 45nm.

TECHNICAL SKILLS

- C Programming
- Verilog
- Xilinx
- Cadence Genus 45nm
- Model SIM
- EDA Playground
- MATLAB
- Dsch2
- Keil uVision

CERTIFICATIONS AND WORKSHOPS

- Lab Workshop VLSI Fundamentals - Organized by NIELIT Calicut jointly with ARM and in association with NPTEL
- Become a good MATLAB Programmer by Udemy(Oct 2021)
- Executive member (2020-2021) - Electronics Computers Communications & Instrumentation Forum
- Electronica Workshop - An introductory workshop where students get introduced to various electronic component

LANGUAGES

- English   – Full Professional proficiency
- Telugu   – Native proficiency

HOBBIES

- Badminton
- Poster Making
- Reading Books