

EDUCATION

- **B.Tech Electronics and Communication Engineering**
CGPA – 9.26 / 10 **2019-2023**
Amrita Vishwa Vidyapeetham
- **Class 12 – 86.6%** **2019**
Institution: Narayana Junior College
- **Class 10 – 88.8%** **2017**
Institution: Narayana Olympiad School

TECHNICAL INTERESTS

- Embedded Systems
- Computer Architectural Development
- IoT

PROJECTS

- RFID based toll fee collection with validation
(02/2022 - Present)
 - To summarize the idea, the project is to reduce the stall time at the toll gates with the help of RF tag which is assigned to every vehicle and is unique and also use of number plate validation.
- Design of a 8-bit CPU using Logisim
(03/2022 - 04/2022)
 - a simple CPU using a self-defined instruction set and assembler

TECHNICAL SKILLS

- C/C++ programming
- Python
- SystemVerilog
- LTSpice
- Microwind DSCH
- RTL Coding

CERTIFICATIONS

- System Design Using Verilog (02/2022 - 04/2022)
- Advanced Computer Architecture (02/2022 - 04/2022)

ACHIEVEMENTS & HONOURS

- Executive Member - IEEE Communications Society

LANGUAGES

- Telugu
- English
- Hindi
- Kannada