DEPARTMENT OF INFORMATION TECHNOLOGY : : VRSEC
20IT3304 COMPUTER ORGANIZATION
ASSIGNMENT II QUESTION BANK
A.Y 2021-2022

Question No.			Course Outcome	BTL
1.	**a**	Discuss Associative memory in detail.	CO4	Understand
	b	Draw memory hierarchy in computer system.	CO4	Remember
2	**a**	Explain Division algorithm for signed magnitude representation with necessary hardware.	CO3	Understand
	b	Apply division algorithm for the given data Divisor B= 1010 Dividend A = 10011111	CO3	Apply
3	**a**	Draw and explain various mapping techniques in cache memory.	CO4	Understand
	b	What is locality of reference property and specify how the performance of cache memory is measured.	CO4	Remember
4	**a**	Draw the flowchart for addition algorithm when data is represented in signed 2's complement representation and mark each individual path in the flowchart by a number and then indicate the overall path that the algorithm takes when the following signed magnitude numbers are computed. In each case give the value of AVF. The left most bit in the following numbers represents the sign bit. a. 0 101101 + 0 011111 b. 1 011111 + 1 101101	CO3	Understand
	b	Specify the hardware required to perform addition operation in computer system.	CO3	Analyze
5	**a**	Draw flowchart for Booths multiplication algorithm.	CO3	Understand
	b	Show the contents of registers E, A, Q and SC during the process of booths multiplication of two binary numbers, +15(multiplicand) and +13(multiplier).	CO3	Apply
6.	**a**	Draw flowchart for multiplication of floating point numbers.	CO3	Understand
	b	When the data is said to be normalized?	CO3	Understand

Designation	Name in Capitals	Signature with Date
Course Coordinators	Dr.K.SITA KUMARI JANGAM EBENEZER	
Program Coordinator	Dr.G.KALYANI	
Head of the Department	Dr. M.SUNEETHA	