**DEPARTMENT OF INFORMATION TECHNOLOGY : : VRSEC**
**20IT3304  COMPUTER ORGANIZATION**
**SESSIONAL II IMPORTANT QUESTIONS**
**A.Y 2021-2022**

1. Draw and explain various mapping techniques in cache memory.
2. What is locality of reference property and specify how the performance of cache memory is measured.
3. Draw flowchart for floating point division operation.
4. Explain DMA in detail with neat block diagram.
5. Summarize the characteristics of multiprocessor system.
6. Explain interconnection structure.
7. Differentiate isolated versus memory mapped I/O.

Short questions:
1. Define hit ratio.
2. What is normalization?
3. What is locality of reference property?
4. What is mapping process?
6. What is associate memory?
7. Differentiate between burst transfer and cycle stealing.
8. What is hand shaking?
9. What is serial and parallel transmission?
10. What are different modes of transfer?
11. Define polling.