

IMPORTANT QUESTIONS

UNIT I

1. Construction of BUS
2. Binary Adder
3. Binary Adder Subtractor
4. Hardware for shifter
5. Hardwired control unit or Timing and control unit 146
6. RTL statements for Memory Reference instructions

UNIT II

1. Microprogrammed control unit
2. Microprogram sequencer
3. Stack organization
4. Instruction formats(one address, two address, three address, zero address)
5. Addressing modes
6. RISC and CISC characteristics

UNIT III

1. Flowcharts for arithmetic operations
2. Associative memory
3. Cache Memory

UNIT IV

1. Handshaking
2. Priority interrupt
3. DMA
4. Multiprocessors characteristics
5. Interconnection structures