(1.3) We said that the pattern of 1s and 0s used to represent an instruction in a computer has no intrinsic meaning. Why is this so and what is the implication of this statement?

The 1s and 0s just represent whether the specific bit is on or off, and the implication of this on and off state allows us to abstract the patterns of on and off bits to fit any meaning that is desired.

- (1.5) Modify the algorithm used in this chapter to locate the longest run of non-consecutive characters in the string.
 - 1. Read the first character in the string and call it New_Character
 - 2. Set the Current_Run_Value to New_Character
 - 3. Set the Current_Run_Length to 1
 - 4. Set the Max_Run to 1
 - 5. REPEAT
 - 6. Read the next character in the sequence (i.e., read New_Character)
 - 7. IF its value is NOT the same as Current_Run_Value
 - 8. THEN Current_Run_Length = Current_Run_Length + 1
 - 9. ELSE { Current_Run_Length = 1
 - 10. Current_Run_Value = New_Character}
 - 11. IF Current_Run_Length > Max_Run
 - 12. THEN Max_Run = Current_Run_Length
 - 13. UNTIL The last character is read
- (1.8) What are the differences between RTL, machine language, assembly language, high-level language, and pseudocode?

RTL - Notation used to define computer operations.

Machine Language - Binary representation of the OP-code for the target platform. Assembly Language - Human readable machine code.

High-Level Language - Code that can run on different types of computers without needing to be rewritten (C, Java).

Pseudocode - expression of actions needed to perform a task, arranged in a logical order.

(1.12) What is the difference between a computer's architecture and its organization?

A computer's architectures is abstract of the CPU, and the organization is the realization of the abstraction.

(1.18) What is the von Neumann bottleneck?

The limiting factor of a stored program computer is the path between the CPU and memory.

(1.33) Is Moore's law a law?

No it's an observance that hasn't deviated far enough yet to be considered broken.