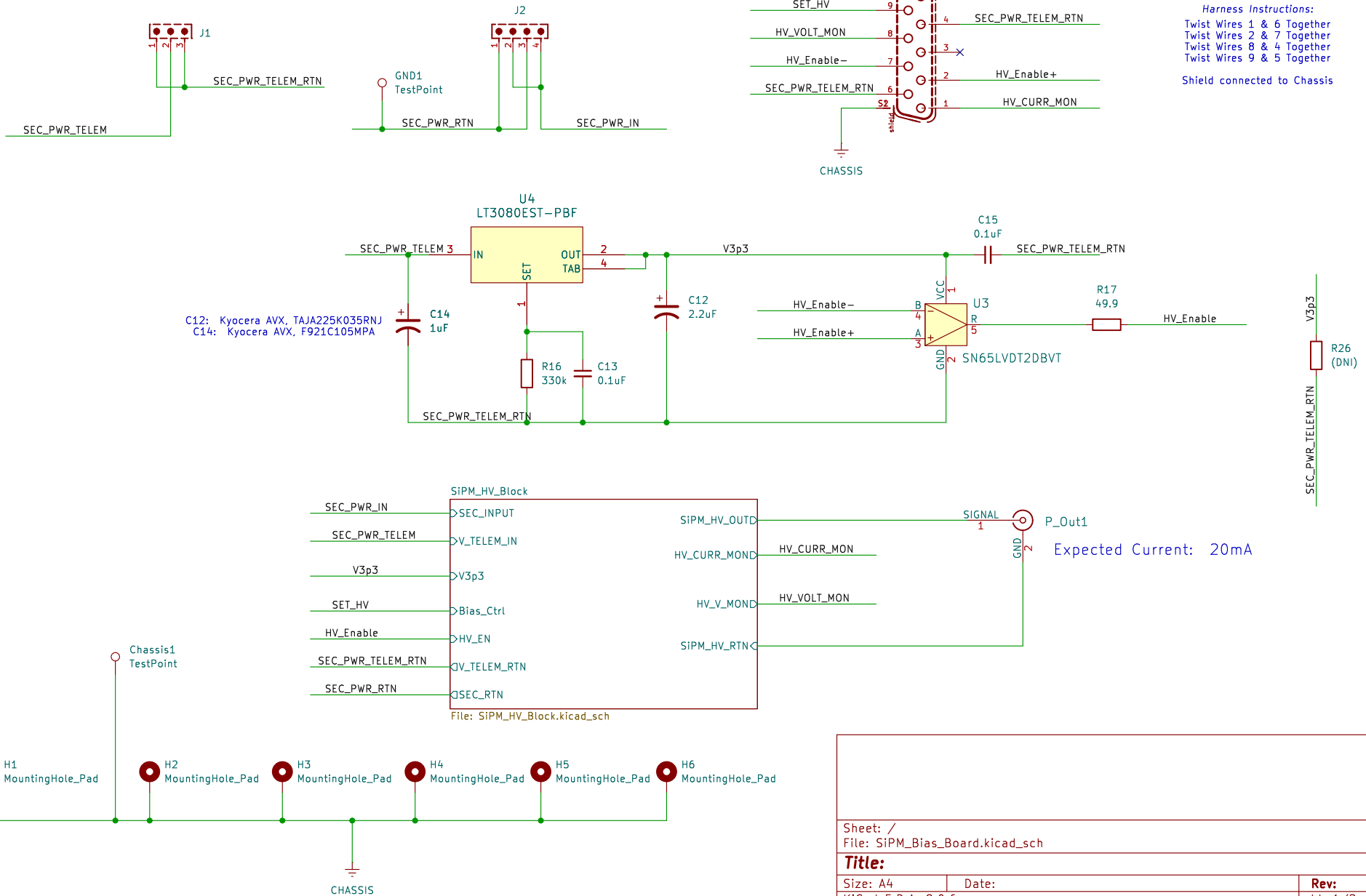


J1 and J2 are terminal blocks. The harnesses that connect to these will be fully enclosed in the box.

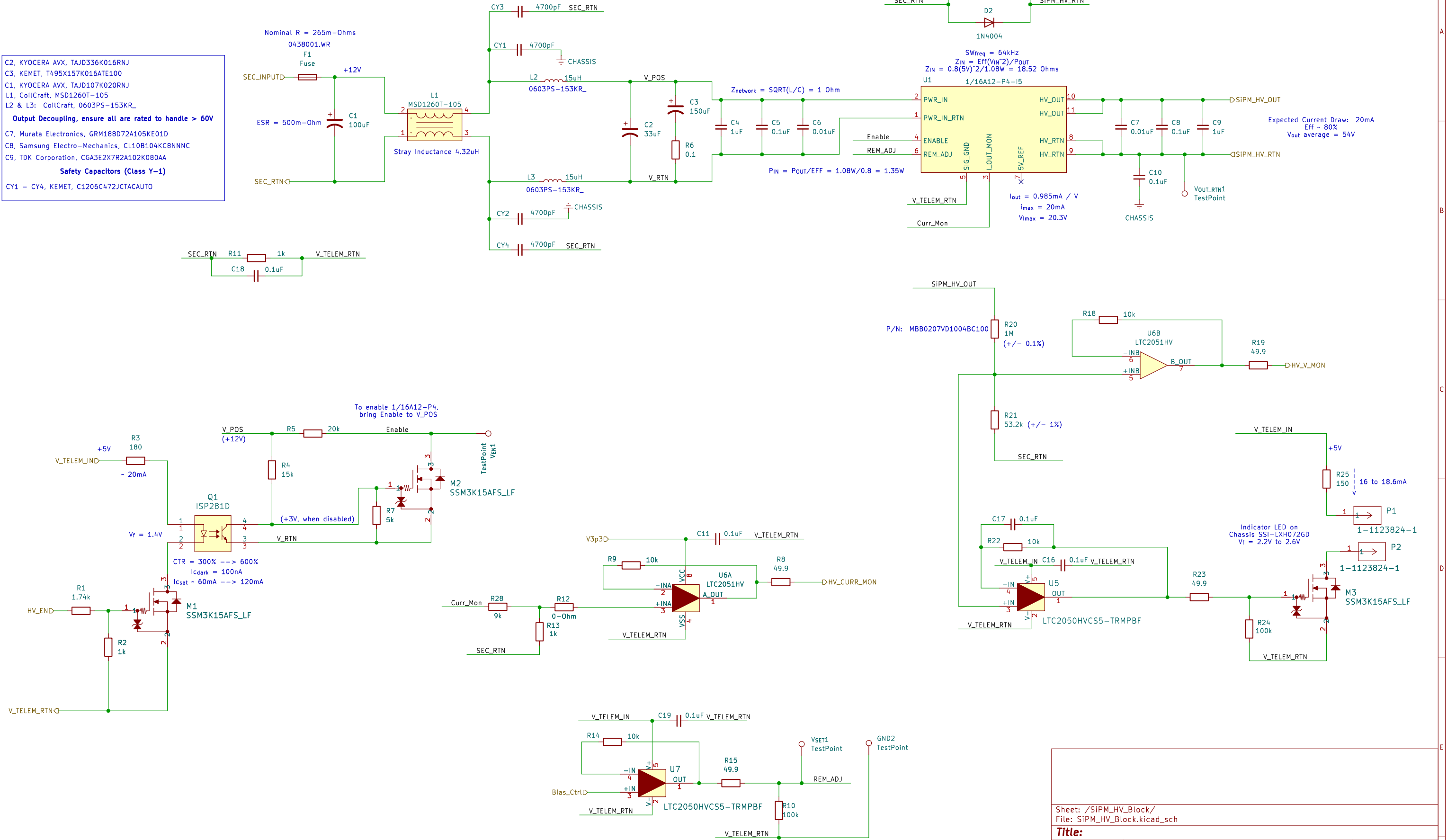
J3 will interface with an external box.

**Harness Instructions:**  
Twist Wires 1 & 6 Together  
Twist Wires 2 & 7 Together  
Twist Wires 8 & 4 Together  
Twist Wires 9 & 5 Together  
Shield connected to Chassis



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File: SiPM_Bias_Board.kicad_sch	
<b>Title:</b>	
Size: A4	Date:
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C2, KYOCERA AVX, TAJD336K016RNJ  
C3, KEMET, T495X157K016ATE100  
C1, KYOCERA AVX, TAJD107K020RNJ  
L1, CoilCraft, MSD1260T-105  
L2 & L3: CoilCraft, 0603PS-153KR\_  
**Output Decoupling, ensure all are rated to handle > 60V**  
C7, Murata Electronics, GRM188D72A105KE01D  
C8, Samsung Electro-Mechanics, CL10B104KC8NNNC  
C9, TDK Corporation, CGA3E2X7R2A102K080AA  
**Safety Capacitors (Class Y-1)**  
CY1 - CY4, KEMET, C1206C472JCTACAU0



Sheet: /SiPM\_HV\_Block/  
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**Title:**

Size: B

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Rev:

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