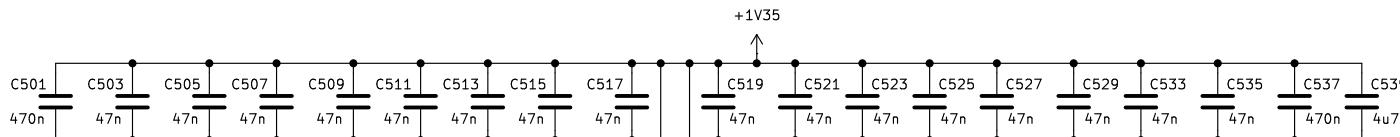


DDR3

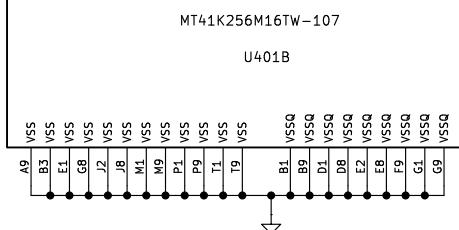
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Size: A3	Date: 2025-09-13	Rev: A
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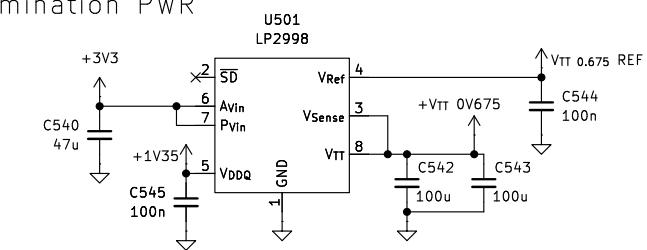
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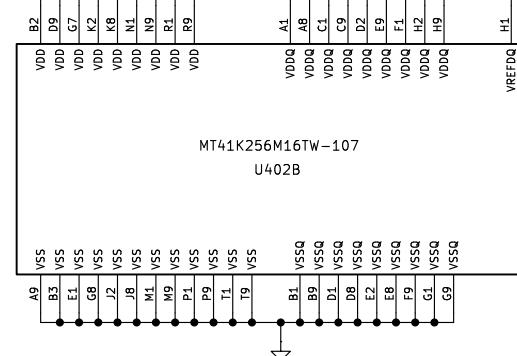
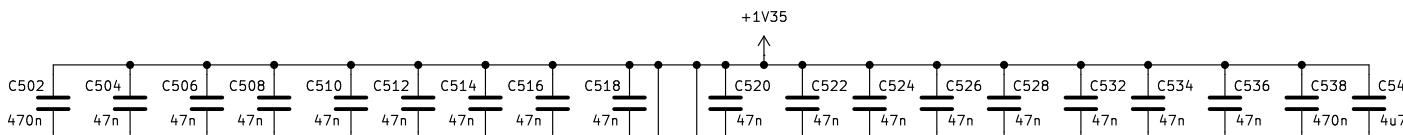
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DDR Termination PWR



C



DDR3 Power

Sheet: /Zynq Dram/DDR PWR/

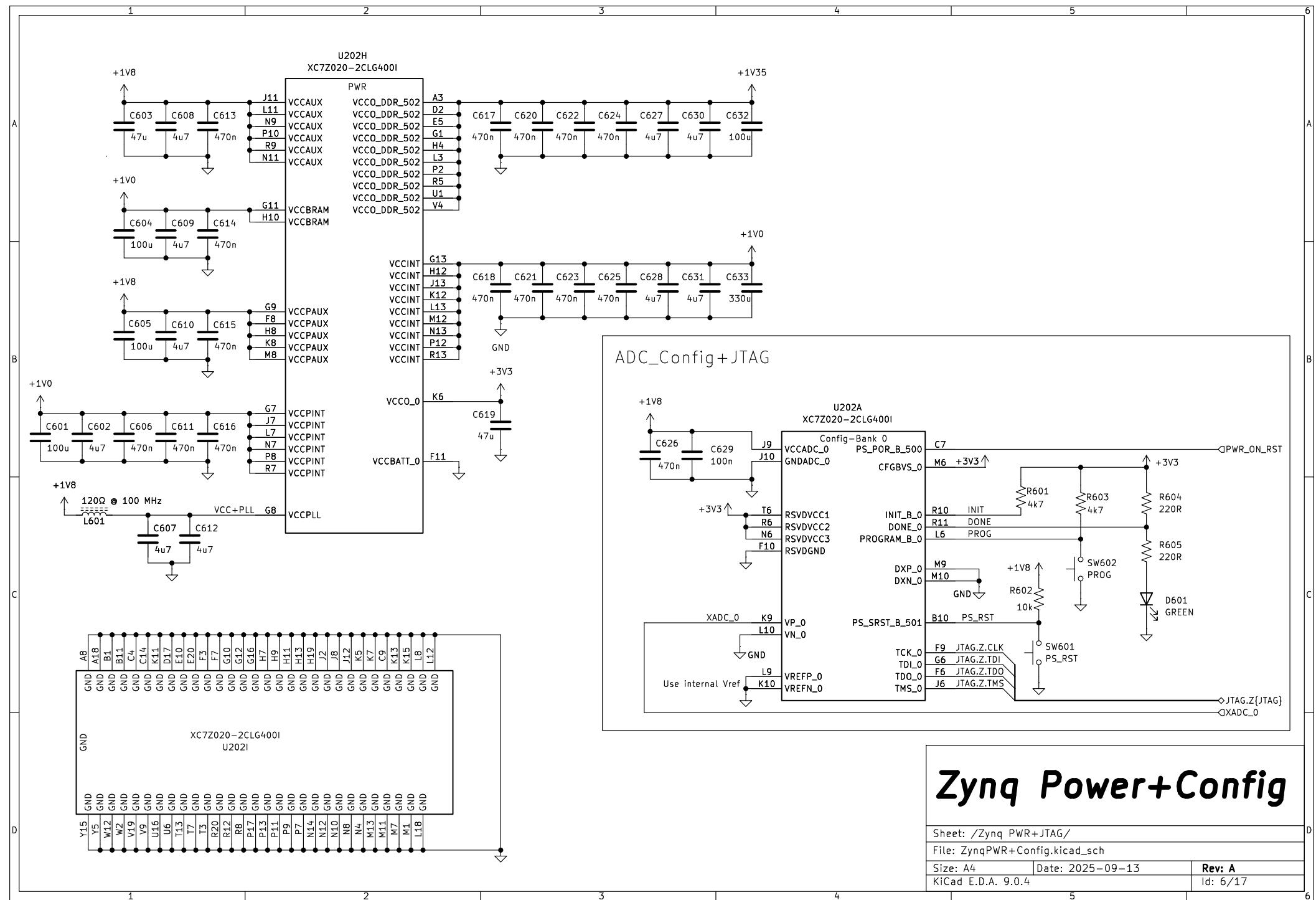
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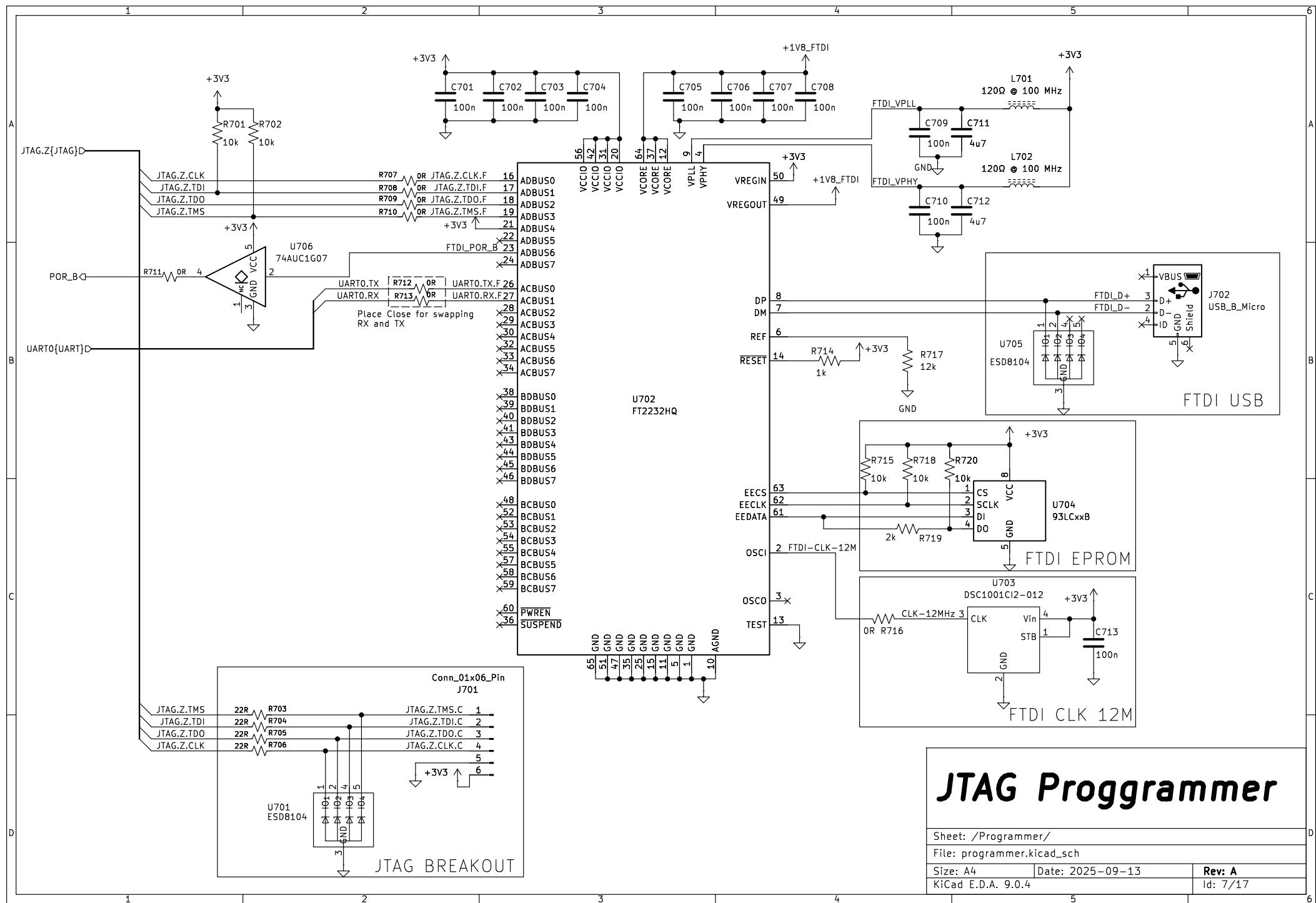
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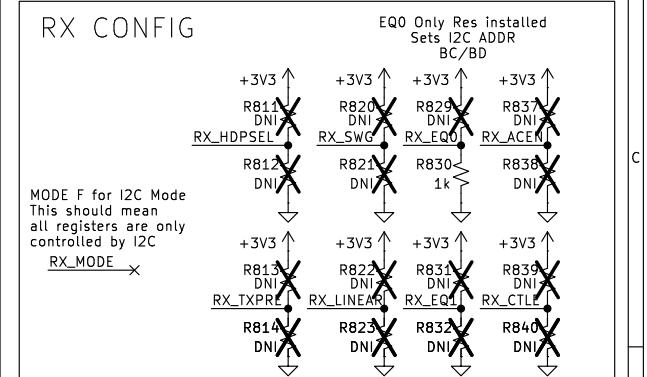
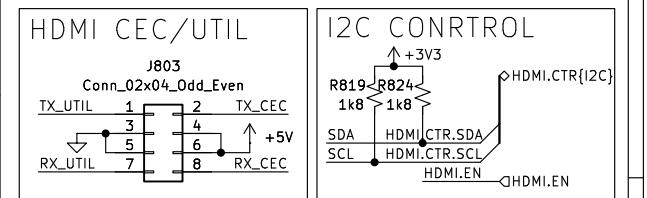
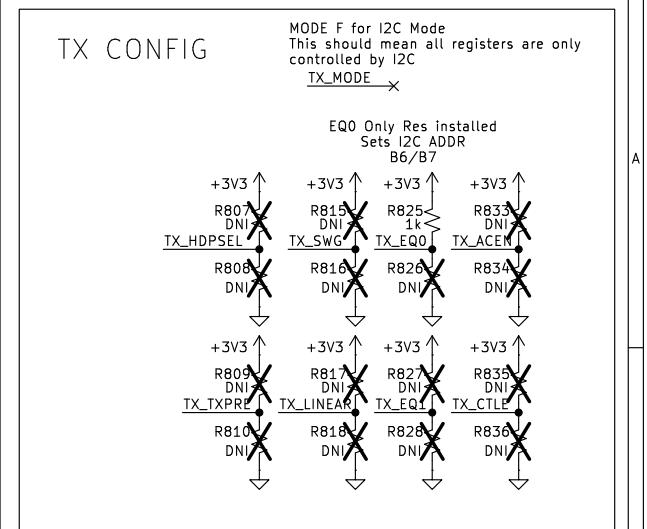
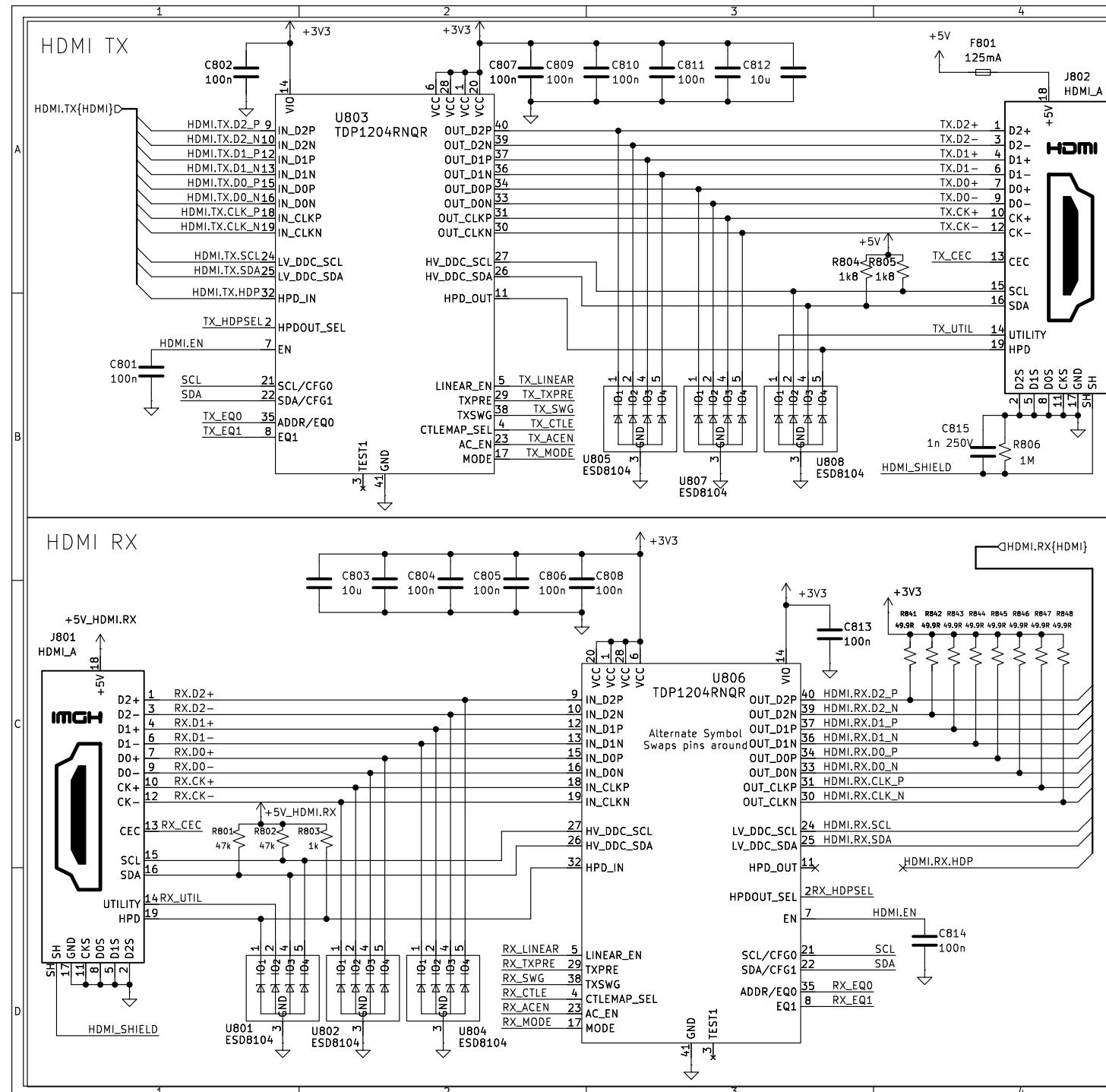
Rev: A

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Id: 5/17



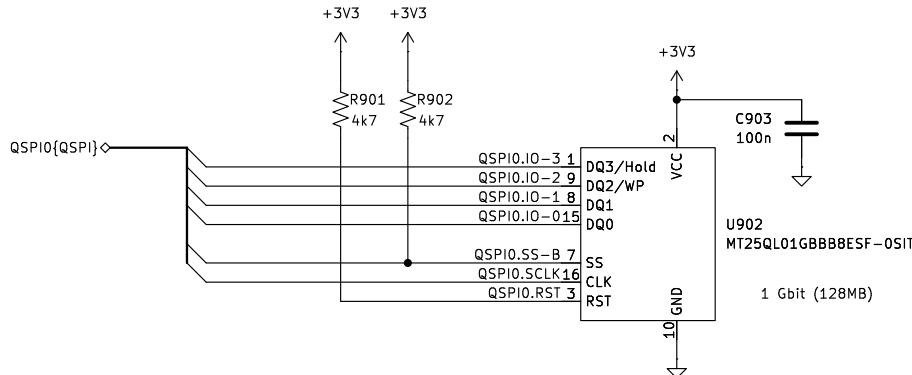




HDMI

1 2 3 4 5 6

QSPI Boot



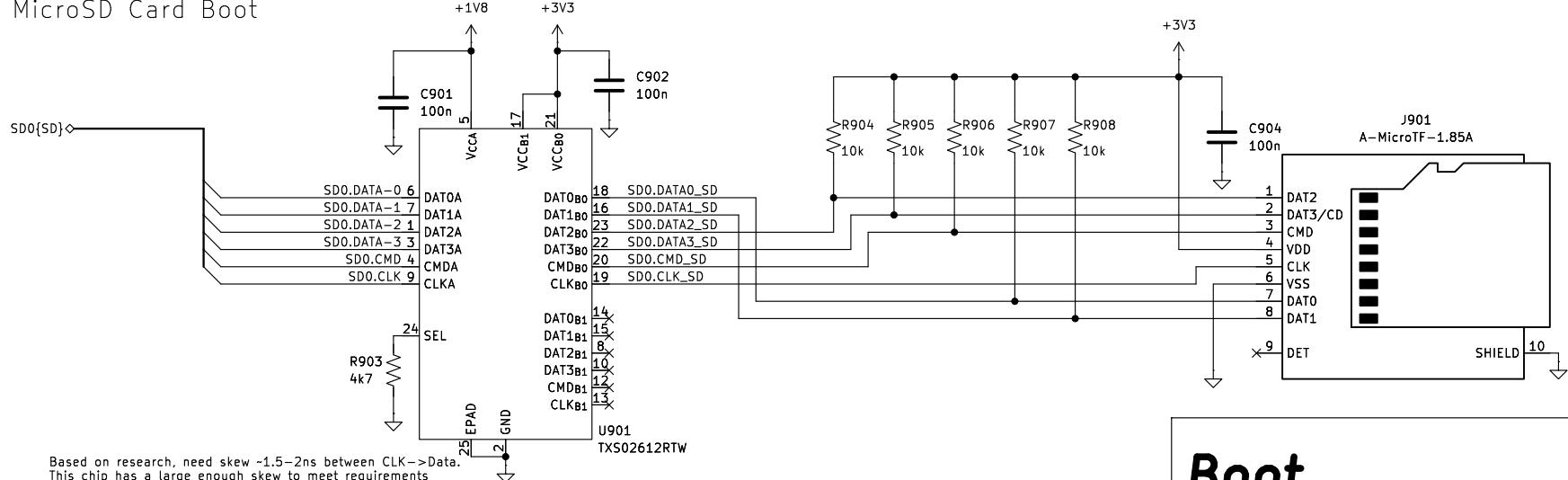
A

A

B

B

MicroSD Card Boot



C

C

Based on research, need skew ~1.5–2ns between CLK->Data.
This chip has a large enough skew to meet requirements

D

D

Boot

Sheet: /Boot/

File: Boot.kicad_sch

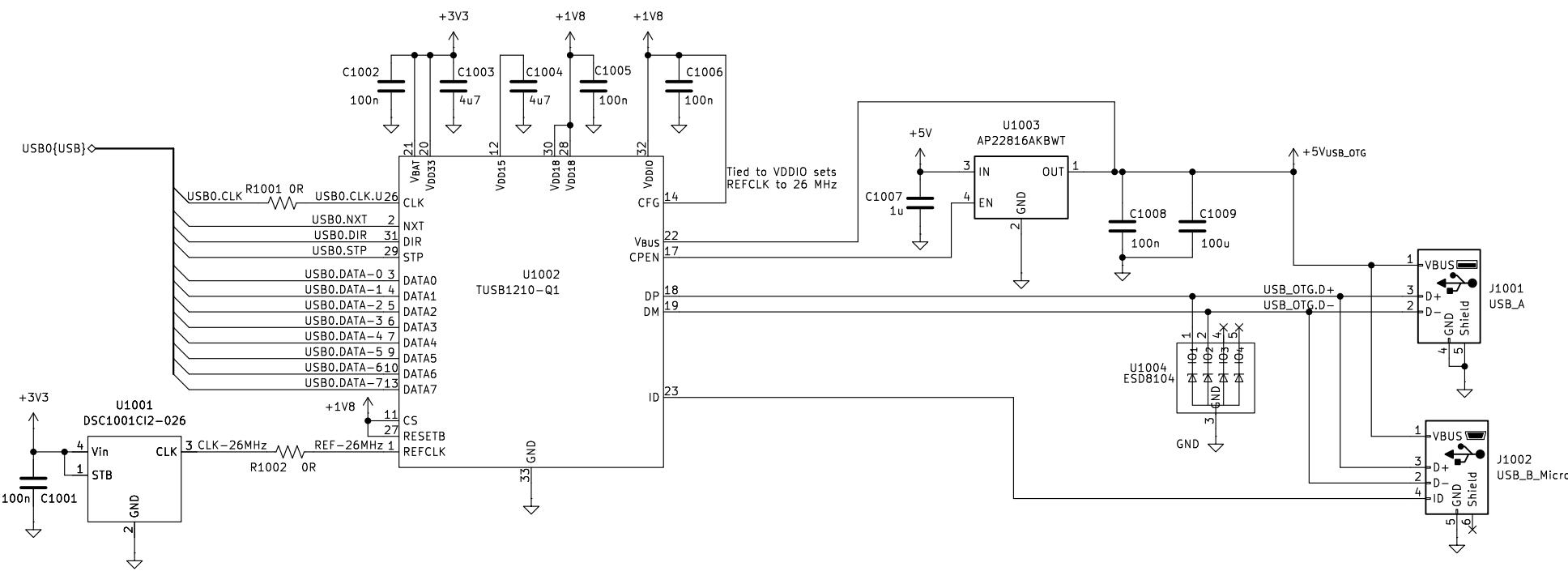
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KiCad E.D.A. 9.0.4

Rev: A

Id: 9/17

1 2 3 4 5 6



USB OTG

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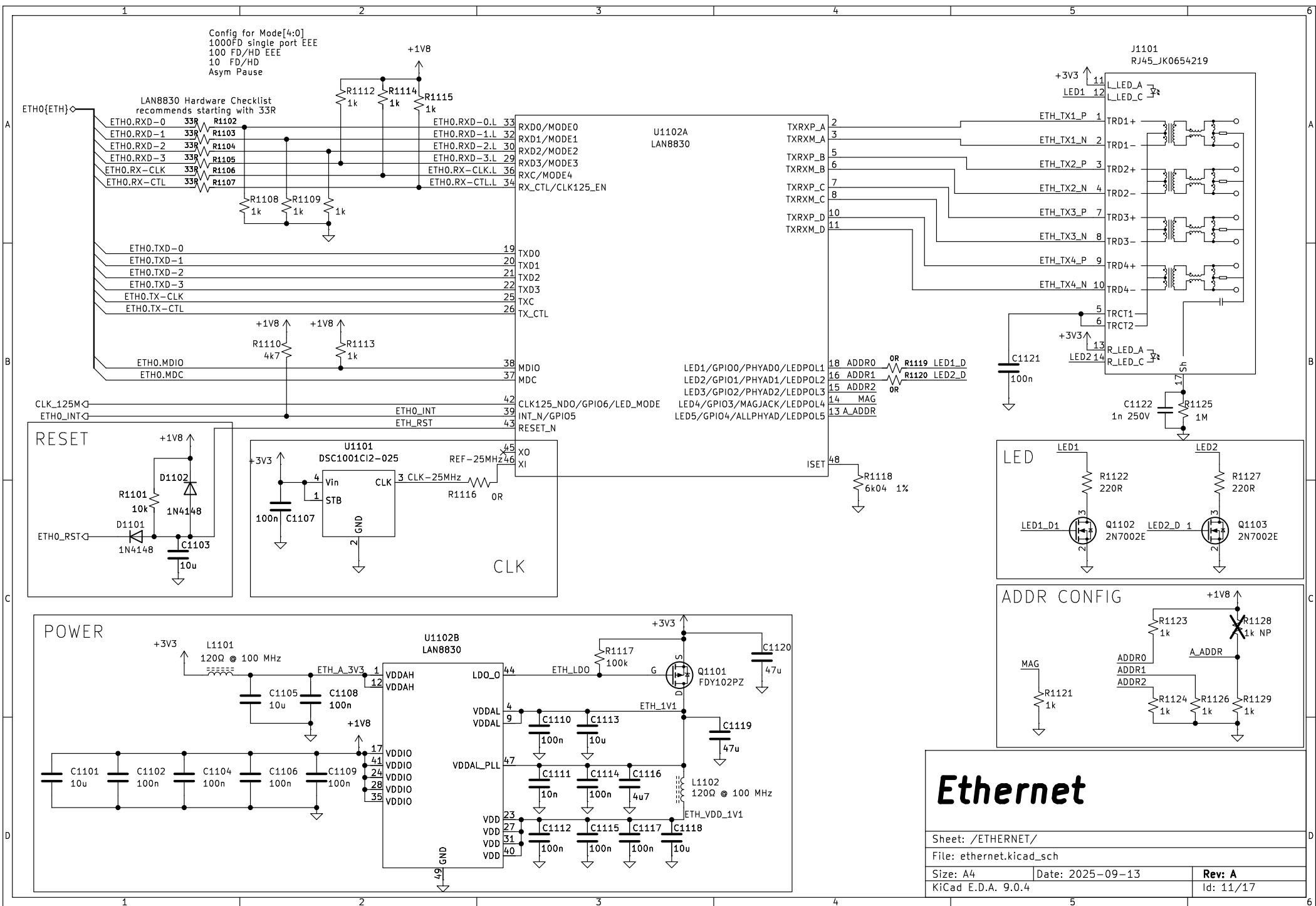
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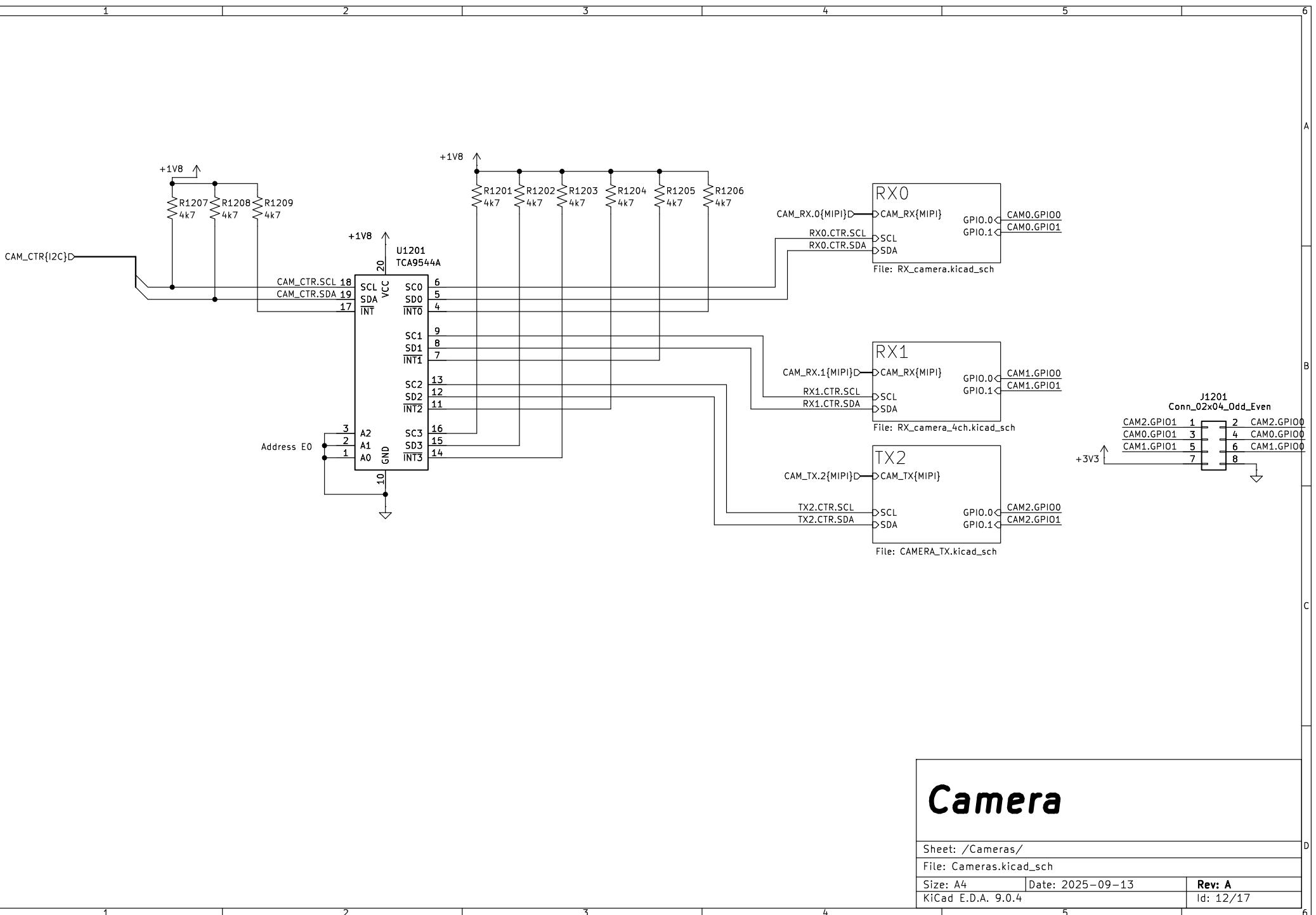
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KiCad E.D.A. 9.0.4

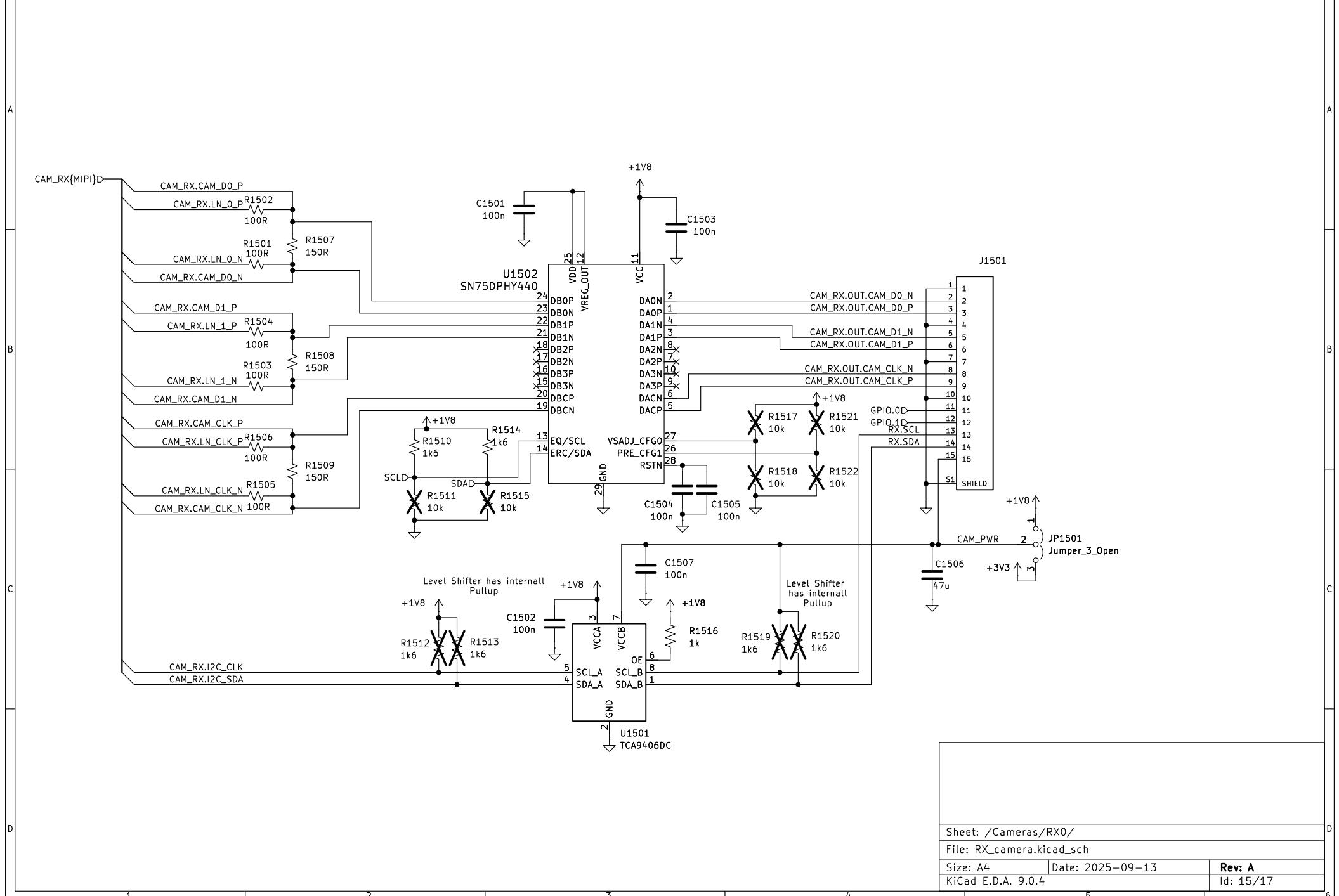
Rev: A

Id: 10/17





1 2 3 4 5 6



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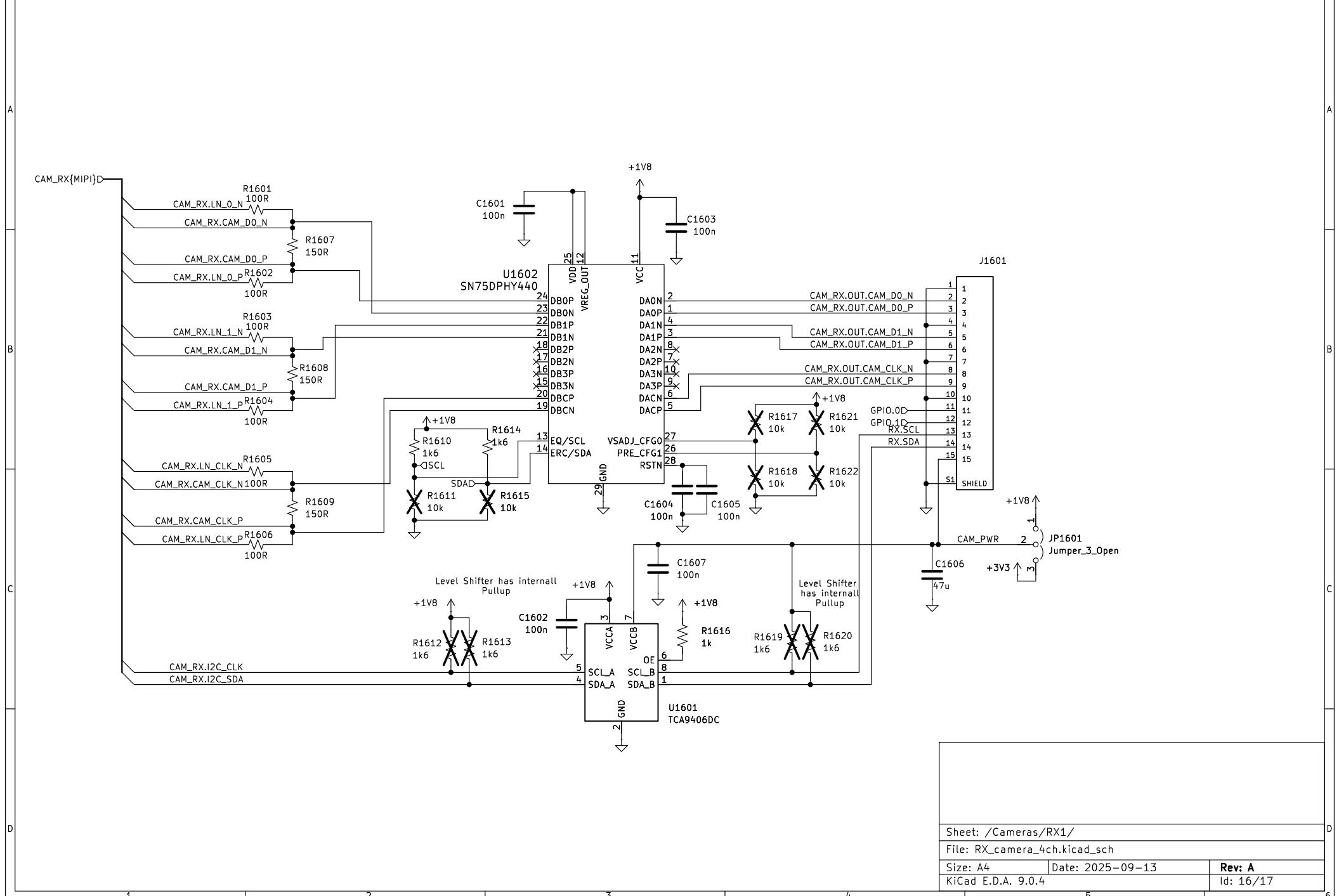
Rev: A

KiCad E.D.A. 9.0.4

Id: 15/17

1 2 3 4 5 6

1 2 3 4 5 6



Sheet: /Cameras/RX1/

File: RX_camera_4ch.kicad_sch

Size: A4 Date: 2025-09-13

Rev: A

KiCad E.D.A. 9.0.4

Id: 16/17

1 2 3 4 5 6

