

Daffodil International University Department of Computer Science and Engineering

Department of Computer Science and Engineering
Faculty of Science & Information Technology
Final Examination Semester: Spring 2018

Course Code: CSE 112
Course Title: Computer Fundamentals
Course Teacher: ALL

| Time: 2 hours Full Marks: | | | : 40 |
|--|----|--|------|
| Answer any four of the following five questions. Figures in the right-hand margin indicate full marks. | | | |
| ħ. | a) | Define network topology. Write the names of major network topologies along | 5 |
| | | with diagrams. | |
| | b) | What do you understand by the OSI model? Describe the OSI model along with | 5 |
| | | relevant diagram. | |
| | | | |
| 2. | a) | Draw the basic processor and memory architecture of a computer system. | 4 |
| | b) | Describe CISC and RISC architecture in detail. | 4 |
| | c) | Calculate 1 gigabyte equals how many bytes, nibbles, and bits. | 2 |
| | | | |
| 3. | a) | What are variables in C? Write the rules and conventions for naming variables in | 5 |
| | | C. Give examples of declaring variables. | |
| | b) | Write a program in C that takes three integer numbers as input and show their | 5 |
| | | sum as output. | |
| | | | |
| 4. | a) | What do you understand by hardware, firmware and software? Discuss the | 5 |
| | | relationship among them. | |
| | b) | Write the ways of acquiring software. Describe advantages and limitations of | 5 |
| | | any two of them. | |

5. a) Draw a logic circuit diagram using NAND and NOR only to implement the 5 following Boolean function.

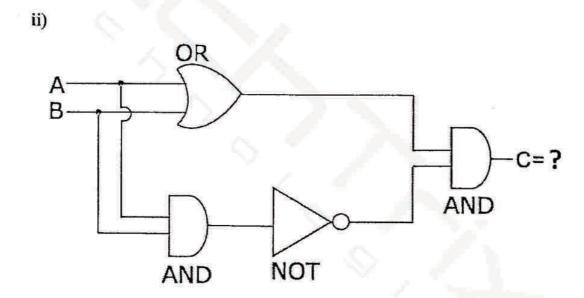
$$W = X.Y + \overline{X}.\overline{Y}.$$

b) Find a Boolean expression from each of the following logic circuits.

A

NOT D=?AND

OR



5