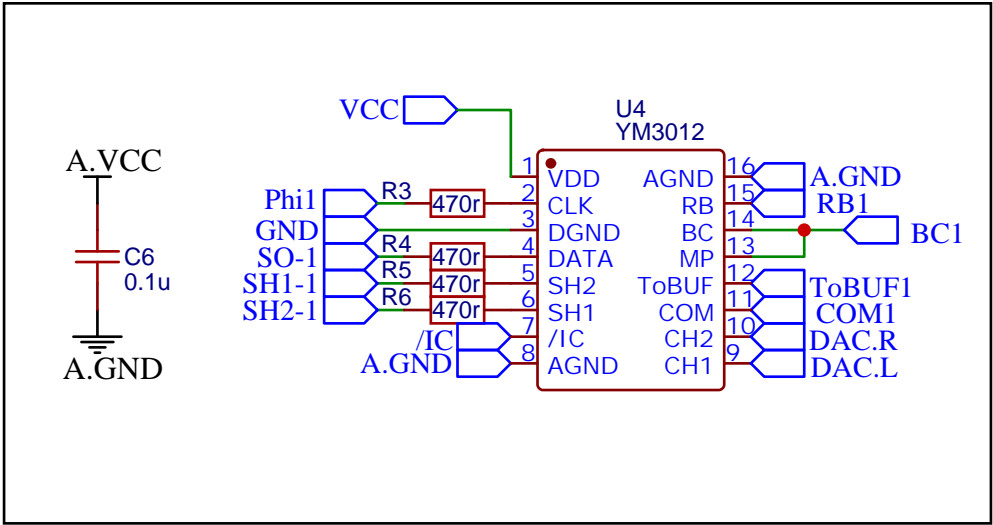


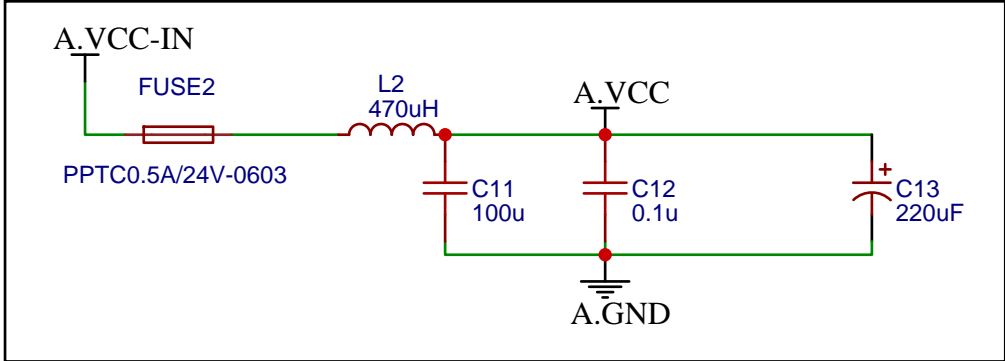
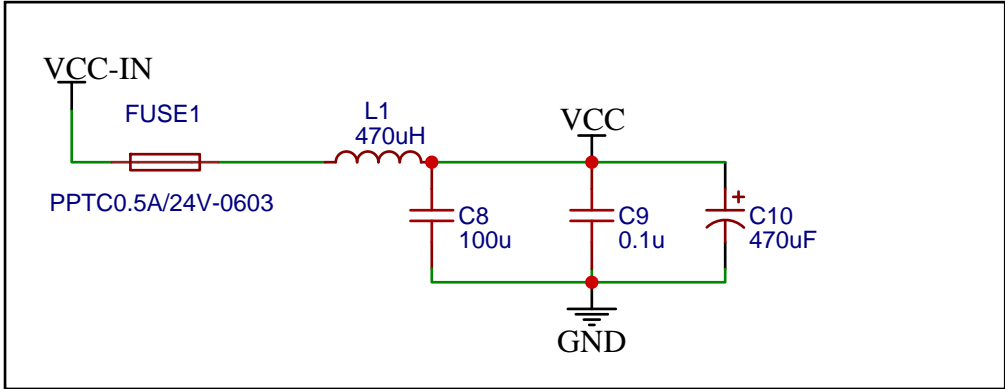
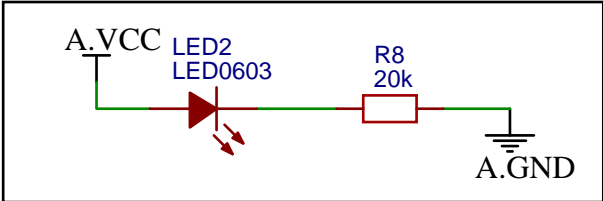
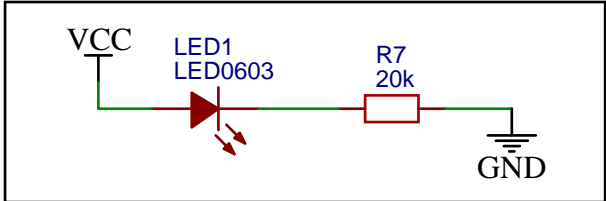
2024.1.8
1.精简放大结构

考虑这里要两级放大

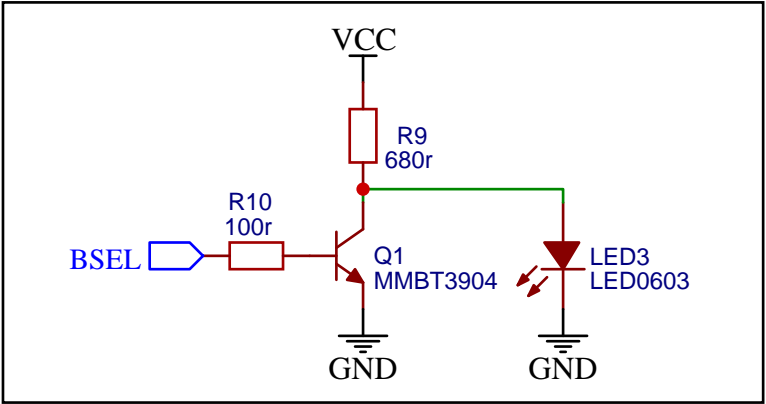
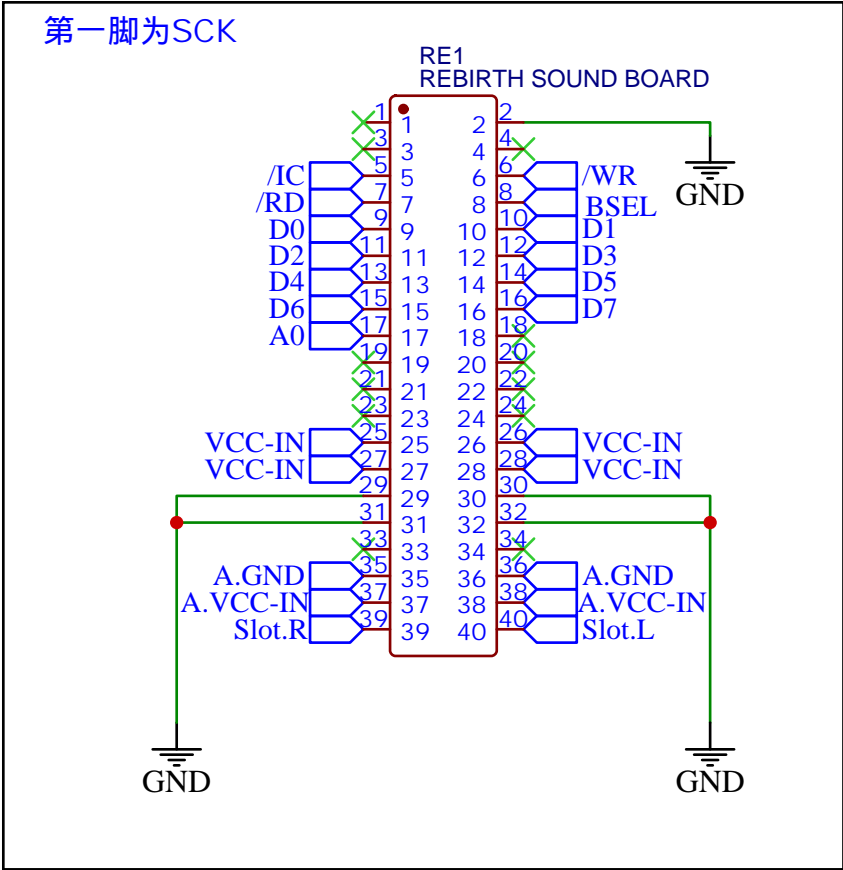
此时YM2151只有3.8V，A.VCC只有4V，由于YM2151为NMOS耗电太高
考虑在主板上加DC-DC稳压模块，现在电压降的很厉害
使用b0505进行实验

2024.1.16
1.第一级缓冲，添加第二级放大
2.删除SCK功能
3.添加下拉电容
4.添加设计规则





去掉二极管，那个会引起压降



A

B

C

D

A

B

C

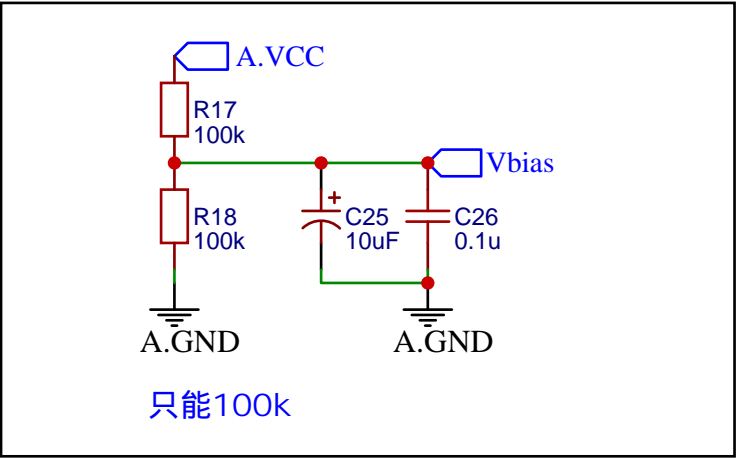
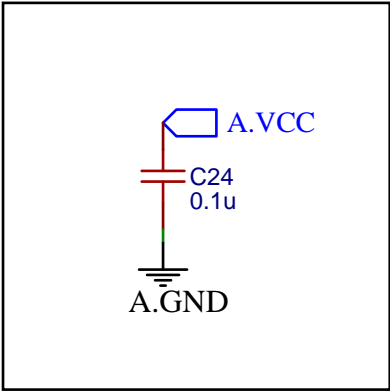
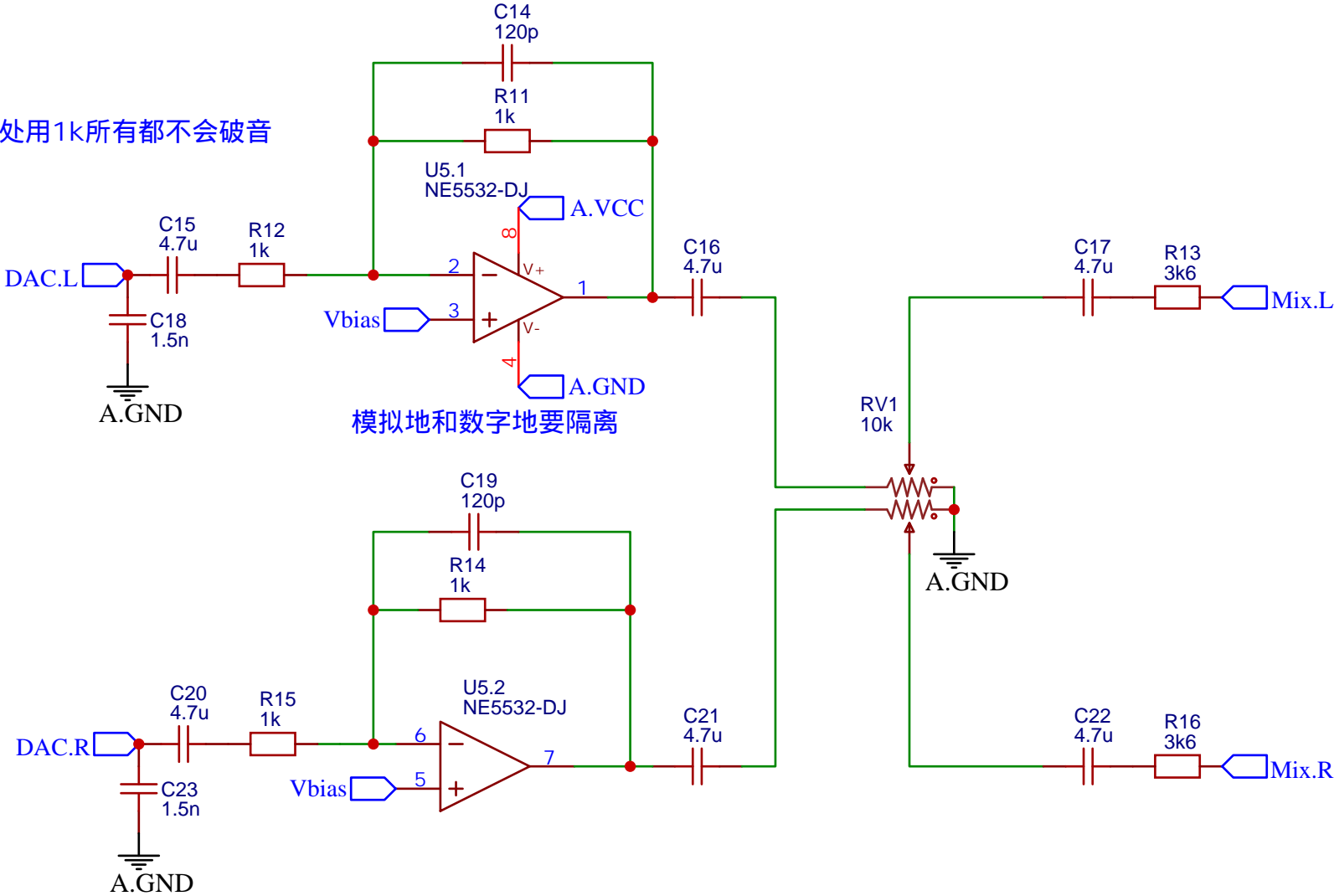
D

此处用1k所有都不会破音

考虑这里要两级放大
x68k的音乐非常容易破音。

模拟地和数字地要隔离

只能100k



TITLE: Sheet_1		REV: 1.0
嘉立创EDA	Company: Your Company	Sheet: 1/1
	Date: 2024-01-08	Drawn By: Denjhang

