

eSim Research Migration Project

Certificate of Participation

This is to certify that

Mr. Krishnendu Roy

from National Institute Of Technology Rourkela

has successfully completed the Research Migration of

Design, Simulation, and Investigation of Basic Logic Gates

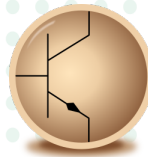
by Using NAND Logic Gate

under eSim Research Migration project.

Prof. Kannan Moudgalya

*Principal Investigator - FOSSEE
IIT Bombay*

y69PR



eSim

An open source EDA tool



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<https://esim.fossee.in/research-migration-project/certificates/verify>