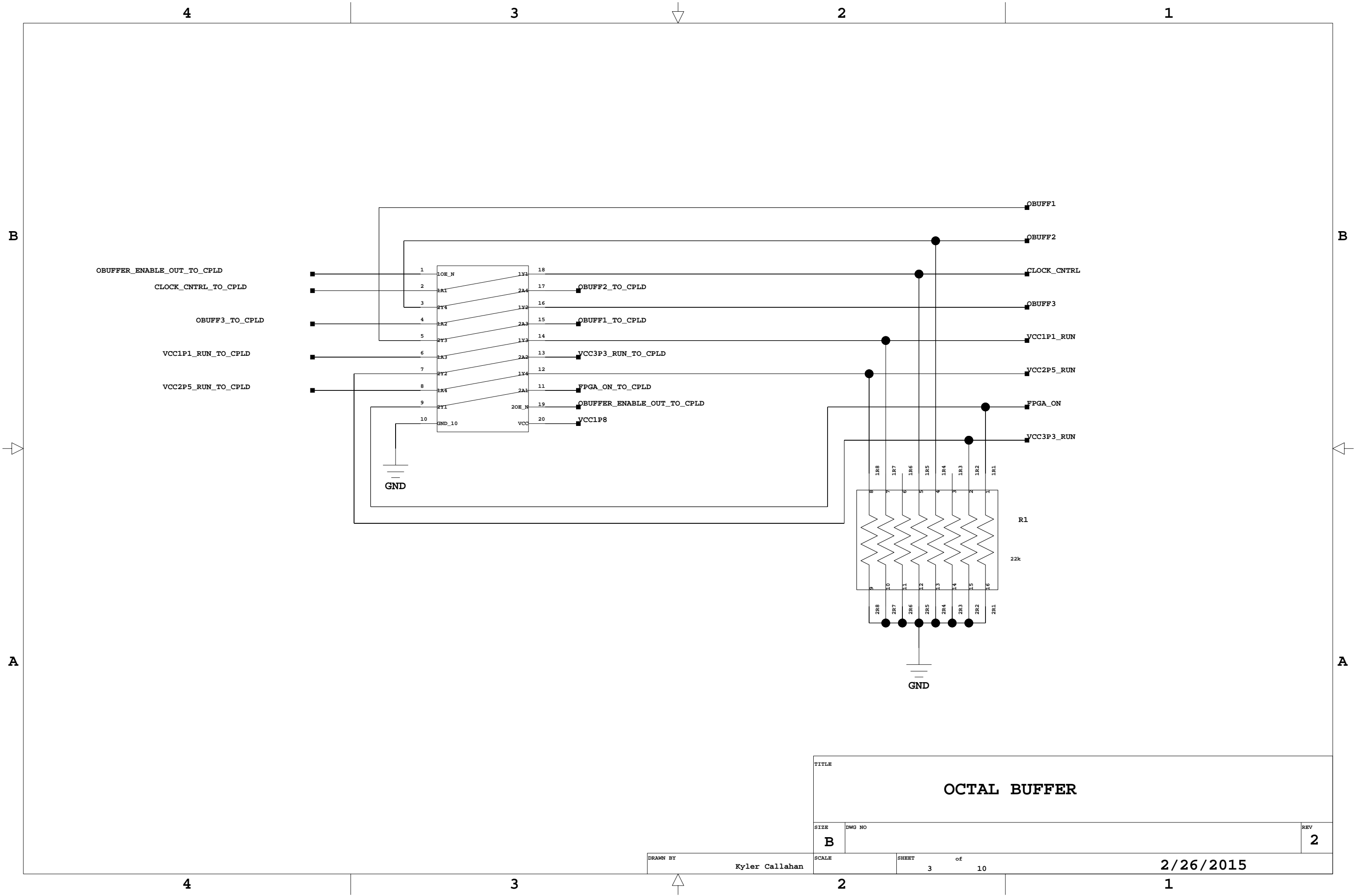
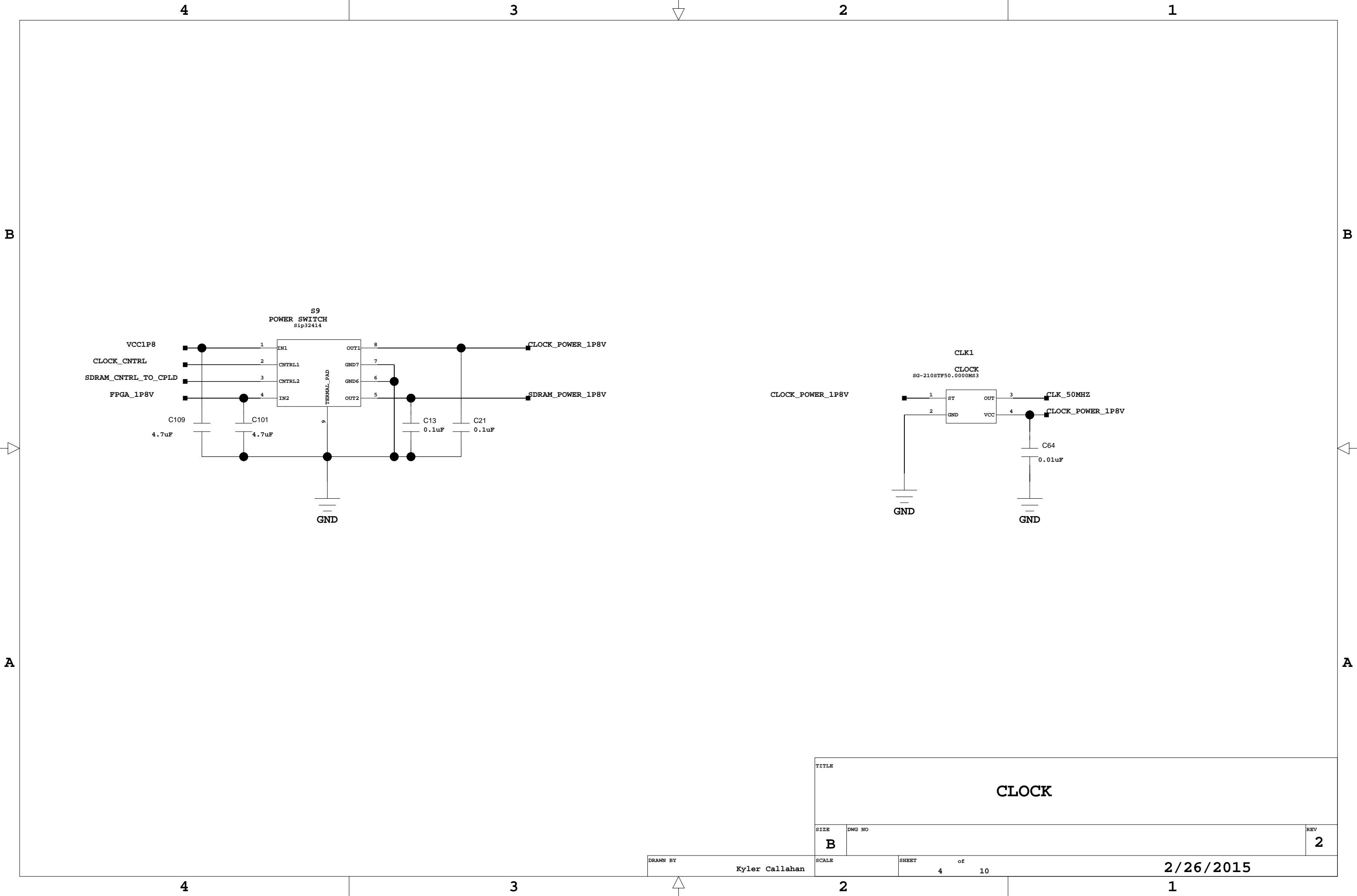


TITLE			
DUAL BUCK WITH BOOST			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	
	2	10	
2/26/2015			



TITLE			
OCTAL BUFFER			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	
	3	10	
2/26/2015			

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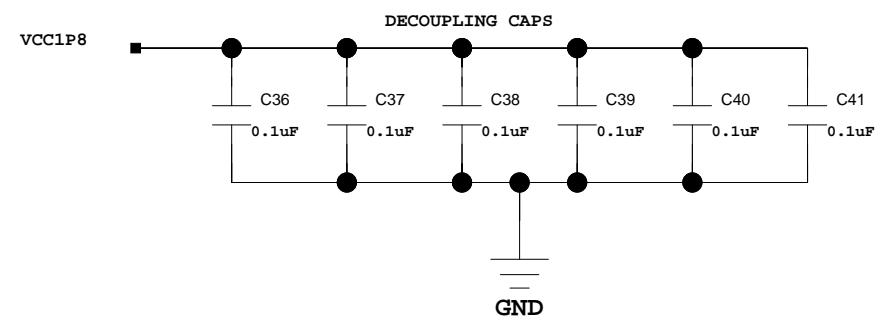
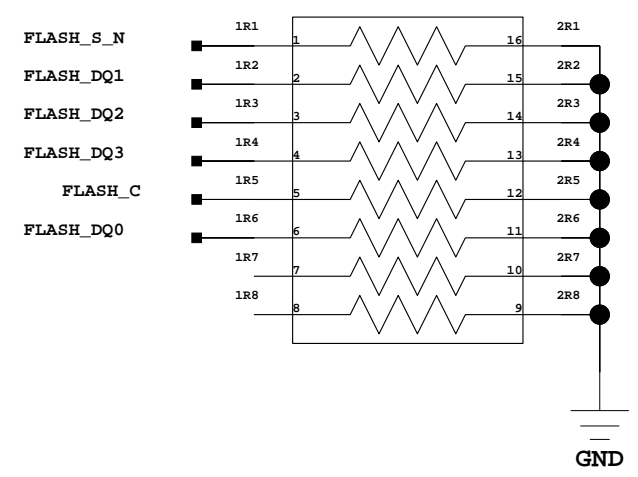
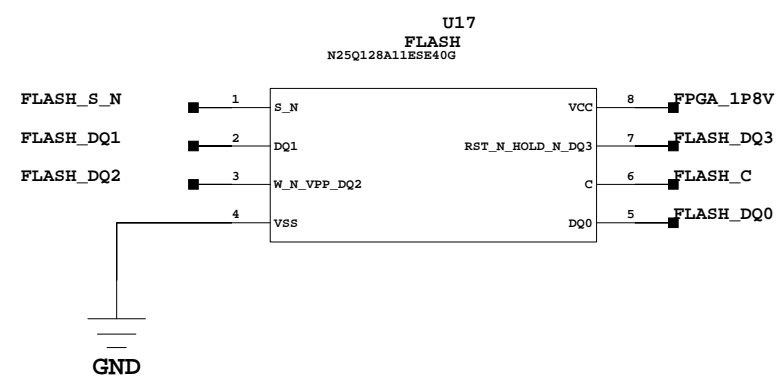


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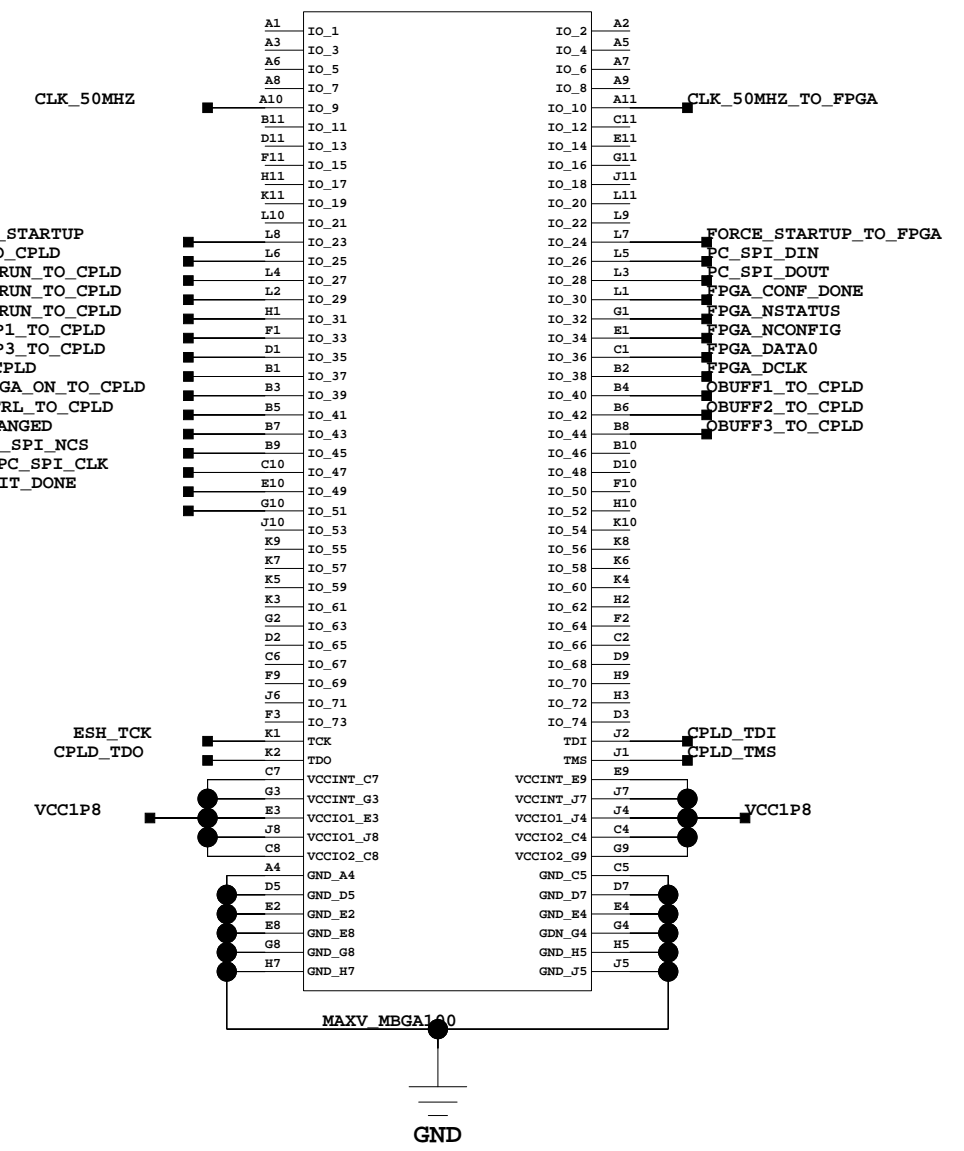
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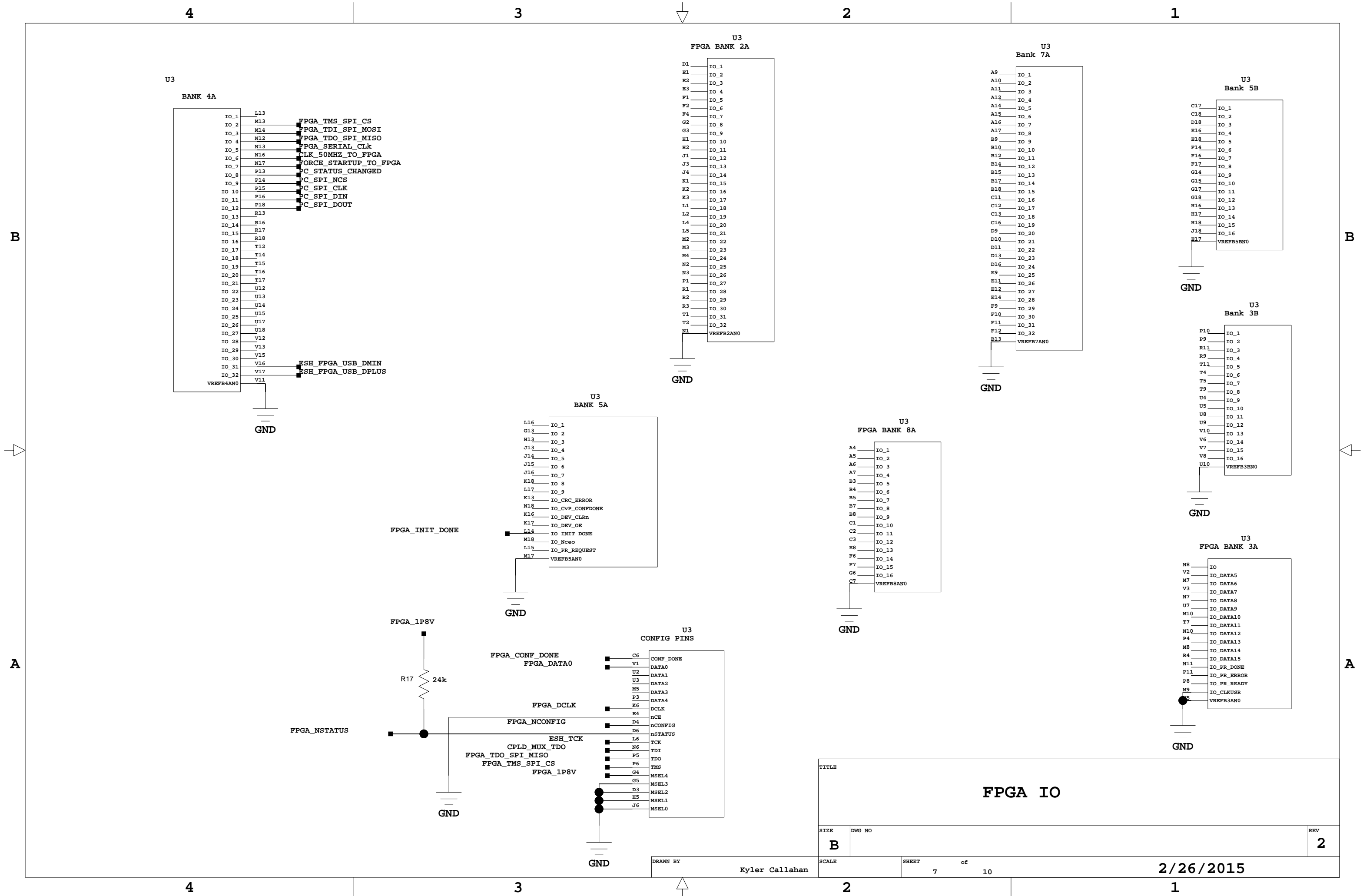
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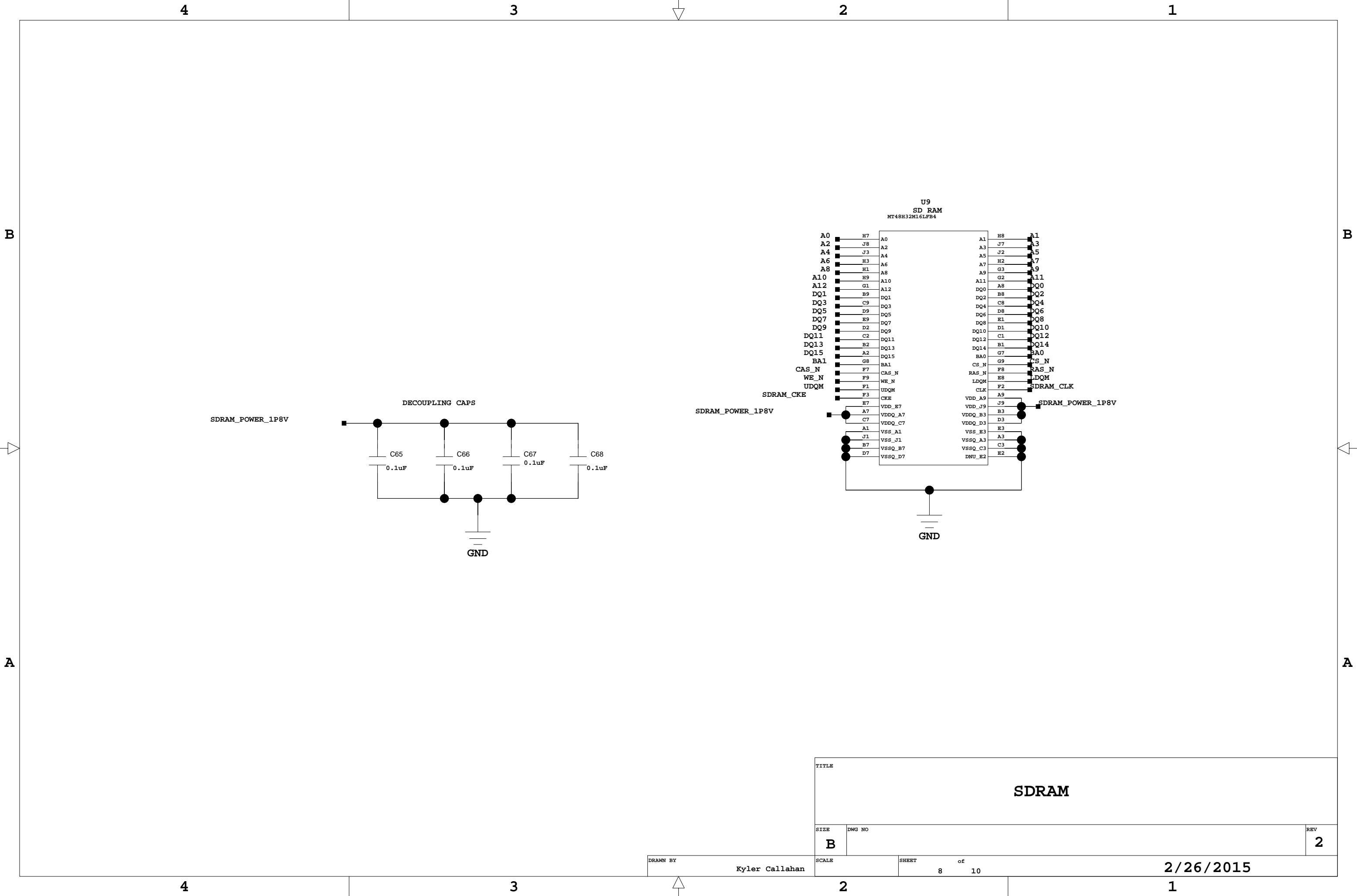
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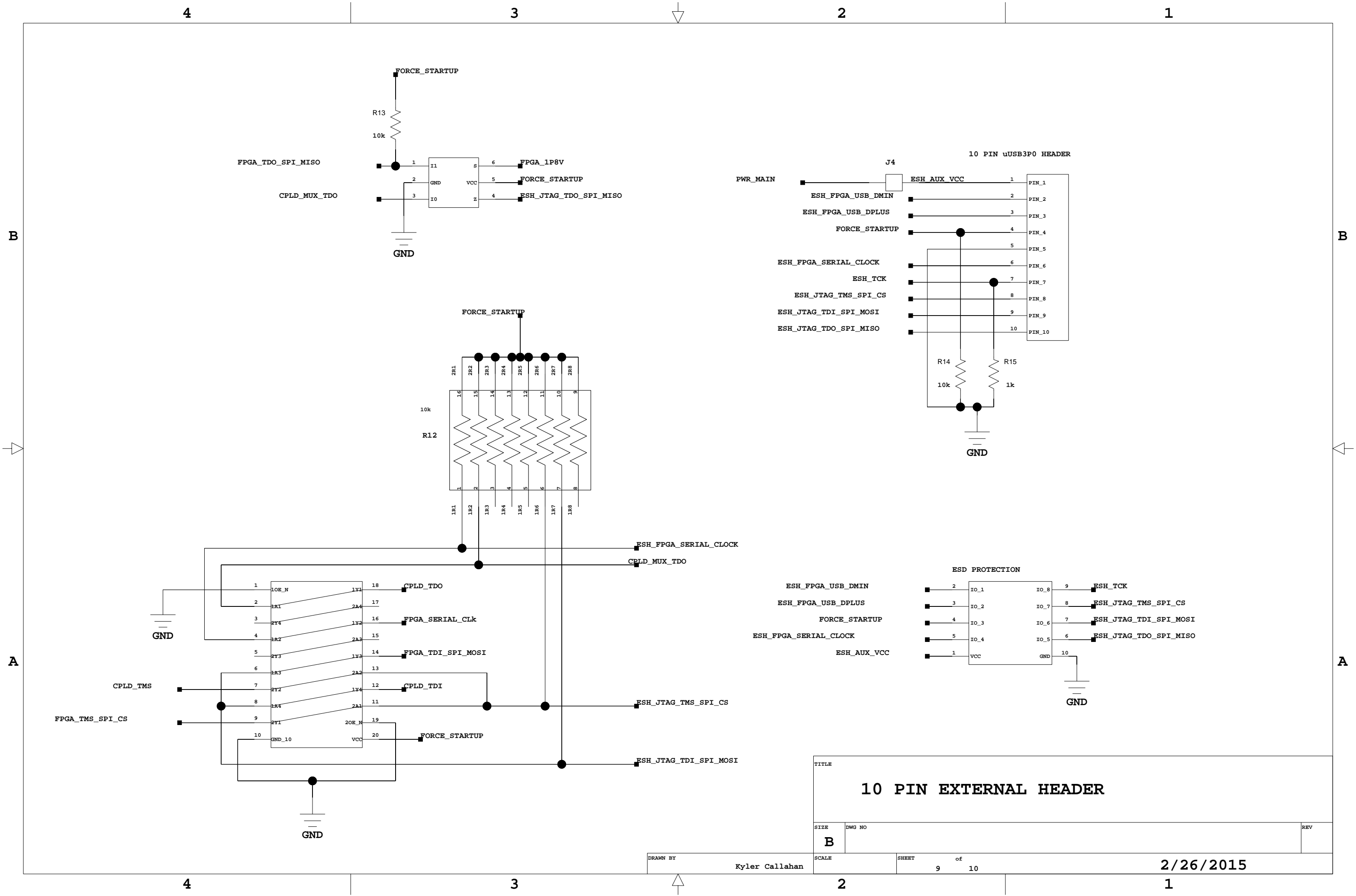


FORCE_STARTUP
BUCK_PWM_TO_CPLD
VCC1P1_RUN_TO_CPLD
VCC2P5_RUN_TO_CPLD
VCC3P3_RUN_TO_CPLD
PWR_GOOD_1P1_TO_CPLD
PWR_GOOD_3P3_TO_CPLD
CLOCK_CNTRL_TO_CPLD
FPGA_ON_TO_CPLD
SDRAM_CNTRL_TO_CPLD
PC_STATUS_CHANGED
PC_SPI_NCS
PC_SPI_CLK
FPGA_INIT_DONE
OBUFFER_ENABLE_OUT_TO_CPLD



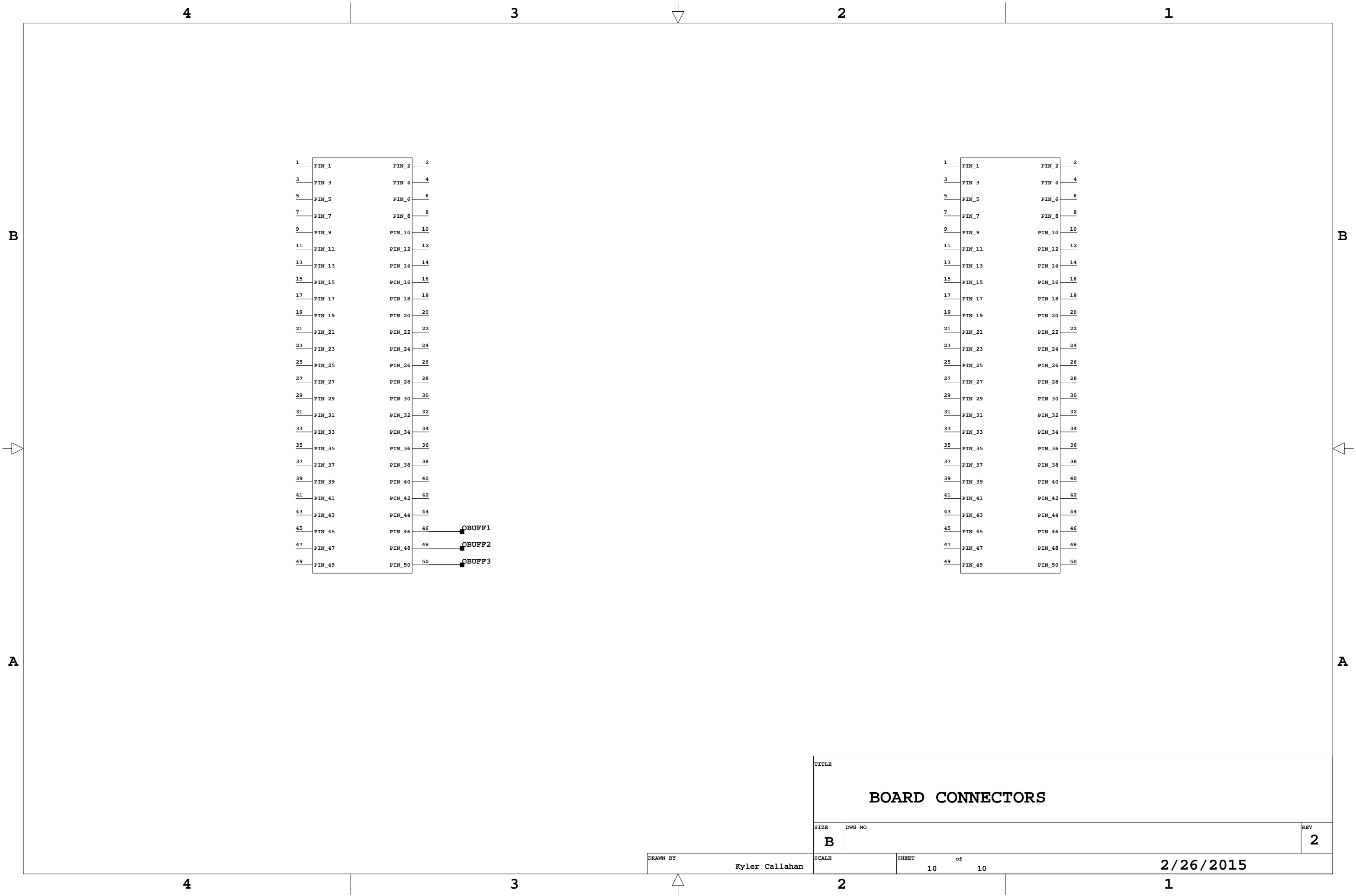






TITLE			
10 PIN EXTERNAL HEADER			
SIZE	DWG NO	REV	
B			
SCALE	SHEET	of	
	9	10	
2/26/2015			

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TITLE			
BOARD CONNECTORS			
SIZE	DWG NO		REV
B			2
SCALE	SHEET	of	2/26/2015
	10	10	

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