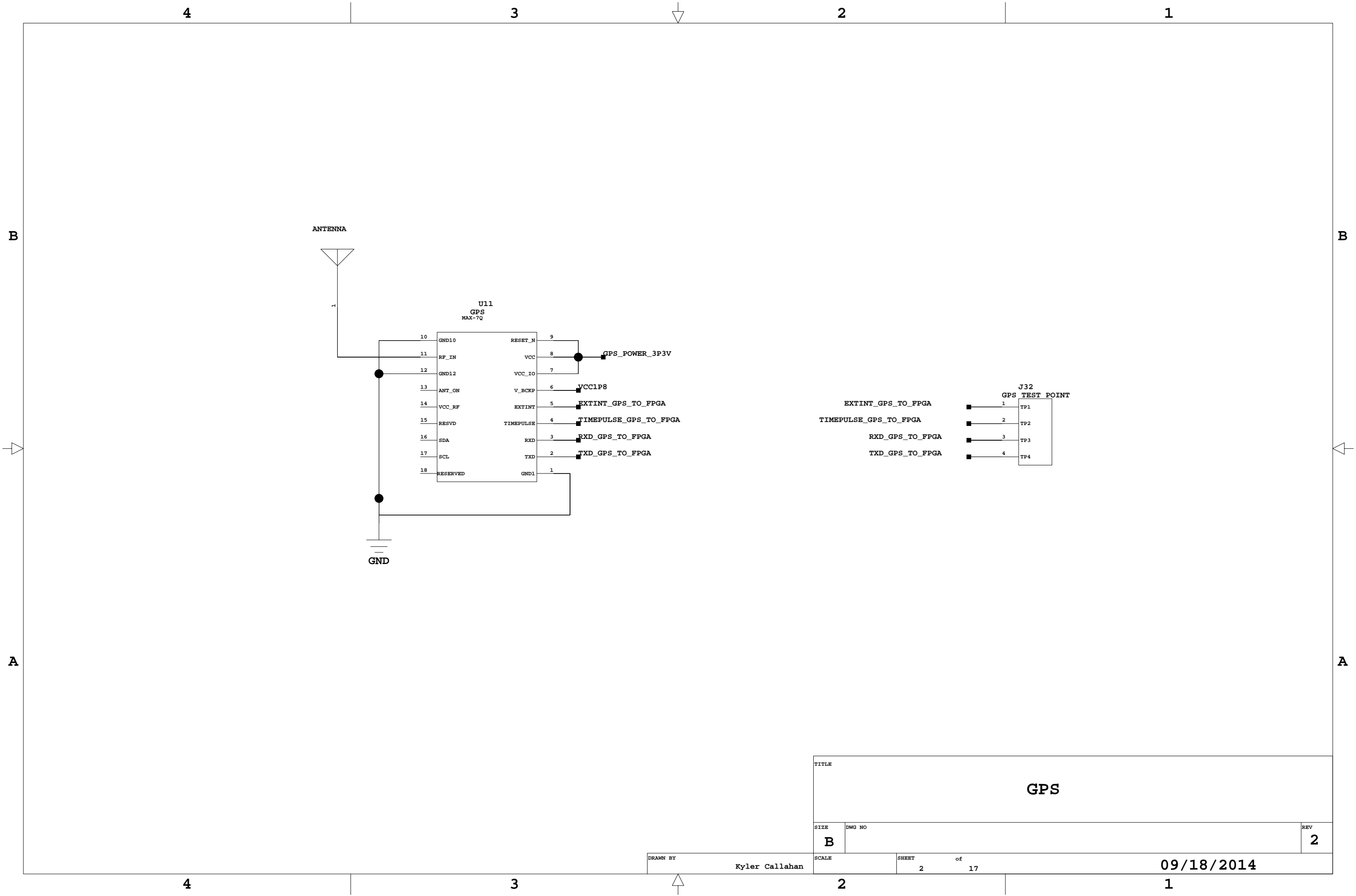


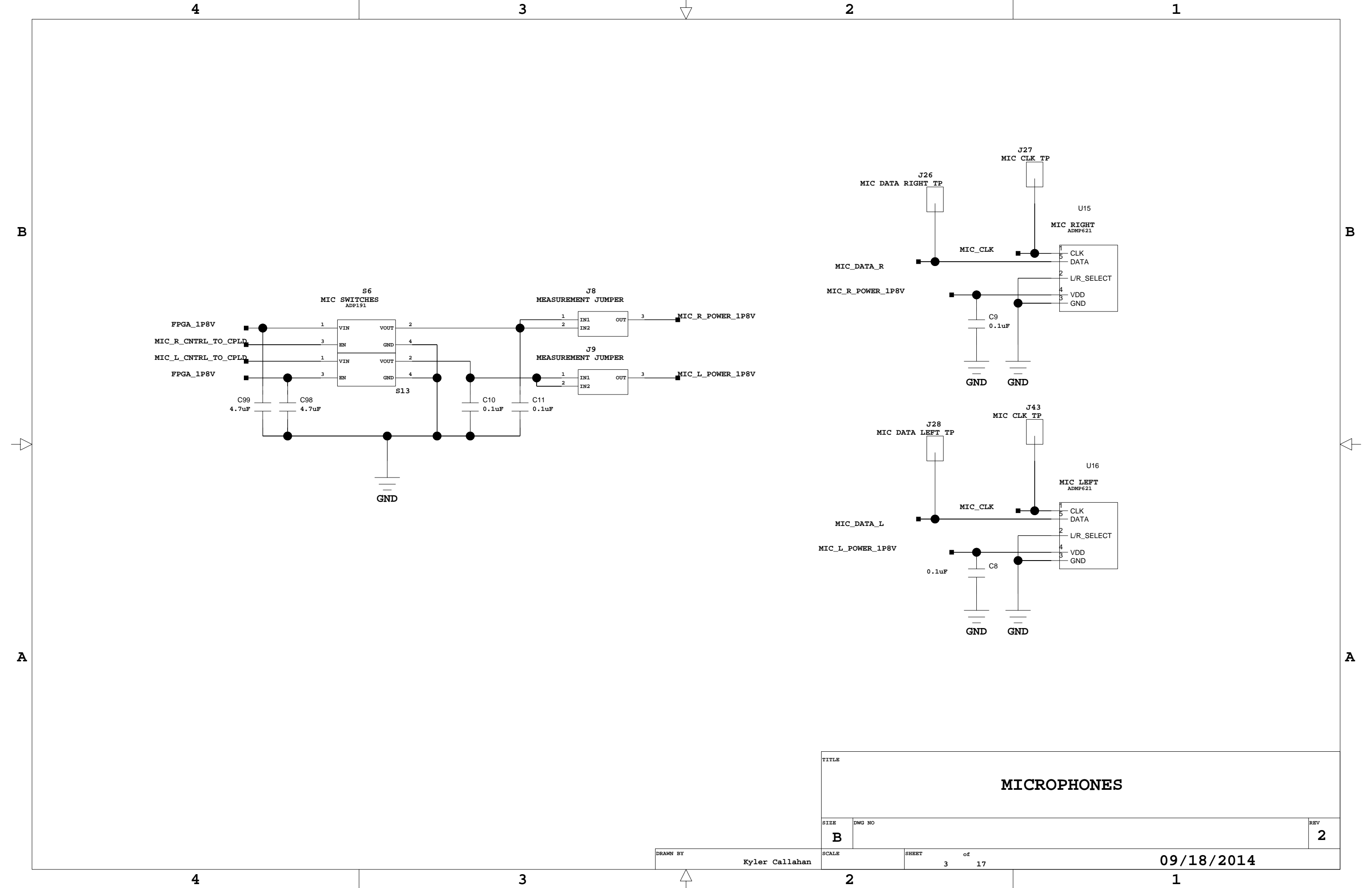
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SCALE	SHEET	of	
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09/18/2014			

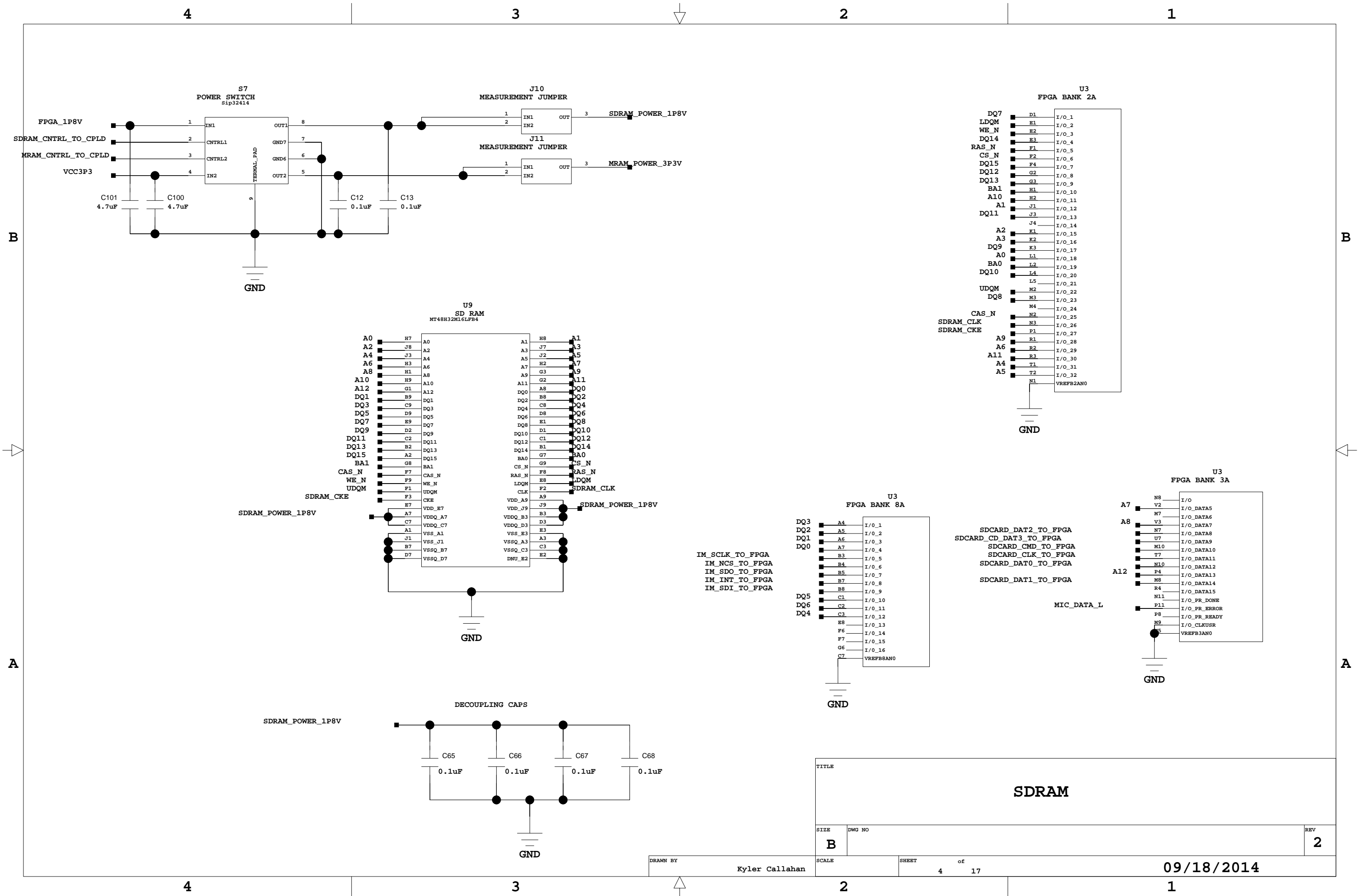
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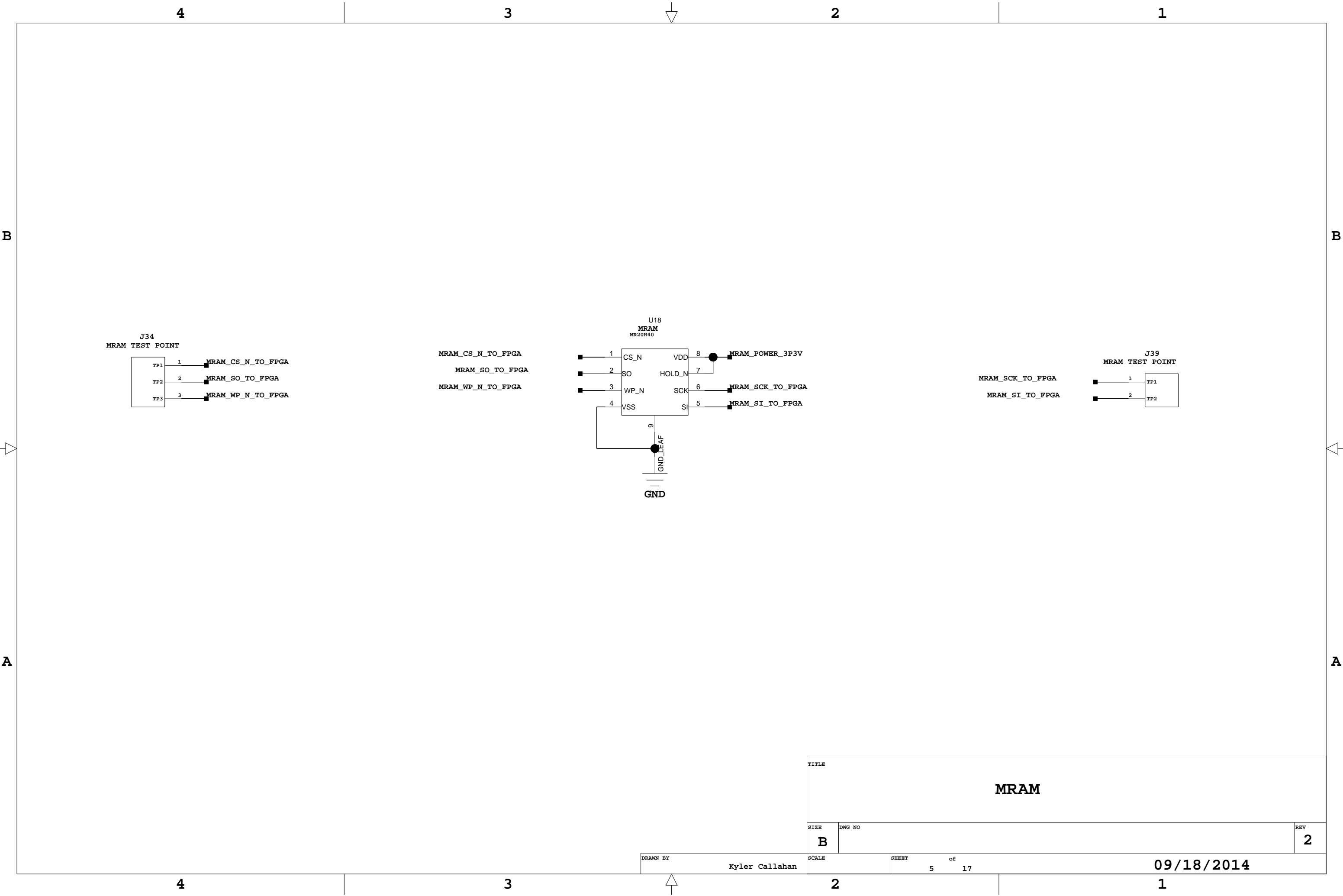


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B						2	
SCALE	SHEET		of	09/18/2014			
	2		17				

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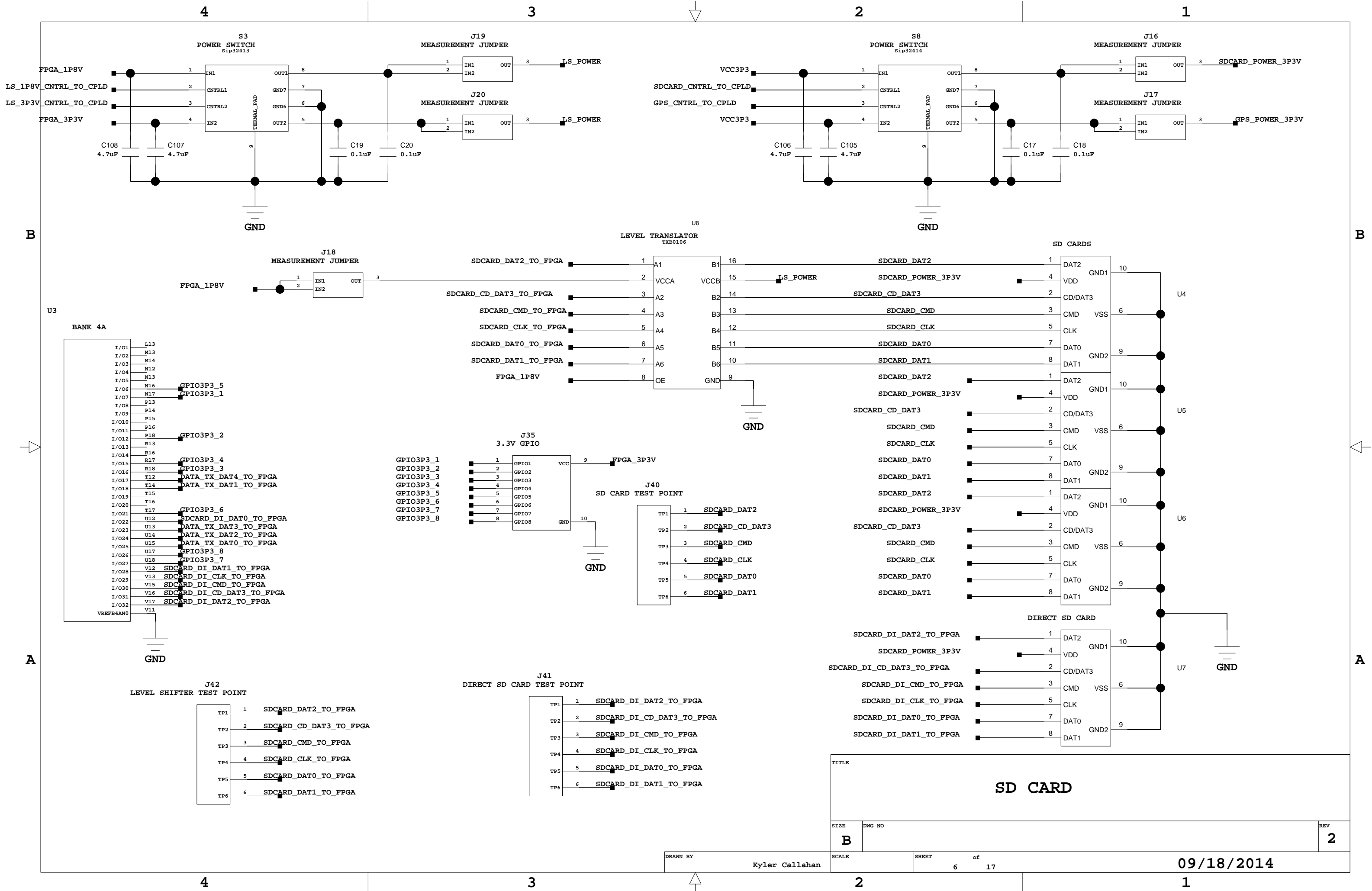


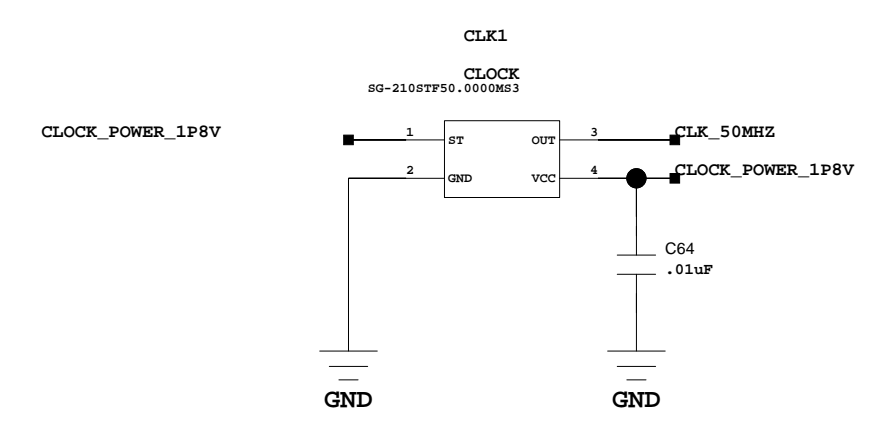
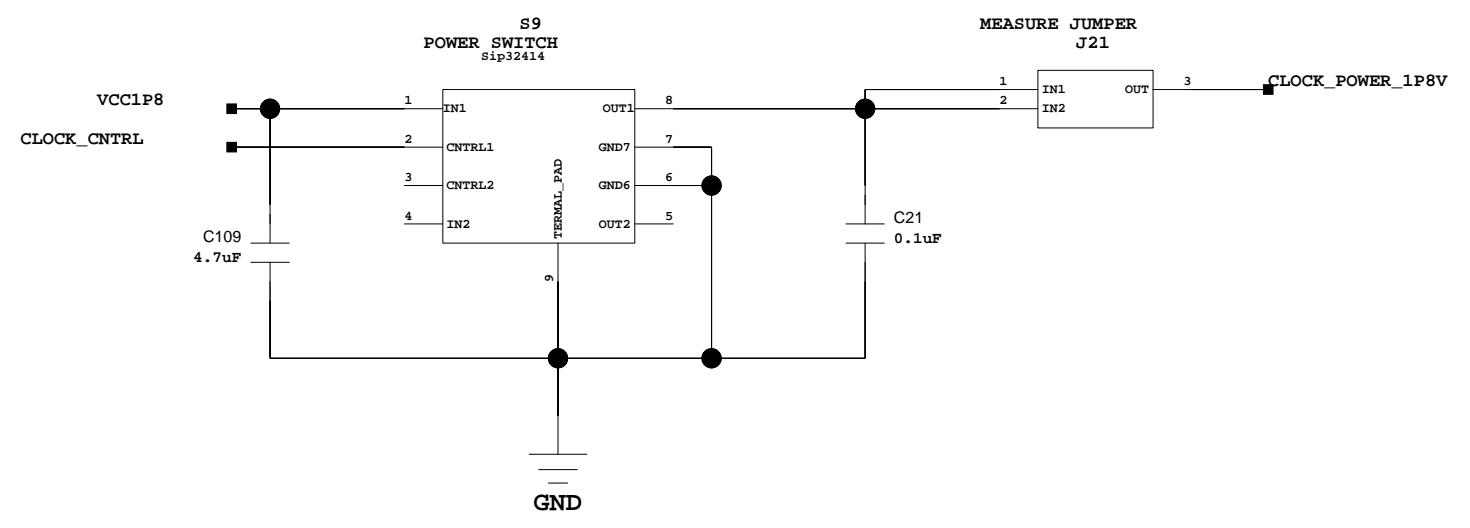




TITLE				MRAM			
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B							
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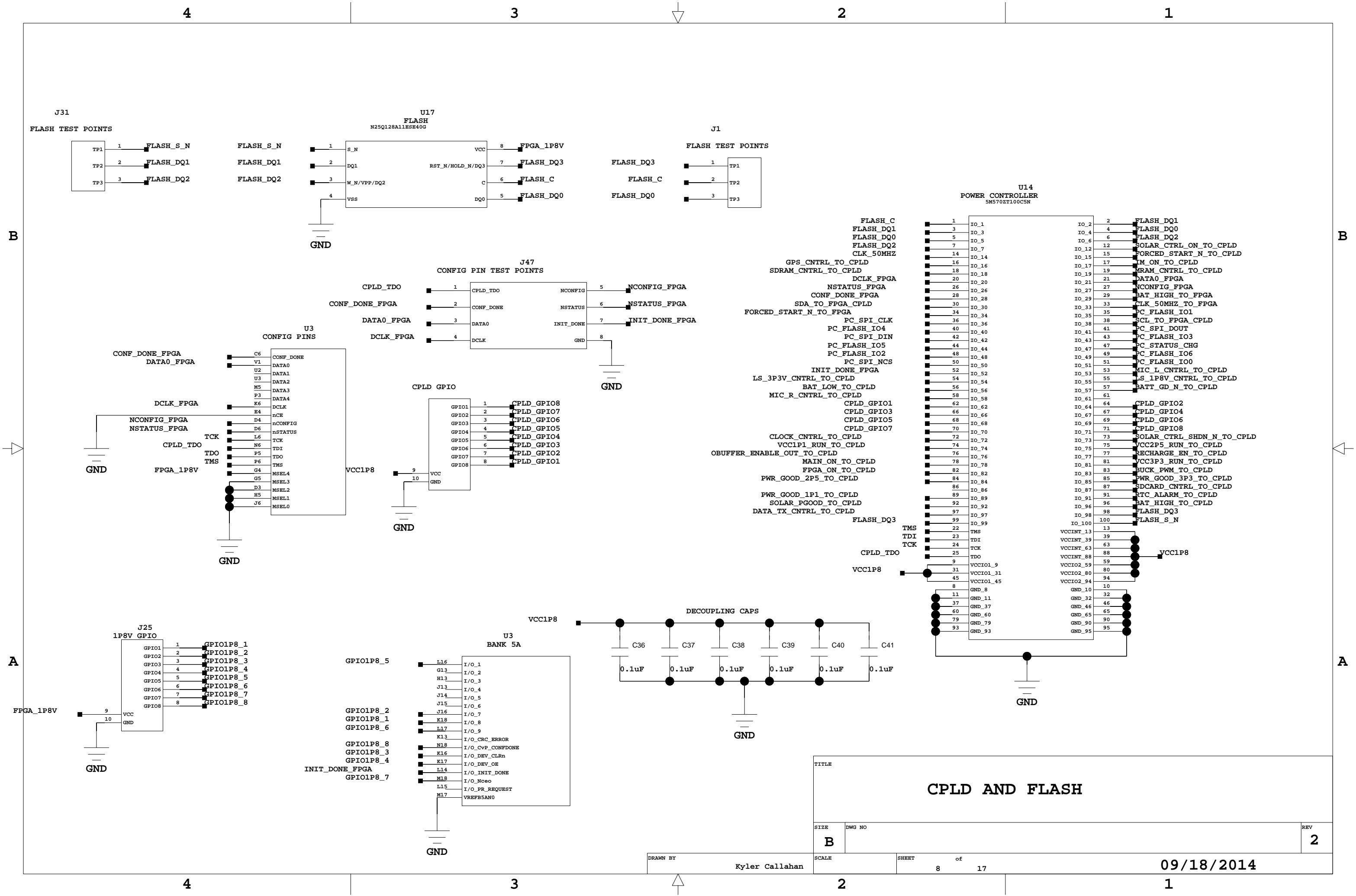
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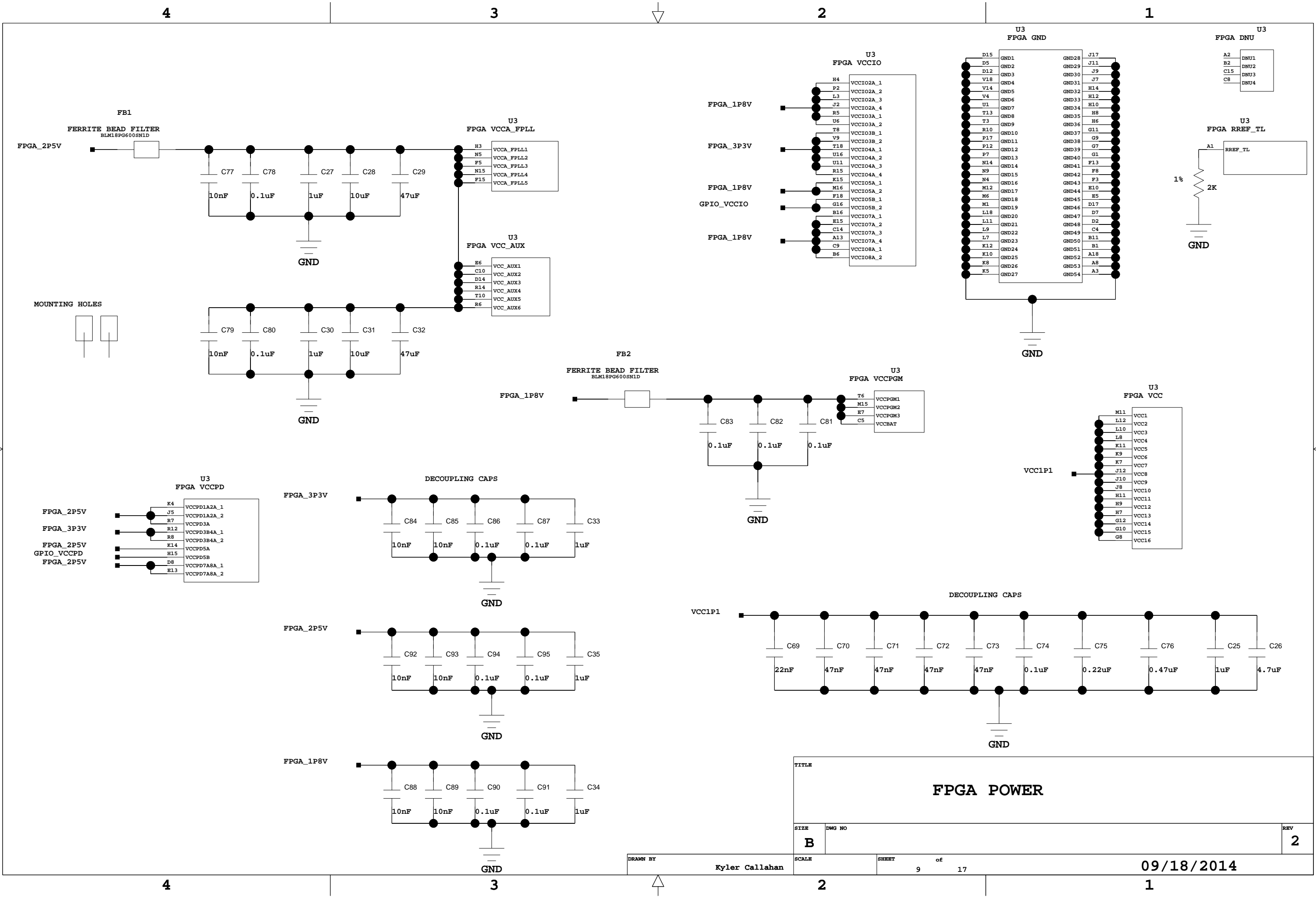
TITLE			
CLOCK			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	09/18/2014
	7	17	

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TITLE			
CPLD AND FLASH			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	
	8	17	
09/18/2014			

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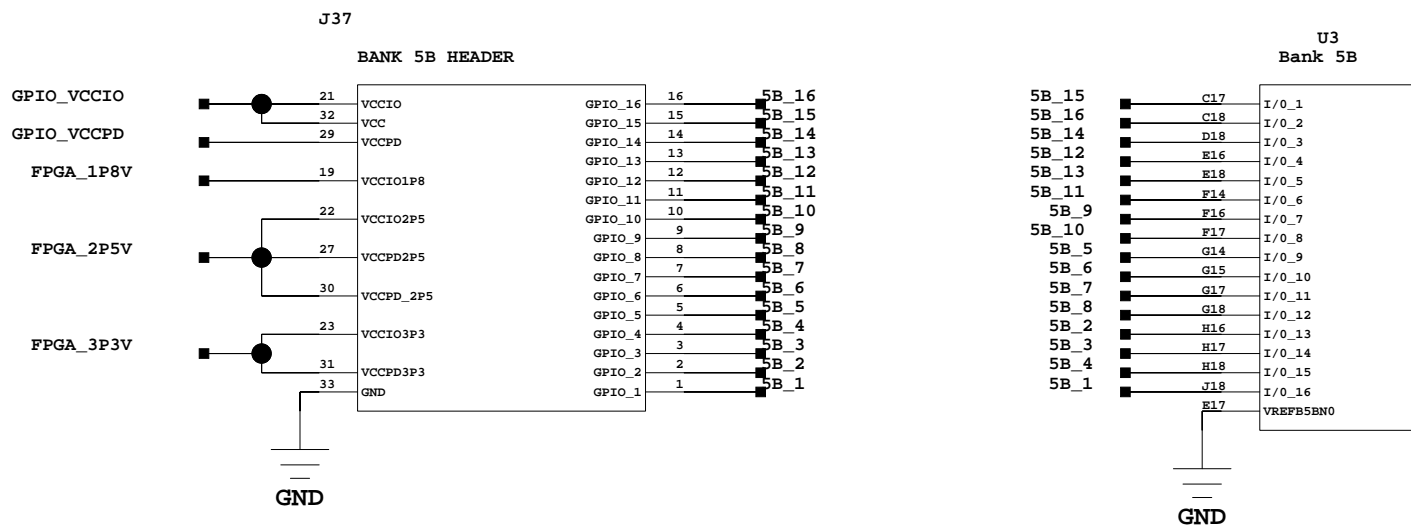


TITLE			
FPGA POWER			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	DATE
	9	17	09/18/2014

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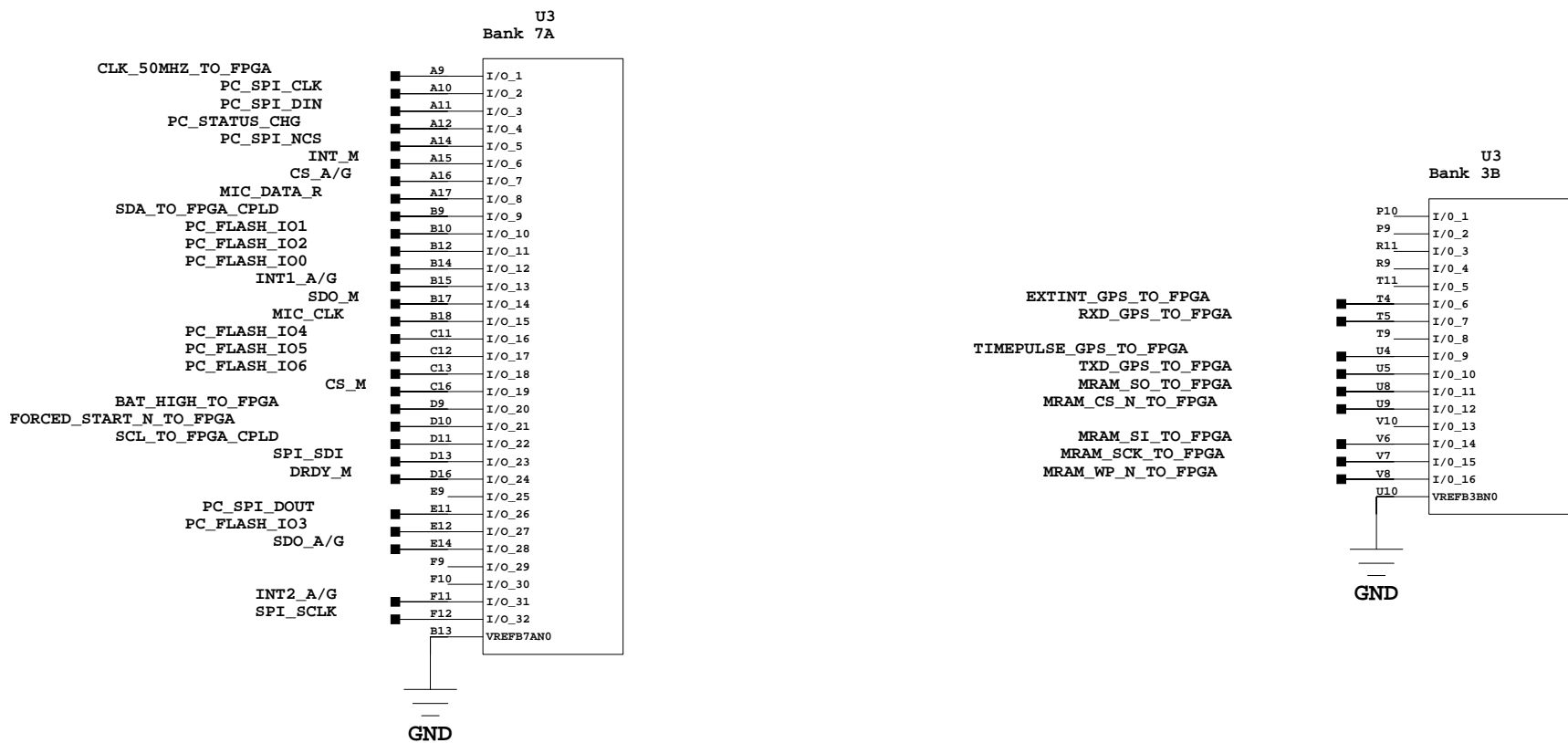
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B



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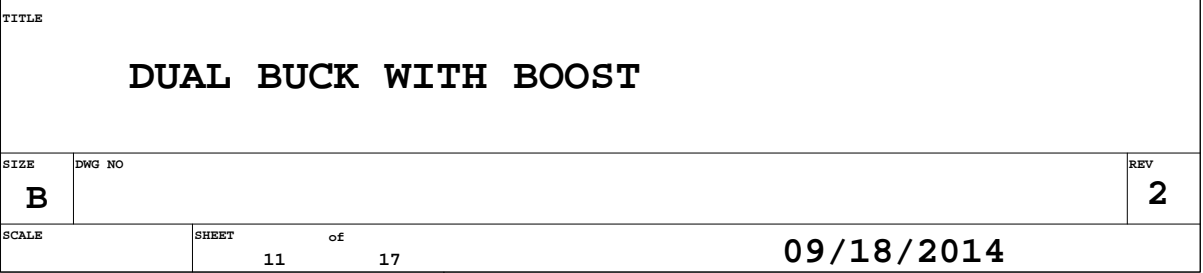
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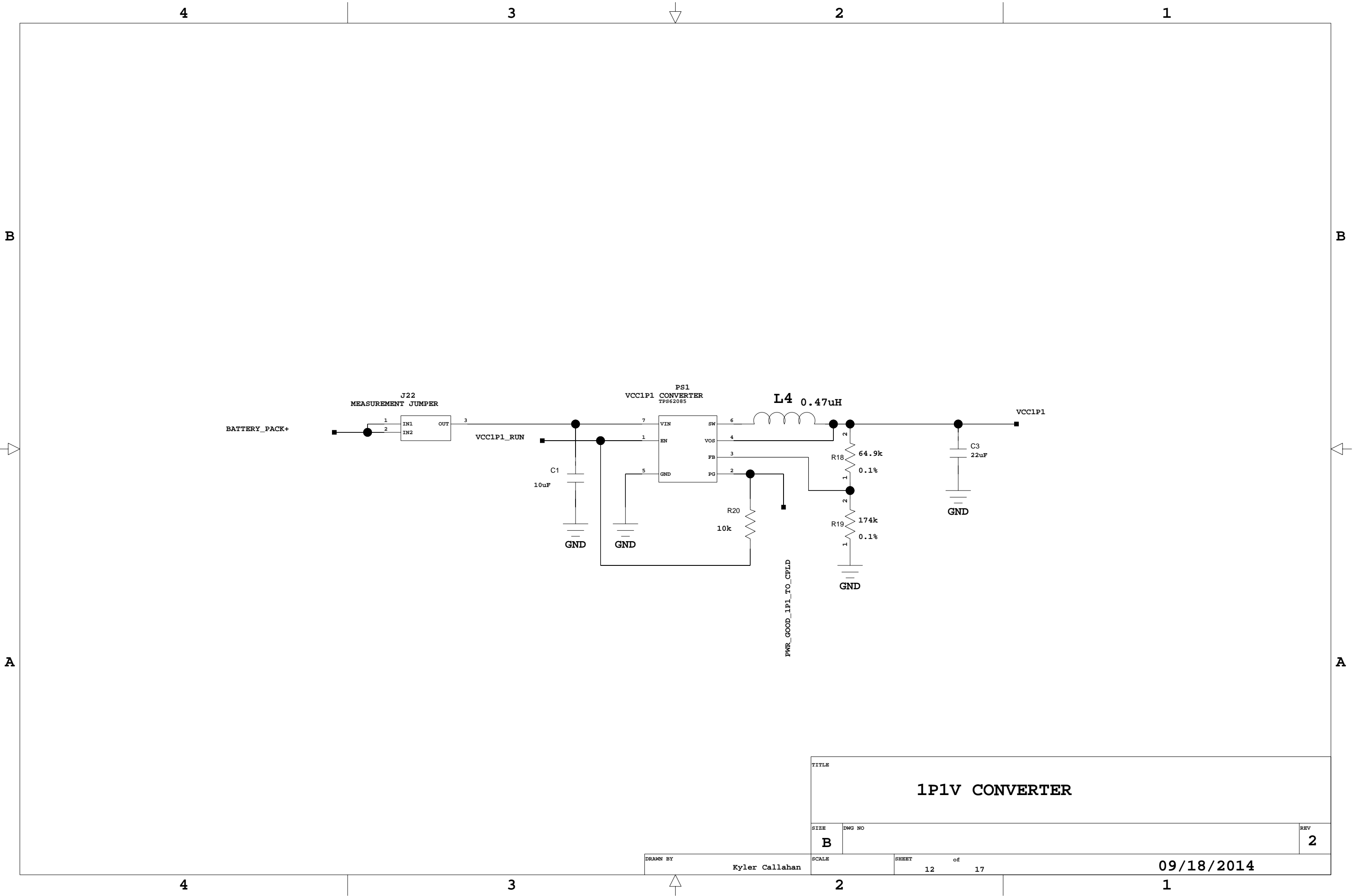


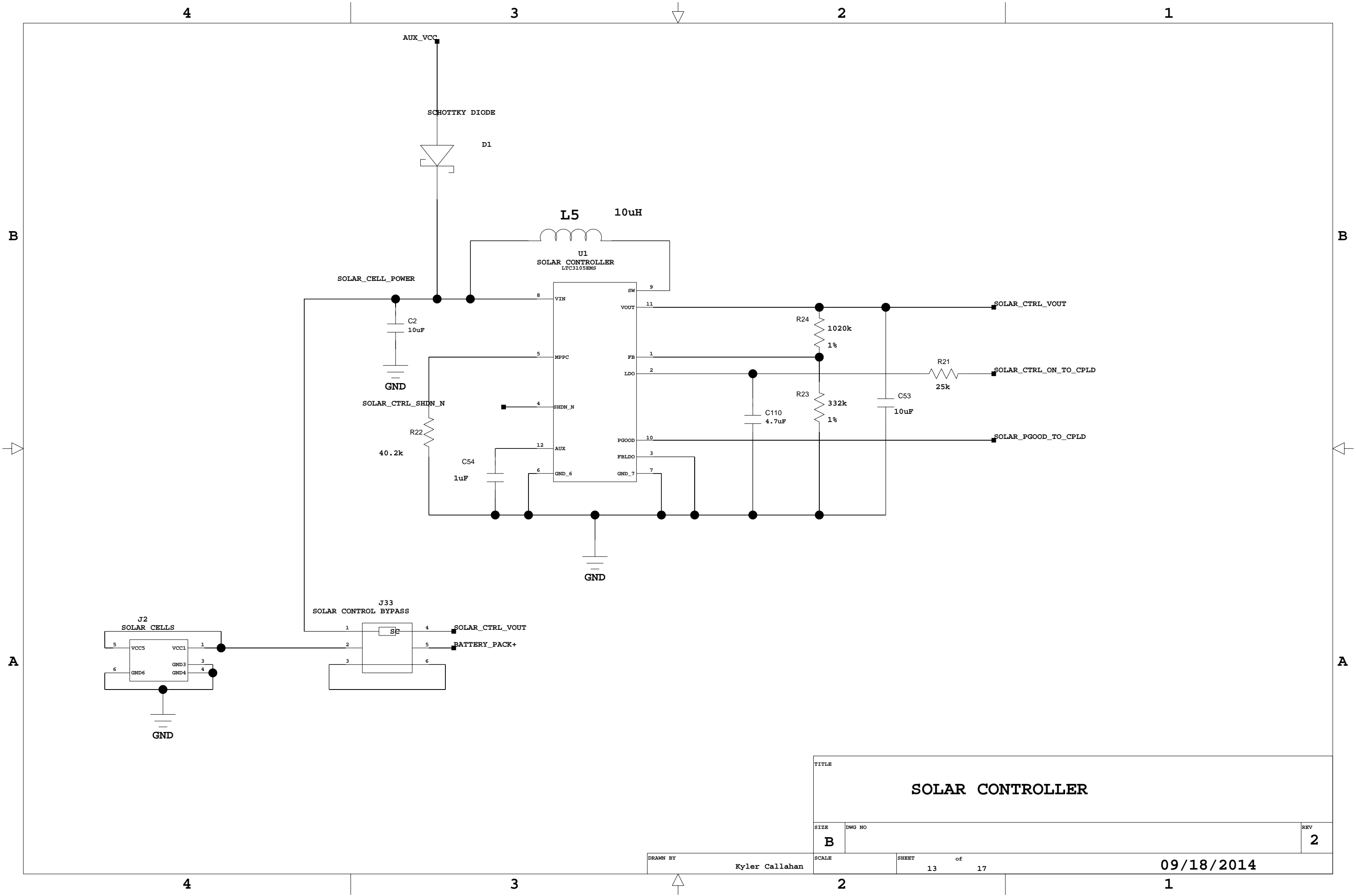
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A

TITLE			
FPGA I/O			
SIZE	DWG NO		REV
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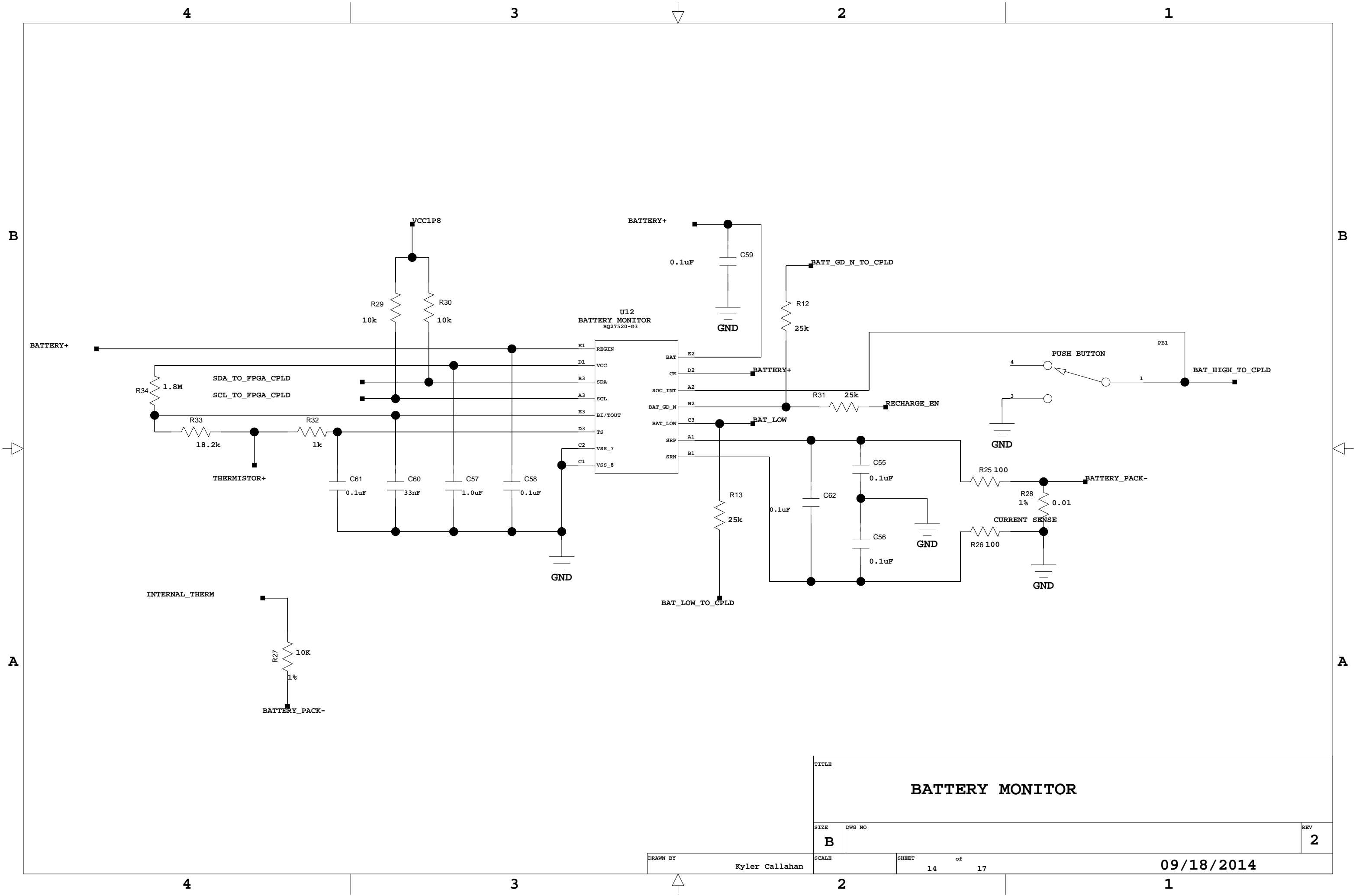






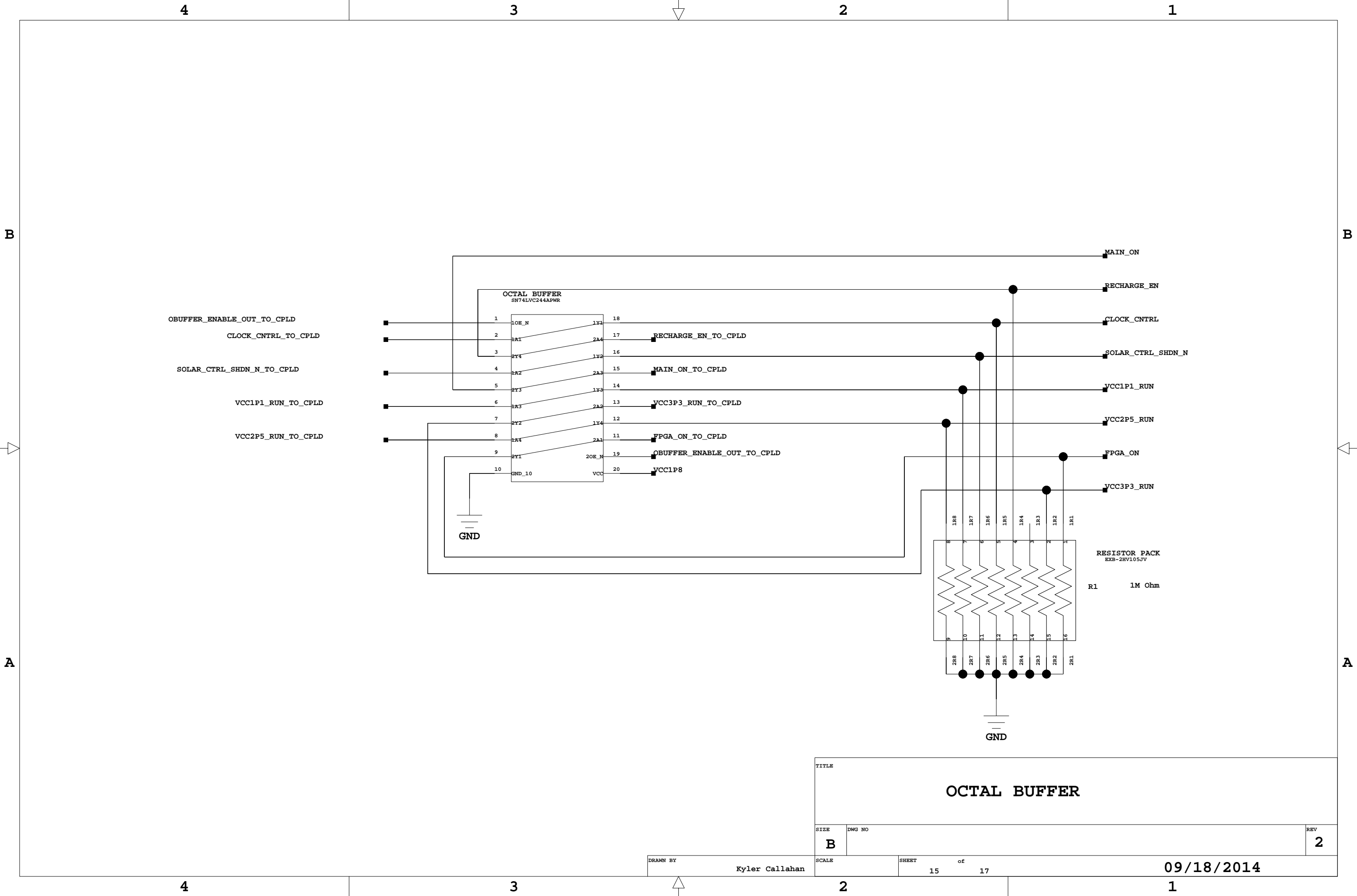
TITLE			
SOLAR CONTROLLER			
SIZE	DWG NO	REV	
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SCALE	SHEET	of	DATE
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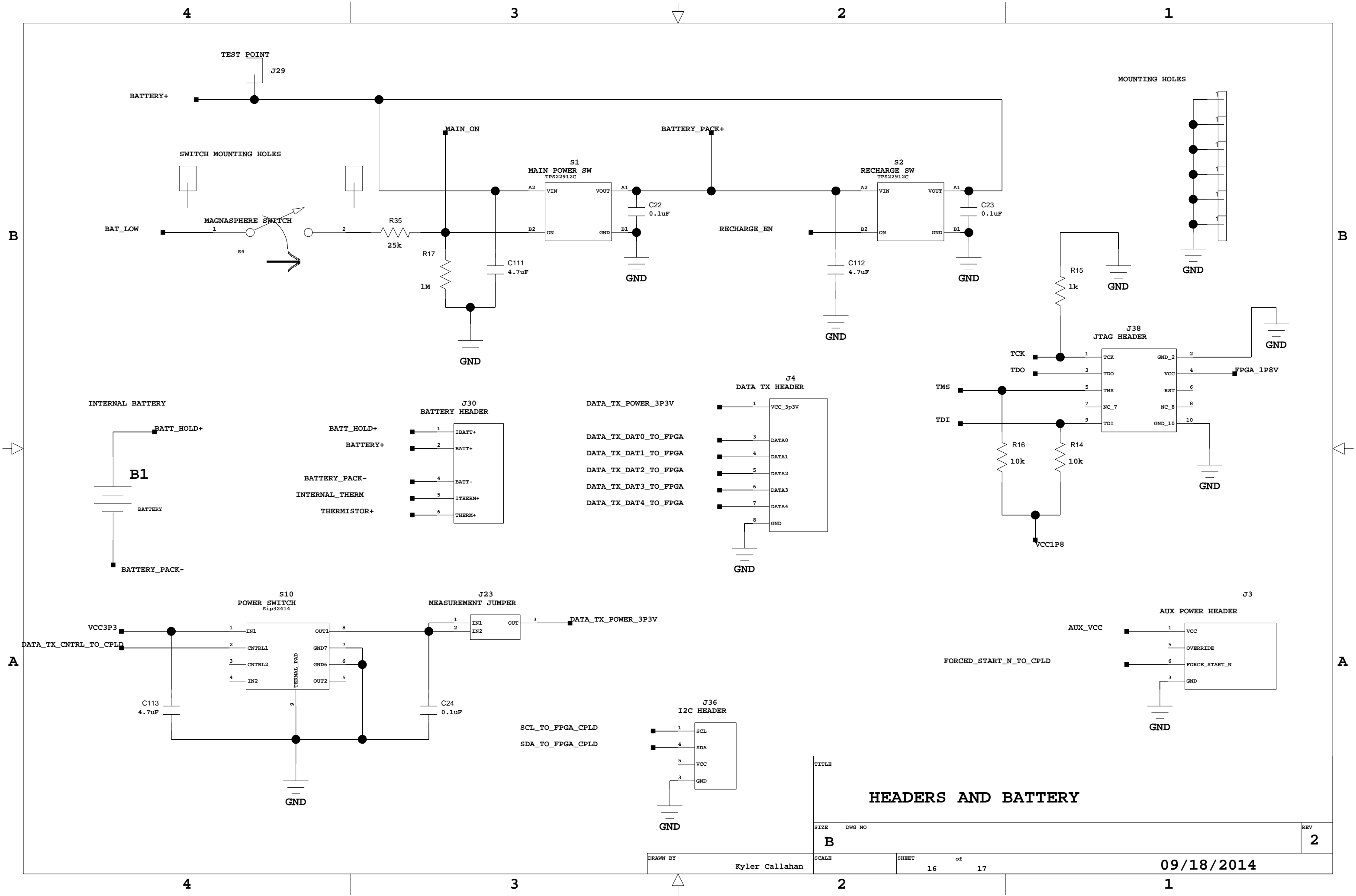
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TITLE			
BATTERY MONITOR			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	09/18/2014
	14	17	

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TITLE			
HEADERS AND BATTERY			
SIZE	DWG NO	REV	
B		2	
SCALE	SHEET	of	DATE
	16	17	09/18/2014

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