## **Processor Performance:**

## Below are the values taken from screenshots present in another document

Wor	Concurren	CPUBen	MatrixBen	HPL	Theoretic	CPUBen	MatrixBen	HPL
k -	су	ch	ch	Measur	al	ch	ch	Efficien
load		Measure	Measured	ed	Ops/Sec	Efficienc	Efficiency	cy (%)
		d	Ops/Sec	Ops/Sec	(GigaOPS	y (%)	(%)	
		Ops/Sec	(GigaOPS)	(GigaOP	)			
		(GigaOP		S)				
		S)						
SP	1	1.21	1.8	NA	2.3	52	78	NA
SP	2	1.30	3.1	NA	4.6	28	67	NA
SP	4	1.34	7.7	NA	9.2	14	83	NA
SP	8	1.31	7.5	NA	9.2	14	81	NA
DP	1	0.98	0.8	1.9	2.3	42	34	82
DP	2	1.17	1.3	3.5	4.6	25	28	76
DP	4	1.26	3.9	3.7	9.2	13	42	40
DP	8	1.31	3.3	4.3	9.2	14	35	46