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🌀 [rliu-swe/code](https://github.com/rliu-swe/code)

ALL PUBLICATIONS (GITHUB DIRECTORY [↗](#))

Ph.D. Dissertation: Automatic Tuning of Digital Circuits PDF ↗	05-2011
• <i>Ph.D. Advisors:</i> Professors David Blaauw & Dennis Sylvester	
In-situ delay monitor for processors using 5ps time-to-digital converter ↗ Int'l Solid-State Circuits Conference	02-2010
A low-voltage processor for sensing applications with picowatt standby ↗ Journal of Solid-State Circuits	04-2009
Centip3De: 3930DMIPS/W Near-V_{th} 3D system, 64 ARM Cortex-M3 cores ↗ Int'l Solid-State Circuits Conf	02-2012
Centip3De: Many-Core Prototype of 3D Integ. & Near-Threshold Computing ↗ Communications of the ACM	11-2013
Centip3De: A 64-Core, 3D Stacked Near-Threshold System ↗ IEEE Micro	01-2013
Centip3De: Cluster NTC Arch. w/ 64 ARM Cores in 3D-Stacked CMOS ↗ Journal of Solid-State Circuits	12-2012
A true random number generator using time-dependent dielectric breakdown ↗ Symposium on VLSI Circuits	06-2011
OxID: On-Chip One-Time Random ID Generation using Oxide Breakdown ↗ Symposium on VLSI Circuits	06-2010
The Phoenix Processor: A 30pW platform for sensor applications ↗ Symposium on VLSI Circuits	06-2008

PATENTS (GITHUB DIRECTORY [↗](#))

Randomized Value Generation PDF ↗ Google Patents ↗	07-2015
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