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🌀 [rliu-swe](https://github.com/rliu-swe)

🌀 [rliu-swe/code](https://github.com/rliu-swe/code)

## ALL PUBLICATIONS (GITHUB DIRECTORY [↗](#))

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<b>Ph.D. Dissertation: Automatic Tuning of Digital Circuits</b> <a href="#">PDF</a> <a href="#">↗</a>	<b>05-2011</b>
• <i>Ph.D. Advisors:</i> Professors David Blaauw & Dennis Sylvester	
<b>In-situ delay monitor for processors using 5ps time-to-digital converter</b> <a href="#">↗</a>   Int'l Solid-State Circuits Conference	<b>02-2010</b>
<b>A low-voltage processor for sensing applications with picowatt standby</b> <a href="#">↗</a>   Journal of Solid-State Circuits	<b>04-2009</b>
<b>Centip3De: 3930DMIPS/W Near-<math>V_{th}</math> 3D system, 64 ARM Cortex-M3 cores</b> <a href="#">↗</a>   Int'l Solid-State Circuits Conf	<b>02-2012</b>
<b>Centip3De: Many-Core Prototype of 3D Integ. &amp; Near-Threshold Computing</b> <a href="#">↗</a>   Communications of the ACM	<b>11-2013</b>
<b>Centip3De: A 64-Core, 3D Stacked Near-Threshold System</b> <a href="#">↗</a>   IEEE Micro	<b>01-2013</b>
<b>Centip3De: Cluster NTC Arch. w/ 64 ARM Cores in 3D-Stacked CMOS</b> <a href="#">↗</a>   Journal of Solid-State Circuits	<b>12-2012</b>
<b>A true random number generator using time-dependent dielectric breakdown</b> <a href="#">↗</a>   Symposium on VLSI Circuits	<b>06-2011</b>
<b>OxID: On-Chip One-Time Random ID Generation using Oxide Breakdown</b> <a href="#">↗</a>   Symposium on VLSI Circuits	<b>06-2010</b>
<b>The Phoenix Processor: A 30pW platform for sensor applications</b> <a href="#">↗</a>   Symposium on VLSI Circuits	<b>06-2008</b>