

Fig-01: Output for Circuit 01

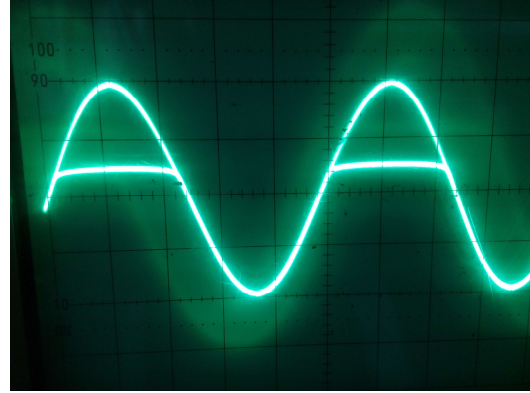


Fig 03: Output for Circuit 03

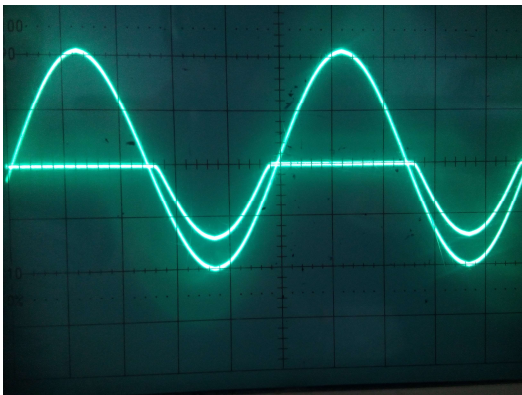


Fig-02: Output for Circuit 02

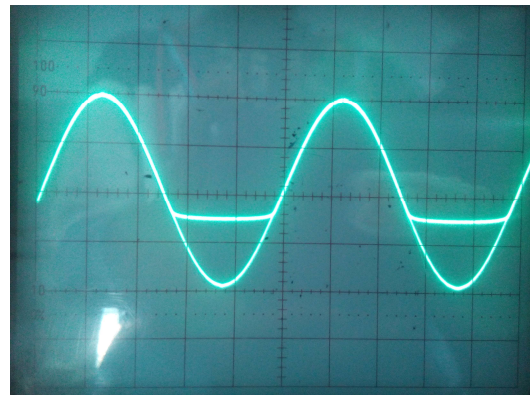


Fig 04: Output for Circuit 04