

Controller Design in MATLAB® Level 3 Control and Signal Processing Z0996690 March 4, 2018



#### **Abstract**

This report describes the design of two controllers for an industrial plant process in Matlab. Each controller was designed to meet a different performance specification.

[s]

## **Nomenclature**

- s Laplace domain variable  $[rad s^{-1}]$
- t Time domain variable [s]
- $\omega_n$  Natural frequency
- $\zeta$  Damping factor
- G Industrial plant process transfer function
- $G_c$  Controller transfer function
- $\mathcal{L}$  Laplace transform operator
- $M_0$  Peak overshoot [%]
- SSE Steady state error
- $T_r$  Eighty percent rise time
- $T_s$  Two percent settling time

Percentage overshoot  $M_0$  is the the peak response in proportion to the final output [rad s<sup>-1</sup>] value and is soley dependent on the damping factor  $\zeta$  for second order systems:

$$M_0 = \exp\left(-\frac{\pi\zeta}{\sqrt{1-\zeta^2}}\right)^* \tag{1}$$

The settling time  $T_s$  of the controller is the time for the output oscillations to decay to within some margin of the final value: 2% is used in this report. Settling time is also dependent on the natural frequency  $\omega_n$ :

$$T_s = -\frac{\ln{(2\%)}}{\zeta\omega_n} \approx \frac{4}{\zeta\omega_n}^*$$
 (2)

The rise time of the controller is the time for the output to transition from 10% to 90% of the final value:

$$T_r = \frac{\ln(90\%/10\%)}{\zeta\omega_n} \approx \frac{2.2}{\zeta\omega_n}^* \tag{3}$$

These three parameters are typically specified as upper bounds and constitute the dynamic specification.

In controller design there is a tradeoff between the dynamic specification and the steady state error. The steady state error is defined as follows:

$$SSE := \lim_{t \to \infty} e(t)$$
 (4)

One of the most common types of controller in industry is the PID controller, which can be easily implemented using microcontrollers.

## 2 Background

Figure 1 illustrates a closed loop system. The controller is designed to drive the output y(s) of the plant process to the desired output r(s) by minimising the error e(s).



FIGURE 1: Block diagram of the closed loop system.

For low frequency applications the controller performance can be characterised by analysing the step response.

<sup>1</sup> Introduction

<sup>\*</sup>Equality holds for second order systems only, but may be used to approximate higher order characteristics.



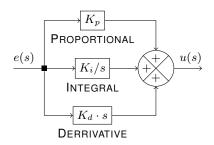


FIGURE 2: Block diagram of a PID controller

$$\underbrace{R_{c} \frac{s + z_{c}}{s + p_{c}}}_{\text{PUASE LEAD}} \underbrace{u(s)}_{\text{c}} : |p_{c}| > |z_{c}|$$

FIGURE 3: Block diagram of a phase-lead controller.

# 3 Specification

The transfer function for the industrial plant process G(s) was given as follows:

$$G(s) = \frac{s+1}{s(s^2+4s+5)} \tag{5}$$

The transfer function  $G_c(s)$  for the two controllers was chosen to meet the system performance specification detailed in Table 1.

TABLE 1: System performance specification.

SYSTEM	$M_0$ [%]	$T_r[s]$	$T_{2\%}[s]$	SSE[%]
1	5	1.0	4	_
2	20	0.5	_	5



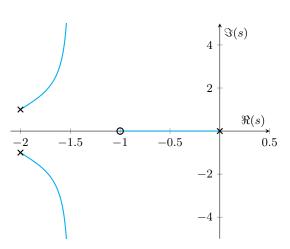


FIGURE 4: Root locus of system poles from closed loop poles to closed loop zeros as  $G_c$  varies from zero to infinity.

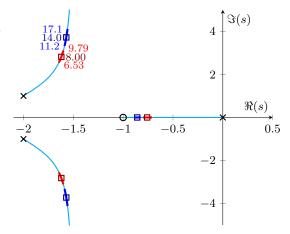


FIGURE 5: Chosen closed loop pole locations, gains and acceptable limits for the gain compensator  $G_c$  designed for SYSTEM 1 and SYSTEM 2, marked on the root locus.

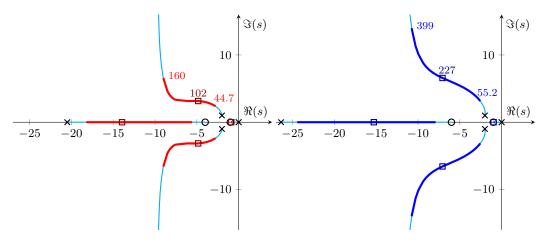


FIGURE 6: Chosen closed loop pole locations, gains and acceptable limits for the phase-lead compensator  $G_c$  designed for SYSTEM 1 and SYSTEM 2, marked on the compensated root loci.



#### A Controller Derivations

## A.1 Phase-lead Compensator

Figure 7 illustrates how the added poles and zeroes were chosen when designing the phase-lead controllers.

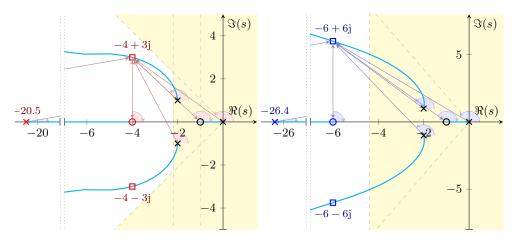


FIGURE 7: Ideal phase-lead compensated root locus for SYSTEM 1 and SYSTEM 2, designed using the angle and magnitude criteria.

The yellow region marks the second order closed loop pole locations which did not satisfy the performance specification and the desired closed loop poles were placed outside this region. The added zero was placed on the real axis in line with desired closed loop poles. The angle criterion was used to uniquely identify the added real pole which ensured the root locus passed through the desired pole locations. This was done programmatically in MATLAB®:

```
function [zc, pc, Kc] = find_lead_compensator(G, s)
% Place real zero 'zc' below desired pole 's'
zc = real(s);
% Find real pole 'pc' using angle criterion
th = sum(angle(s-pole(G))) - sum(angle(s-zero(G))) - angle(s-zc);
th = mod(th-pi, 2*pi);
pc = zc + imag(s)/tan(th);
% Find gain 'Kc' using magnitude criterion
K = prod(abs(s-pole(G)))/prod(abs(s-zero(G))) * abs(s-pc)/abs(s-zc);
Kc = K / dcgain(G);
end % function
```

The magnitude criterion was used to find the gain corresponding to this desired pole, which can be seen on lines 9 and 10, but this was not the gain used in the final controller. The gain was varied in Matlab® sisotool() to find the limiting gains which met the performance specification for each system. The median gains were chosen for the optimum controllers.