

## Abstract

This report describes the design and characteristics of P, PD, PID and phase-lead controllers for an type I, III order industrial plant process in MATLAB®. Controllers were designed to meet one of two performance specifications. After comparison it was concluded that PD controllers achieved the best performance.

## Nomenclature

$e(\infty)$	Steady state error	[%]
$p$	Pole in Laplace domain	[rad s <sup>-1</sup> ]
$s$	Laplace domain variable	[rad s <sup>-1</sup> ]
$t$	Time domain variable	[s]
$z$	Zero in Laplace domain	[rad s <sup>-1</sup> ]
$\zeta$	Damping factor	
$\omega_n$	Natural frequency	[rad s <sup>-1</sup> ]
$G$	Plant process transfer function	
$G_c$	Controller transfer function	
$K$	Plant process gain factor	
$K_c$	Controller gain factor	
$K_d$	Derivative gain factor	
$K_i$	Integral gain factor	
$K_p$	Proportional gain factor	
$M_0$	Peak overshoot	[%]
$T_d$	Derivative time constant	[s]
$T_i$	Integral time constant	[s]
$T_r$	Rise time (10 % to 90 %)	[s]
$T_s$	Settling time (2 %)	[s]

The controller gain factor  $K_c$  is a key design parameter and its effect on the system poles can be represented by the root locus. Each point on the root locus satisfies the angle criterion:

$$\sum_i \angle (s - p_i) - \sum_j \angle (s - z_j) \equiv \pi \pmod{2\pi} \quad (1)$$

This allows the root locus to be manipulated deterministically by adding poles and/or zeros. The controller gain at a point on the root locus can be determined using the magnitude criterion:

$$K_c \cdot K \equiv \frac{\prod_i |s - p_i|}{\prod_j |s - z_j|} \quad (2)$$

where  $K$  is the gain factor of the plant process transfer function.

## 2.1 Performance

For low frequency applications the controller performance can be characterised by analysing the step response.

Overshoot  $M_0$  is the the peak response in proportion to the final output value and is solely dependent on the damping factor  $\zeta$  for second order systems:

$$M_0 = \exp\left(-\frac{\pi\zeta}{\sqrt{1-\zeta^2}}\right)^* \quad (3)$$

The settling time  $T_s$  of the controller is the time for the output oscillations to decay to within some margin of the final value: 2 % is used in this report. Settling time is also dependent on the natural frequency  $\omega_n$ :

$$T_s = -\frac{\ln(2\%)}{\zeta\omega_n} \approx \frac{4}{\zeta\omega_n}^* \quad (4)$$

The rise time  $T_r$  of the controller is the time for the output to transition from 10 % to 90 % of the final value:

$$T_r = \frac{\ln(90\%/10\%)}{\zeta\omega_n} \approx \frac{2.2}{\zeta\omega_n}^* \quad (5)$$

These three parameters are typically specified as upper bounds and constitute the dynamic specification.

The steady state error  $e(\infty)$  for a step input is defined and determined as follows:

$$e(\infty) := \lim_{t \rightarrow \infty} e(t) \quad (6)$$

$$= \begin{cases} \frac{1}{1 + \lim_{s \rightarrow +0} G_c(s)G(s)}, & \text{type 0} \\ 0, & \text{otherwise} \end{cases} \quad (7)$$

\*Equality holds for second order systems only, but may be used to approximate higher order characteristics.

## 1 Introduction

Control systems find applications across engineering, economics and the environment.

Robotics are the obvious application where servo and stepper-motors are used in control systems to actuate precise armature movement.

Robots mimic control systems found in nature, such as the feedback between motor and sensory neurons in animals.

Many markets trade automatically, employing algorithms originally developed for electronic control theory.

## 2 Background

Figure 1 illustrates a closed loop system. The controller is designed to drive the output  $y(s)$  of the plant process to the desired output  $r(s)$  by minimising the error  $e(s)$ .

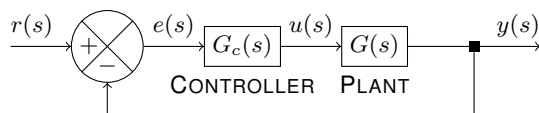


FIGURE 1: Block diagram of the closed loop system.

where the type of the open loop transfer function  $G_c(s)G(s)$  is the number of poles at the origin.

In controller design there is a tradeoff between the dynamic specification and the steady state error. The type of controller has a large impact on how the tradeoff is done.

## 2.2 PID Control

One of the most common types of controller in industry is the PID controller, which can be easily implemented using microcontrollers. Figure 2 shows how a PID controller is composed.

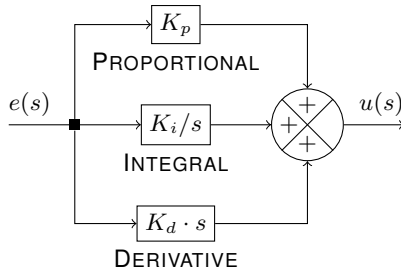


FIGURE 2: Block diagram of a PID controller

The relative contribution of the derivative factor  $K_d$  and integral factor  $K_i$  can be expressed as time constants  $T_d$  and  $T_i$  as follows:

$$T_i = \frac{K_p}{K_i} \quad T_d = \frac{K_d}{K_p} \quad (8)$$

Consequently, an equivalent form of the controller in Figure 2 is as follows:

$$G_c(s) = K_p \left( 1 + \frac{1}{T_i \cdot s} + T_d \cdot s \right) \quad (9)$$

In any controller, the proportional gain  $K_p$  is used to set the tradeoff between overshoot and speed. A faster system has a short rise and settling time but greater overshoot, achieved with a larger gain.

Increasing the contribution of the integrator term improves the steady-state performance at the expense of the dynamic performance. Generally, controllers with short  $T_i$  have greater overshoot, longer rise and settling time but smaller steady-state error.

Increasing the contribution of the differentiator term improves the dynamic performance at the expense of the steady-state performance. Generally, controllers with large  $T_d$  have smaller overshoot, shorter rise and settling time but greater steady-state error.

Some variations of the PID controller are named as follows:

- **P controller** where  $K_i$  and  $K_d$  are zero. Also known as gain compensation.
- **PI controller** where  $K_d$  is zero.
- **PD controller** where  $K_i$  is zero. Used in

## 2.3 Phase Compensation

Practical phase compensation involves adding real pole-zero pairs close to the origin to improve steady-state performance (phase-lag compensation) or left of the uncompensated root locus to improve dynamic performance (phase-lead compensation) or both (lag-lead compensation). Figure 3 illustrates the block diagram of the compensator.

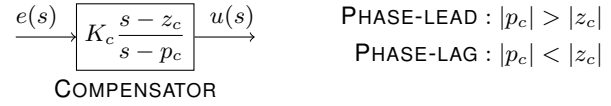


FIGURE 3: Block diagram of a phase-lead controller.

The compensator gain  $K_c$  is then used to tradeoff the overshoot and speed of the modified system, much like gain compensation.

## 3 Specification

The transfer function for the industrial plant process  $G(s)$  was given as follows:

$$G(s) = \frac{s + 1}{s(s^2 + 4s + 5)} \quad (10)$$

The controller transfer functions  $G_c(s)$  were chosen to meet the system performance specification detailed in Table 1.

TABLE 1: System performance specification.

SYSTEM	$M_0$ [%]	$T_r$ [s]	$T_s$ [s]	$e(\infty)$ [%]
1	5	1.0	4	—
2	20	0.5	—	5

## 4 Design

The transfer function (10) described a type I, 3rd order system.

The pole at the origin and equation (7) suggested the steady-state error would be zero. PI and phase-lag controllers seek to improve steady-state performance so these were not used.

The step response characteristics of gain compensators were compared against phase-lead compensators, to demonstrate how modifying the root locus affects performance.

The gain controllers were then compared against PD and PID controllers tuned in MATLAB® `sisotool()` using the 'robust response time' method with the same 'response time', varying the 'robust' parameter to achieve a similar degree of overshoot.

## 5 Results

The step response of the gain compensator and the margins by which the system gain is allowed to increase or decrease were marked on Figure 4.

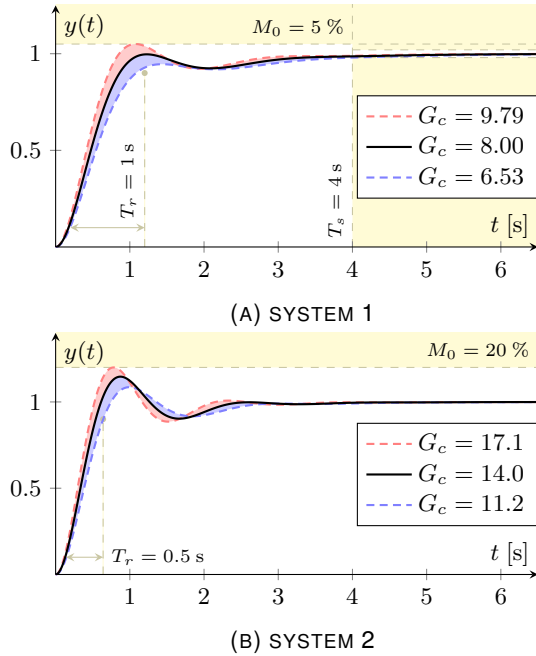


FIGURE 4: Step response of gain controller designed for each system. The gain limits which met the performance specification are also highlighted.

Figure 5 shows similar traces for the chosen phase-lead compensators.

The same gain compensators in Figure 4 are displayed alongside the designed PD and PID controllers in Figure 6.

## 6 Discussion

The gain margins indicated in Figure 4 show the tradeoff between overshoot and response time: as gain was increased, the system responded faster but exhibited more overshoot. Consequently there was a limit to the minimum rise time the controller could achieve before overshoot became unacceptable. Unsurprisingly SYSTEM 2, which had a more relaxed overshoot requirement, was able to rise and settle in less time. These values are detailed in Table 2. All performance requirements were satisfied.

The steady-state error was verified to be zero by taking the `dcgain()` of the closed loop system in MATLAB®.

Table 2 also lists the phase-lead compensators and their performance characteristics. By bending the root locus towards away from the origin, the gain factor was able to be increased without the

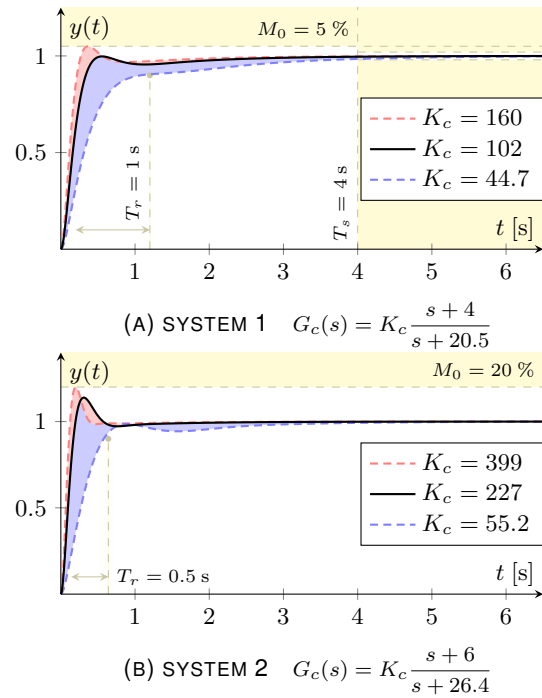


FIGURE 5: Step response of lead compensator designed for each system. The gain limits which met the performance specification are also highlighted.

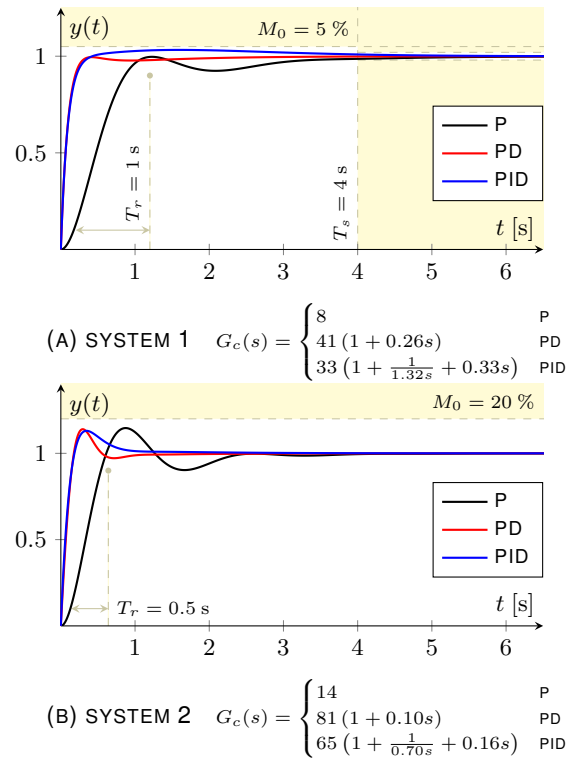


FIGURE 6: Step response of P, PD and PID controllers designed for each system.

damping factor becoming too small. Figure 7 illustrates how the compensated root loci extended away from the imaginary axis where the damping factor was small. The larger gain allowed faster rise

TABLE 2: Performance characteristics of gain and phase-lead compensators designed for each system.

	$K_c$	$ z_c ^*$	$ p_c ^*$	$M_0$ [%]	$T_r$ [s]	$T_s$ [s]	$e(\infty)$
SYSTEM 1	8	—	—	<b>0.0</b>	<b>0.69</b>	<b>3.35</b>	0
	102	4	20.5	<b>0.0</b>	<b>0.28</b>	<b>2.16</b>	0
	14	—	—	<b>14.6</b>	<b>0.41</b>	2.26	<b>0</b>
SYSTEM 2	227	6	26.4	<b>13.9</b>	<b>0.14</b>	0.98	<b>0</b>

\* Units  $[\text{rad s}^{-1}]$ . Values in **bold** match the specification in Table 1.

TABLE 3: Performance characteristics of P, PD and PID controllers designed for each system.

	$K_p$	$T_i$ [s]	$T_d$ [s]	$M_0$ [%]	$T_r$ [s]	$T_s$ [s]	$e(\infty)$
SYSTEM 1	8	—	—	<b>0.0</b>	<b>0.69</b>	<b>3.35</b>	0
	41	—	0.26	<b>0.0</b>	<b>0.18</b>	<b>1.22</b>	0
	33	1.32	0.33	<b>3.3</b>	<b>0.20</b>	<b>2.93</b>	0
SYSTEM 2	14	—	—	<b>14.6</b>	<b>0.41</b>	2.26	<b>0</b>
	81	—	0.10	<b>14.0</b>	<b>0.13</b>	0.87	<b>0</b>
	65	0.70	0.16	<b>13.0</b>	<b>0.13</b>	0.87	<b>0</b>

Values in **bold** match the specification in Table 1.

and settling times.

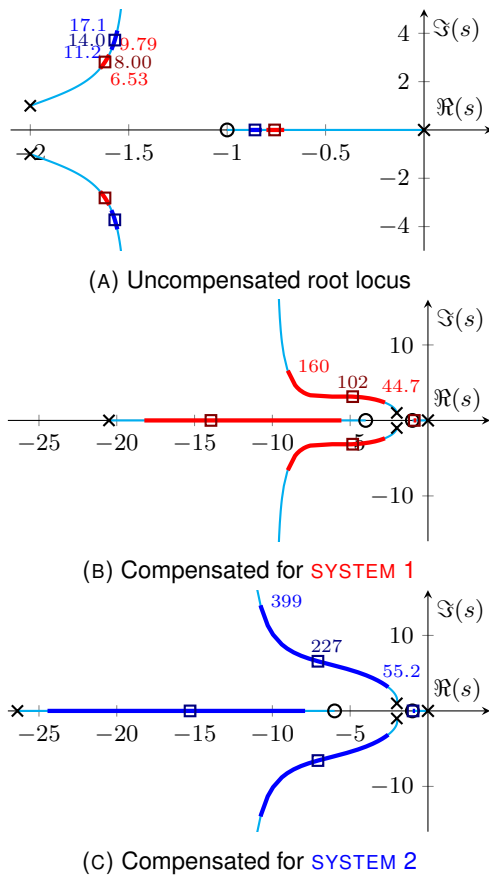


FIGURE 7: Chosen closed loop pole locations, gains and acceptable limits for the gain and phase-lead compensator designed for both systems, marked on the original and compensated root loci.

Table 3 lists the performance characteristics of the P, PD and PID controllers illustrated in Figure 6. All performance requirements were satisfied.

The overshoot of each controller was similar by design, but the corresponding rise and settling times were significantly better for the PD controller than the baseline P controller. The derivative term controlled the output by predicting future error, avoiding overshoot, making the transient more robust.

The integral term in PID controllers had little addi-

tional effect, as the plant process already contained an integrator. The integral term slowed the transient response but had no effect on the already perfect steady-state error. This behaviour was consistent with other type I systems, such as servo-motors, where the velocity of the motor is varied in order to set the servo position. For such applications, PD controllers are common as their decreased complexity results in comparable or better performance.

For similar reasons PD controllers outperformed phase-lead compensators. Practical phase-lead compensators have a real pole, which behaves as a low pass filter, much like an integrator. A PD controller is an ideal phase-lead compensator as the real pole is omitted.

## 7 Conclusion

Two P, PD, PID and phase-lead controllers were designed to successfully meet all of the performance requirements for two different systems using the same plant process.

The second system had a more relaxed overshoot requirement, so controllers could be designed with faster rise and settling times.

Steady-state requirement was not an issue as the plant-process was of type I, with an integrator at the origin.

The PD controllers outperformed the P controller, as the derivative term limited overshoot by predicting future error; the PID controller, which had similar performance but increased complexity because the integral term was redundant; and the practical phase-lead compensator, as the PD controller is also an ideal phase-lead compensator.

## A Controller Derivations

### A.1 P Controller/Gain Compensator

### A.2 Phase-lead Compensator

Figure 8 illustrates how the added poles and zeroes were chosen when designing the phase-lead controllers.

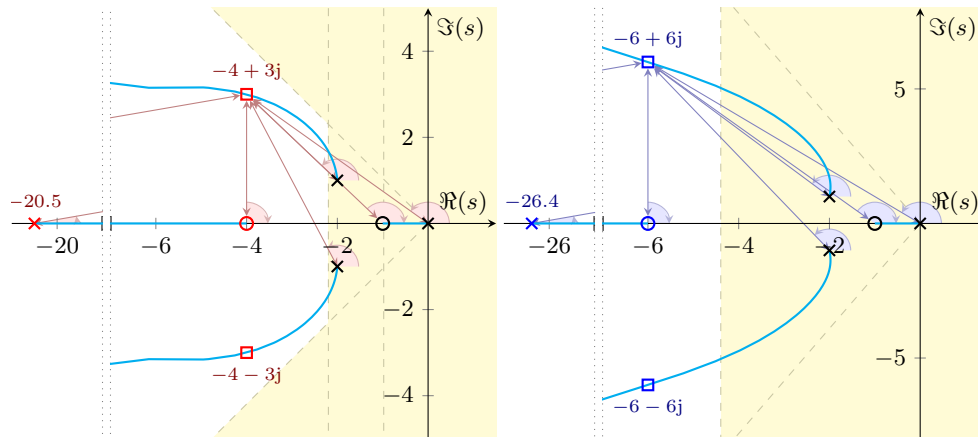


FIGURE 8: Ideal phase-lead compensated root locus for **SYSTEM 1** and **SYSTEM 2**, designed using the angle and magnitude criteria.

The yellow region marks the closed loop pole locations which did not satisfy the performance specification. Values from Table 1 were substituted into (3), (4) and (5) to define the boundaries of these regions. The locus of  $\zeta\omega_n$  manifests as a vertical line on the  $s$ -plane whilst the locus of  $\zeta$  is a straight line through the origin in the second quadrant, reflected in the real axis.

Desired closed loop poles labelled in Figure 8 were chosen close to these boundaries to avoid large compensator parameters and ensure a practical design. The added zero was placed on the real axis in line with the labelled poles and the location of the real pole was determined by rearranging the angle criterion given in (1). This was done programmatically in MATLAB®:

```
1 function [zc, pc, Kc] = find_lead_compensator(G, s)
2 % Place real zero 'zc' below desired pole 's'
3 zc = real(s);
4 % Find real pole 'pc' using angle criterion
5 th = sum(angle(s-pole(G))) - sum(angle(s-zero(G))) - angle(s-zc);
6 th = mod(th-pi, 2*pi);
7 pc = zc + imag(s)/tan(th);
8 % Find gain 'Kc' using magnitude criterion
9 K = prod(abs(s-pole(G)))/prod(abs(s-zero(G))) * abs(s-pc)/abs(s-zc);
10 Kc = K;
11 end % function
```

The magnitude criterion given in (2) was used to find the controller gain corresponding to this desired pole, which can be seen on lines 9 and 10, but this was not the DC gain used in the final controller. The DC gain was varied in MATLAB® `sisotool()` to find the limiting gains which met the performance specification for each system. The median gains were chosen for the optimum controllers.

### A.3 PID Tuning

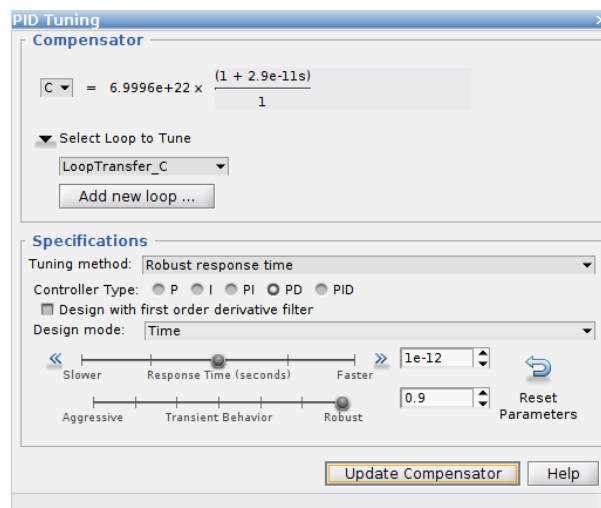


FIGURE 9