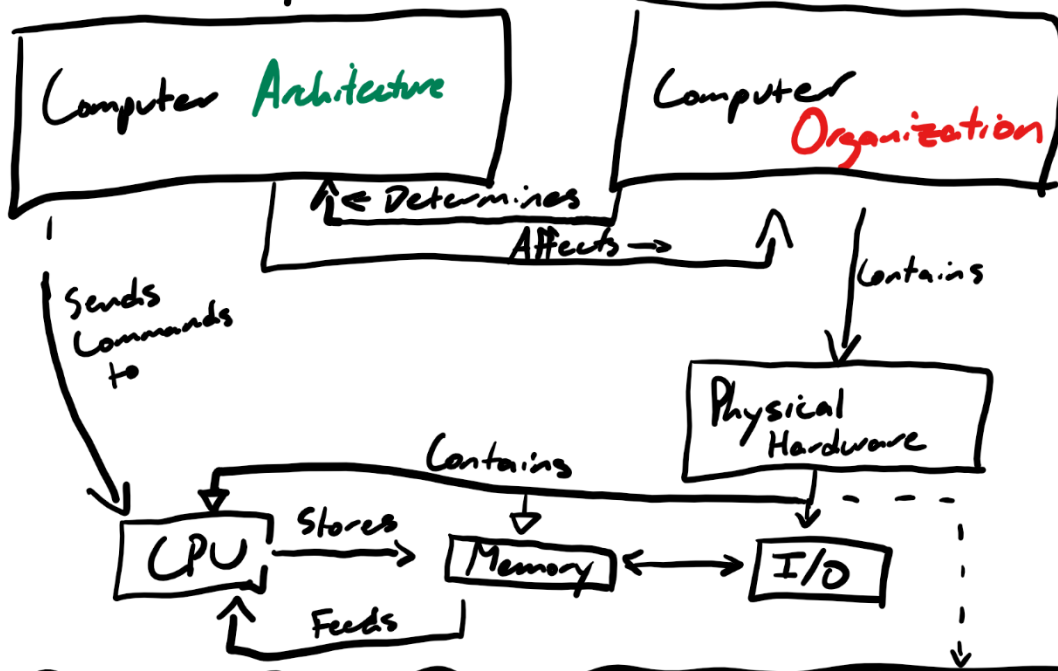


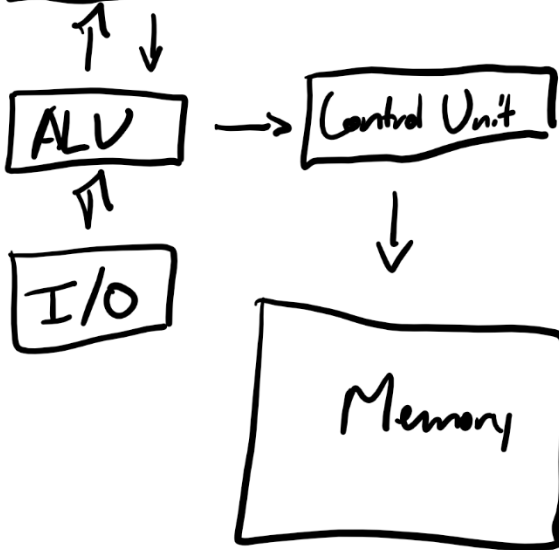
Chapter One, Intro



Von Newman Model



- ① Arithmetic Logic Unit (ALU)
- ② Memory
- ③ Input/Output



Digital Logic ❶

↓
Control Level ❷

↓
Machine Language ❸

↓
System Software ❹

↓
Assembly Language ❺

↓
High-Level Language ❻

↓
User Interface ❼

Transistors Invented



Applied to New
Technology



Boards can compute
faster



Limit is reached
on # of transistors
per circuit

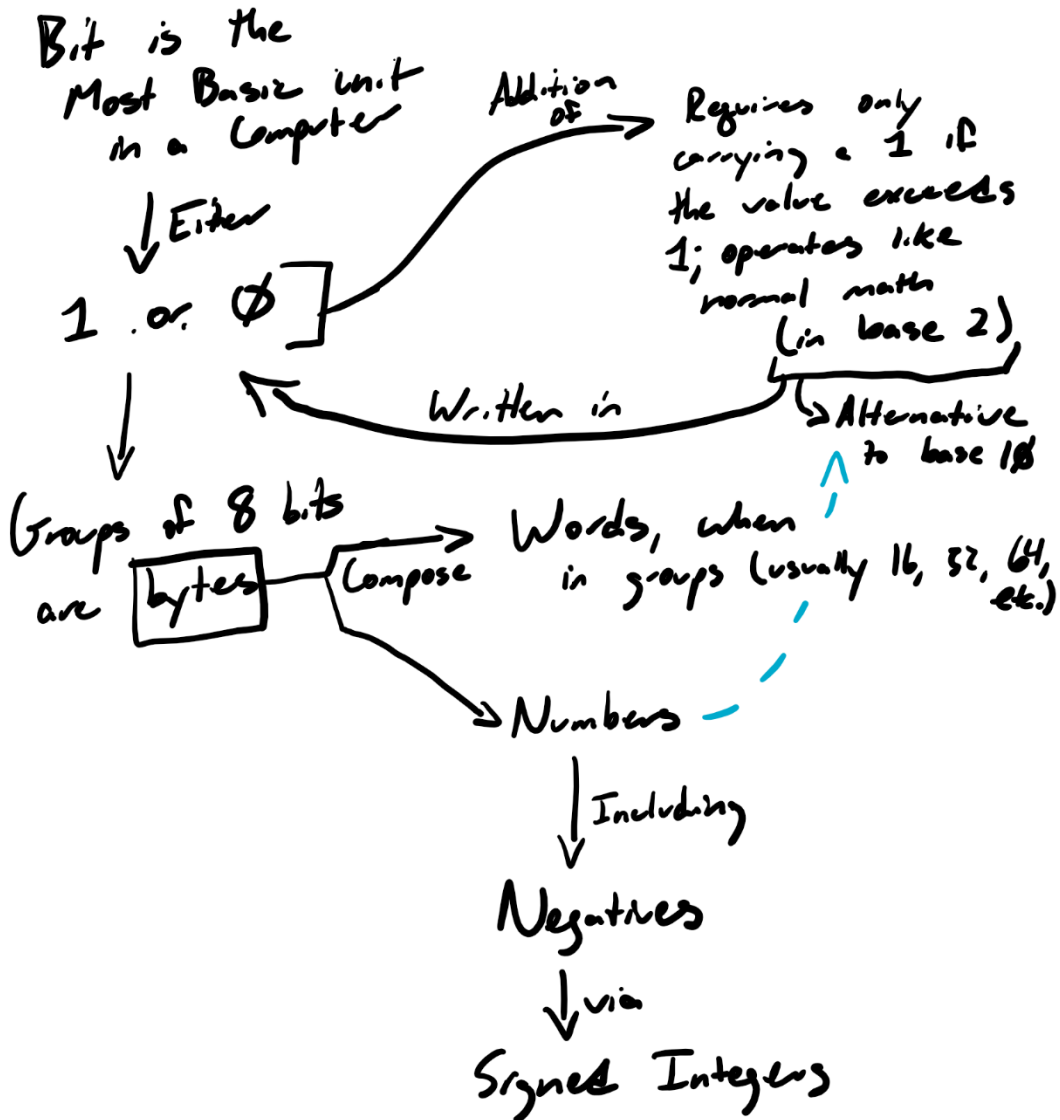


Transistors
become
smaller
(by half, every 2y)

Moore's
Law



Chapter Two

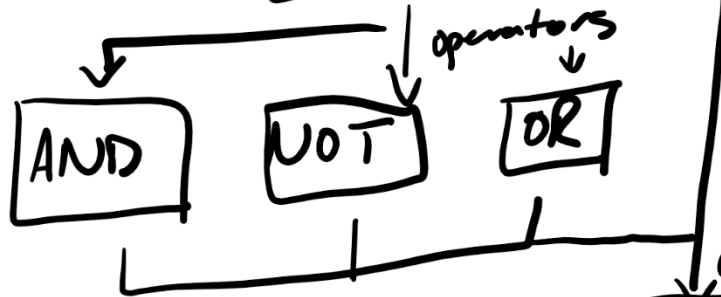


Chapter Three

Boolean Algebra

Based on

BINARY



Create

Truth Tables

Allow for

