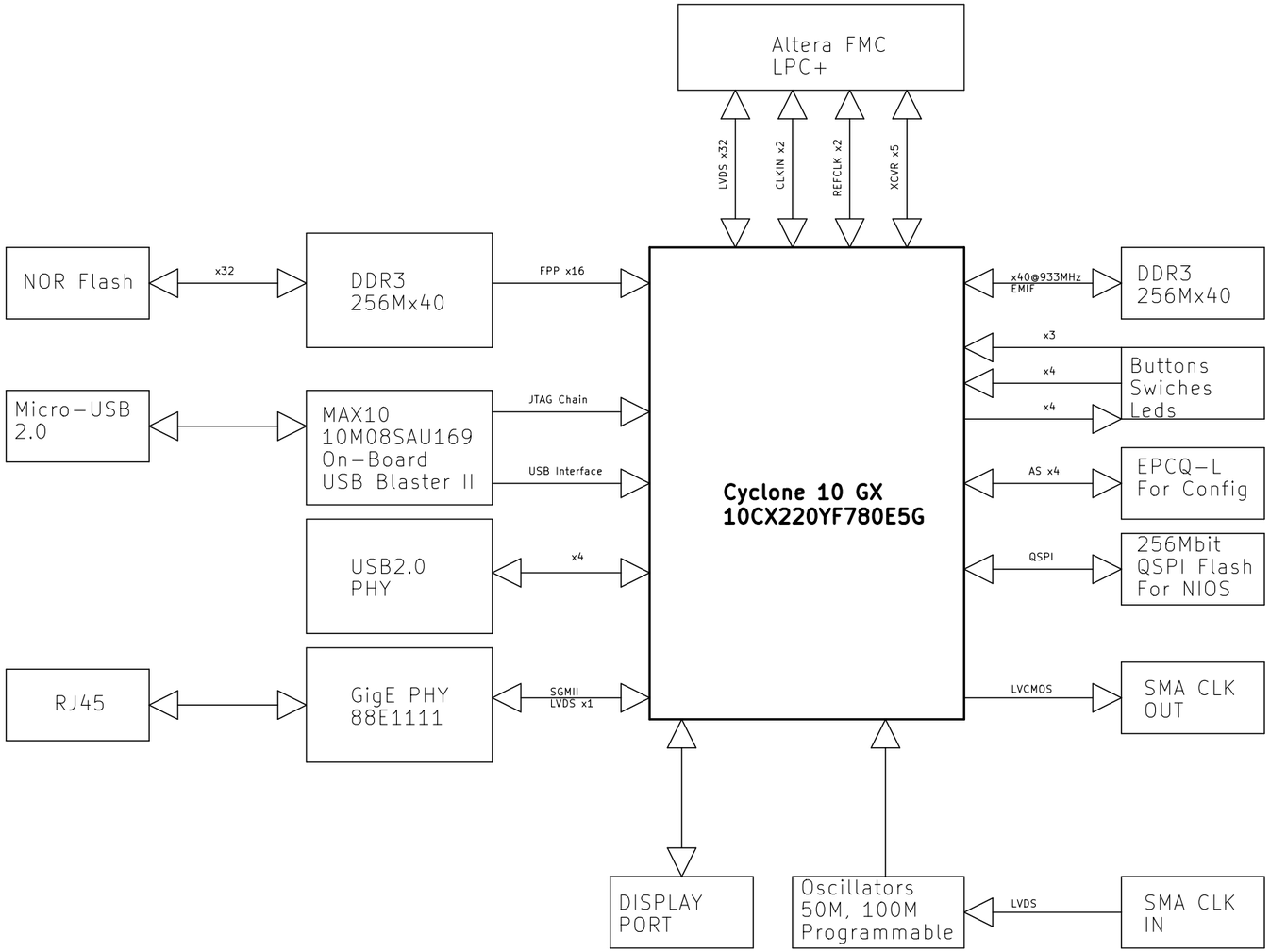
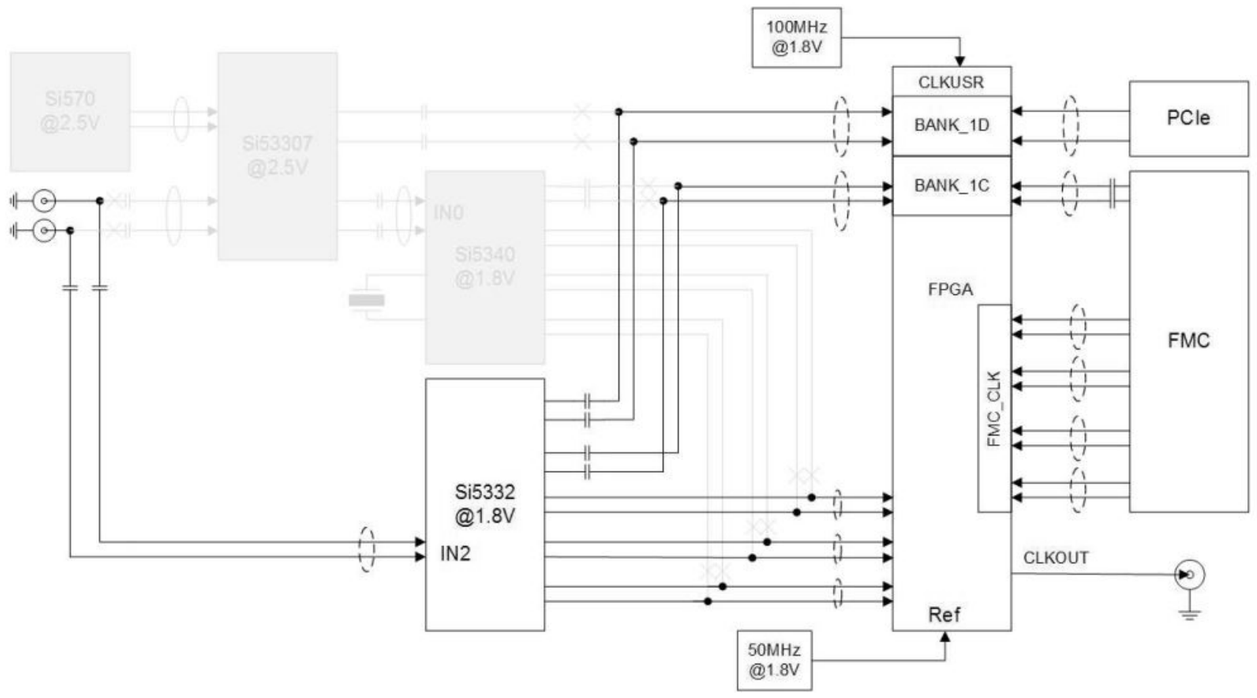


Sheet: /
File: INTEL CYCLONE 10 GX FPGA DEV KIT.sch

Title:

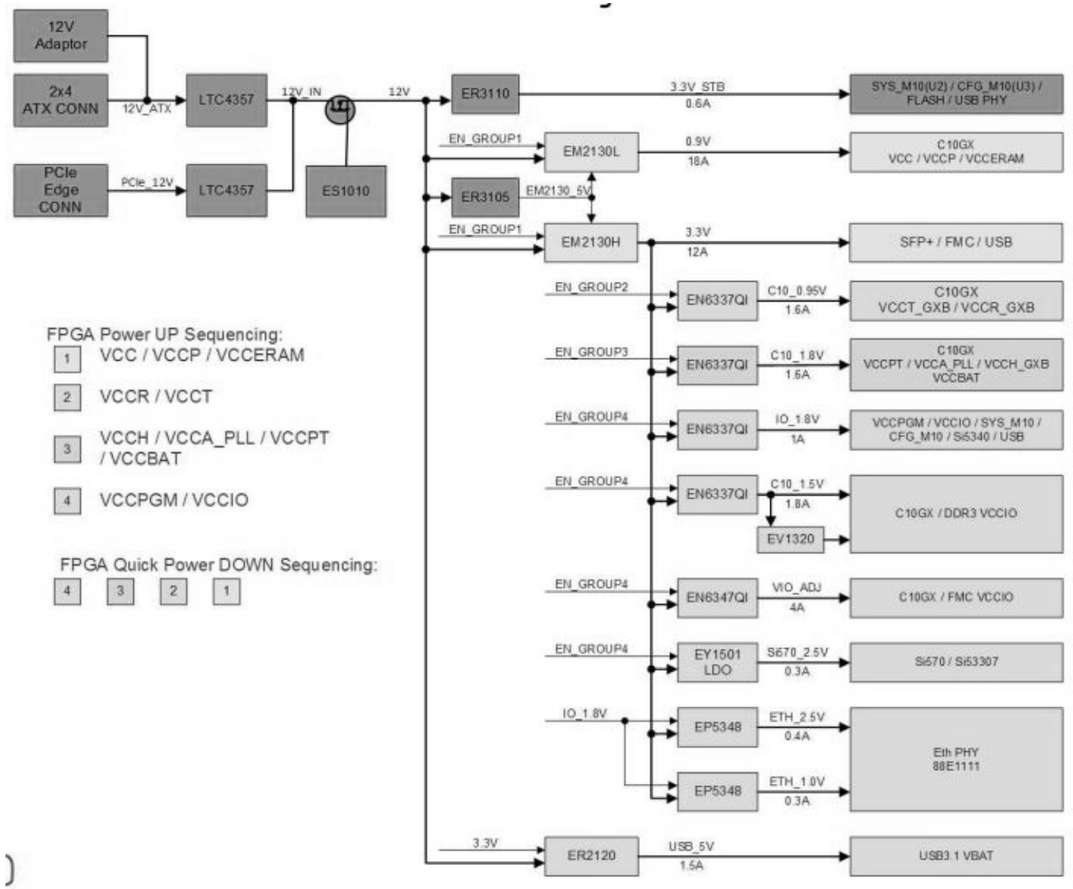
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 1/43



Sheet: /02 – Clock Diagram/
File: 02–Clock Diagram.sch

Title:

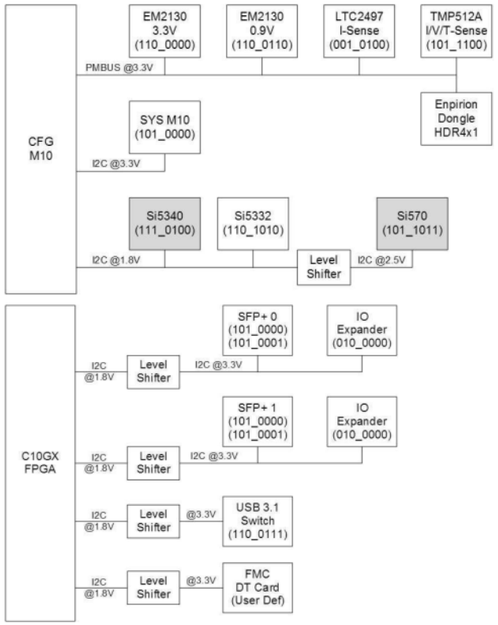
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)–1		Id: 2/43

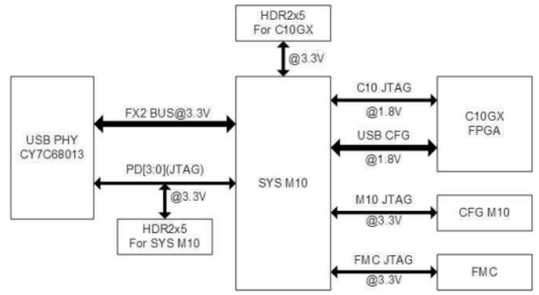


Sheet: /03 – Power Tree Diagram/
File: 03 – Power Tree Diagram.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 3/43



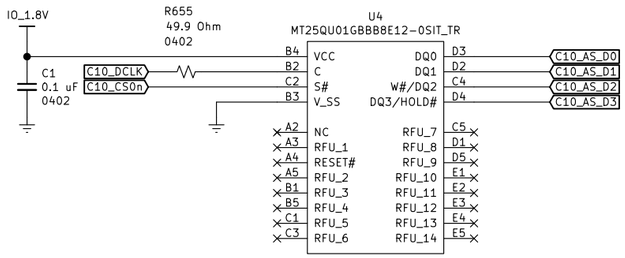


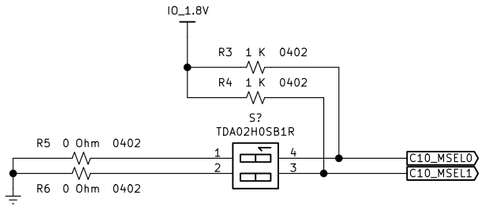
Sheet: /04 – I2C JTAG Chain Diagram/
File: 04 – I2C JTAG Chain Diagram.sch

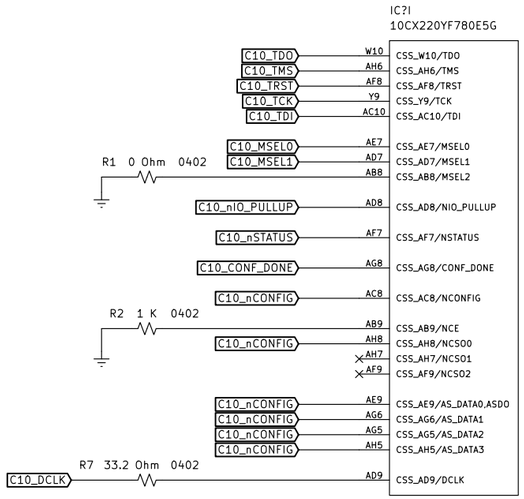
Title:

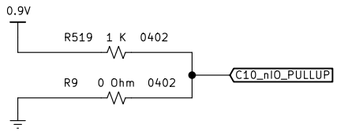
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 4/43

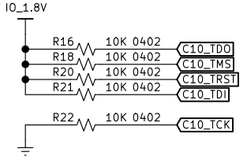
C10GX BANK CSS – Config

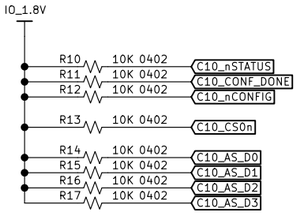












Configuration Scheme	V_{CCPGM} (V)	Power-On Reset (POR) Delay	Valid MSEL[2..0]
JTAG-based configuration	—	—	Use any valid MSEL pin settings below
AS (x1 and x4)	1.8	Fast	010
		Standard	011
PS and FPP (x8, x16, and x32)	1.2/1.5/1.8	Fast	000
		Standard	001

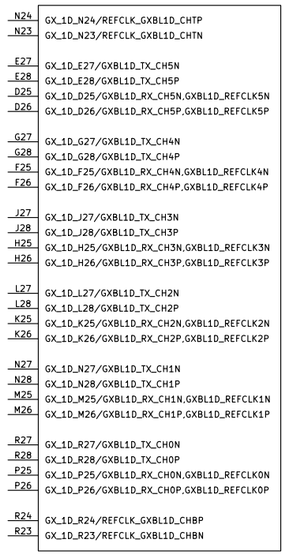
Note:
MSEL2 is fixed '0', MSEL1 and MSEL0 are '0' when corresponding switch is ON

Sheet: /05 – C10GX BANK CSS – Config/
File: 05 – C10GX BANK CSS – Config.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 5/43

C10GX BANK1C/1D – XCVR



U1A
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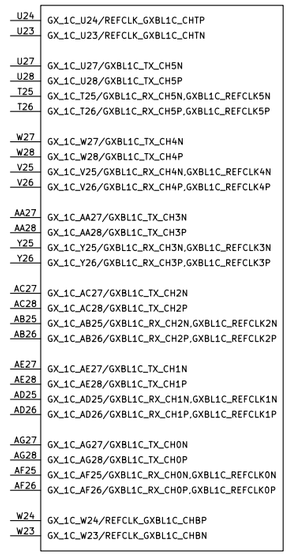


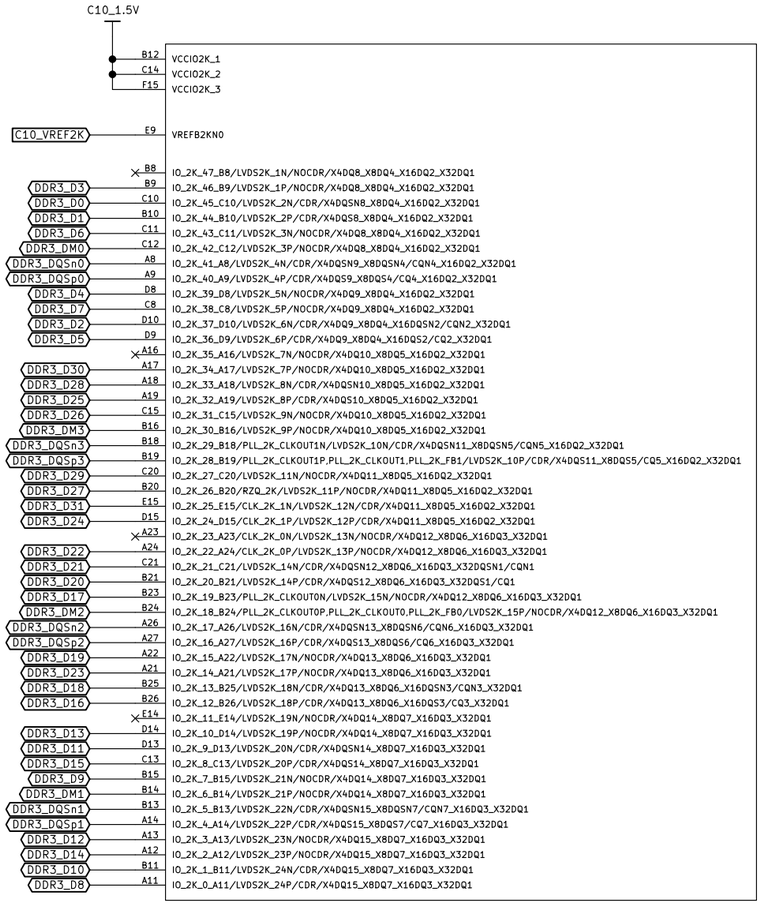
U1B
10CX220YF780E5G

Sheet: /06 – C10GX BANK1C–1D – XCVR/
File: 06 – C10GX BANK1C–1D – XCVR.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)–1		Id: 6/43

C10GX BANK2K – EMIF



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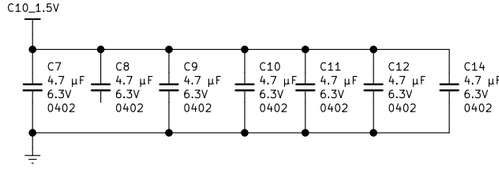
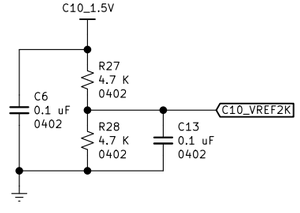


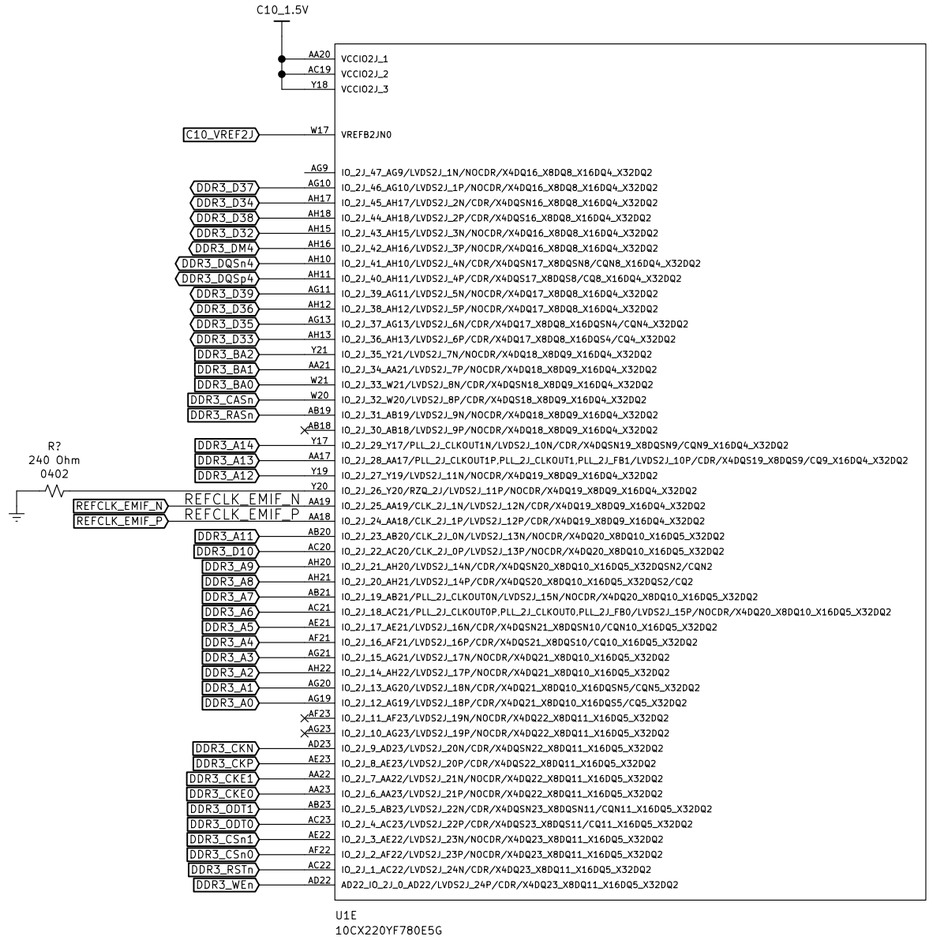


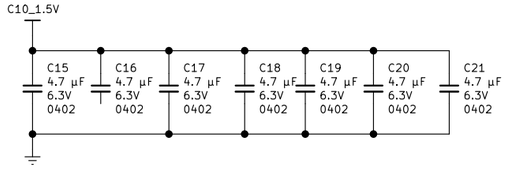
Sheet: /07 – C10GX BANK2K – EMIF/
File: 07 – C10GX BANK2K – EMIF.sch

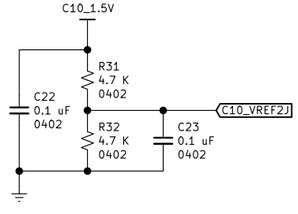
Title:

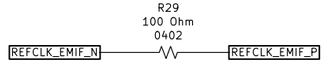
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 7/43

C10GX BANK2J – EMIF









Sheet: /08 – C10GX BANK2J – EMIF/
File: 08 – C10GX BANK2J – EMIF.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 8/43

C10GX BANK3A – FMC LVDS

AA5	VCCIO3A_1
W6	VCCIO3A_2
Y8	VCCIO3A_3
W9	VREFB3AN0
Y4	IO_3A_47_Y4/LVDS3A_1N/NOCDR/X4DQ56_X8DQ28_X16DQ14_X32DQ7
W4	IO_3A_46_W4/LVDS3A_1P/NOCDR/X4DQ56_X8DQ28_X16DQ14_X32DQ7
W7	IO_3A_45_W7/LVDS3A_2N/CDR/X4DQSN56_X8DQ28_X16DQ14_X32DQ7
W8	IO_3A_44_W8/LVDS3A_2P/CDR/X4DQS56_X8DQ28_X16DQ14_X32DQ7
Y6	IO_3A_43_Y6/LVDS3A_3N/NOCDR/X4DQ56_X8DQ28_X16DQ14_X32DQ7
Y7	IO_3A_42_Y7/LVDS3A_3P/NOCDR/X4DQ56_X8DQ28_X16DQ14_X32DQ7
Y5	IO_3A_41_Y5/LVDS3A_4N/CDR/X4DQSN57_X8DQSN28/CQN28_X16DQ14_X32DQ7
W5	IO_3A_40_W5/LVDS3A_4P/CDR/X4DQS57_X8DQS28/CQ28_X16DQ14_X32DQ7
Y2	IO_3A_39_Y2/LVDS3A_5N/NOCDR/X4DQ57_X8DQ28_X16DQ14_X32DQ7
Y1	IO_3A_38_Y1/LVDS3A_5P/NOCDR/X4DQ57_X8DQ28_X16DQ14_X32DQ7
AA8	IO_3A_37_AA8/LVDS3A_6N/CDR/X4DQ57_X8DQ28_X16DQSN14/CQN14_X32DQ7
AA9	IO_3A_36_AA9/LVDS3A_6P/CDR/X4DQ57_X8DQ28_X16DQS14/CQ14_X32DQ7
AB4	IO_3A_35_AB4/LVDS3A_7N/NOCDR/X4DQ58_X8DQ29_X16DQ14_X32DQ7
AC5	IO_3A_34_AC5/LVDS3A_7P/NOCDR/X4DQ58_X8DQ29_X16DQ14_X32DQ7
AA1	IO_3A_33_AA1/LVDS3A_8N/CDR/X4DQSN58_X8DQ29_X16DQ14_X32DQ7
AB1	IO_3A_32_AB1/LVDS3A_8P/CDR/X4DQS58_X8DQ29_X16DQ14_X32DQ7
AB5	IO_3A_31_AB5/LVDS3A_9N/NOCDR/X4DQ58_X8DQ29_X16DQ14_X32DQ7
AB6	IO_3A_30_AB6/LVDS3A_9P/NOCDR/X4DQ58_X8DQ29_X16DQ14_X32DQ7
AB3	IO_3A_29_AB3/PLL_3A_CLKOUT1N/LVDS3A_10N/CDR/X4DQSN59_X8DQSN29/CQN29_X16DQ14_X32DQ7
AA2	IO_3A_28_AA2/PLL_3A_CLKOUT1P,PLL_3A_CLKOUT1,PLL_3A_FB1/LVDS3A_10P/CDR/X4DQS59_X8DQS29/CQ29_X16DQ14_X32DQ7
AA4	IO_3A_27_AA4/LVDS3A_11N/NOCDR/X4DQ59_X8DQ29_X16DQ14_X32DQ7
AA3	IO_3A_26_AA3/RZQ_3A/LVDS3A_11P/NOCDR/X4DQ59_X8DQ29_X16DQ14_X32DQ7
AA7	IO_3A_25_AA7/CLK_3A_1N/LVDS3A_12N/CDR/X4DQ59_X8DQ29_X16DQ14_X32DQ7
AA6	IO_3A_24_AA6/CLK_3A_1P/LVDS3A_12P/CDR/X4DQ59_X8DQ29_X16DQ14_X32DQ7
AC3	IO_3A_23_AC3/CLK_3A_0N/LVDS3A_13N/NOCDR/X4DQ60_X8DQ30_X16DQ15_X32DQ7
AD3	IO_3A_22_AD3/CLK_3A_0P/LVDS3A_13P/NOCDR/X4DQ60_X8DQ30_X16DQ15_X32DQ7
AF2	IO_3A_21_AF2/LVDS3A_14N/CDR/X4DQSN60_X8DQ30_X16DQ15_X32DQSN7/CQN7
AE1	IO_3A_20_AE1/LVDS3A_14P/CDR/X4DQS60_X8DQ30_X16DQ15_X32DQS7/CQ7
AC2	IO_3A_19_AC2/PLL_3A_CLKOUT0N/LVDS3A_15N/NOCDR/X4DQ60_X8DQ30_X16DQ15_X32DQ7
AC1	IO_3A_18_AC1/PLL_3A_CLKOUT0P,PLL_3A_CLKOUT0,PLL_3A_FB0/LVDS3A_15P/NOCDR/X4DQ60_X8DQ30_X16DQ15_X32DQ7
AD2	IO_3A_17_AD2/LVDS3A_16N/CDR/X4DQSN61_X8DQSN30/CQN30_X16DQ15_X32DQ7
AE2	IO_3A_16_AE2/LVDS3A_16P/CDR/X4DQS61_X8DQS30/CQ30_X16DQ15_X32DQ7
AF1	IO_3A_15_AF1/LVDS3A_17N/NOCDR/X4DQ61_X8DQ30_X16DQ15_X32DQ7
AG1	IO_3A_14_AG1/LVDS3A_17P/NOCDR/X4DQ61_X8DQ30_X16DQ15_X32DQ7
AF3	IO_3A_13_AF3/LVDS3A_18N/CDR/X4DQ61_X8DQ30_X16DQSN15/CQN15_X32DQ7
AG3	IO_3A_12_AG3/LVDS3A_18P/CDR/X4DQ61_X8DQ30_X16DQS15/CQ15_X32DQ7
AH3	IO_3A_11_AH3/LVDS3A_19N/NOCDR/X4DQ62_X8DQ31_X16DQ15_X32DQ7
AH2	IO_3A_10_AH2/LVDS3A_19P/NOCDR/X4DQ62_X8DQ31_X16DQ15_X32DQ7
AD4	IO_3A_9_AD4/LVDS3A_20N/CDR/X4DQSN62_X8DQ31_X16DQ15_X32DQ7
AE4	IO_3A_8_AE4/LVDS3A_20P/CDR/X4DQS62_X8DQ31_X16DQ15_X32DQ7
AC7	IO_3A_7_AC7/LVDS3A_21N/NOCDR/X4DQ62_X8DQ31_X16DQ15_X32DQ7
AC6	IO_3A_6_AC6/LVDS3A_21P/NOCDR/X4DQ62_X8DQ31_X16DQ15_X32DQ7
AE6	IO_3A_5_AE6/LVDS3A_22N/CDR/X4DQSN63_X8DQSN31/CQN31_X16DQ15_X32DQ7
AF6	IO_3A_4_AF6/LVDS3A_22P/CDR/X4DQS63_X8DQS31/CQ31_X16DQ15_X32DQ7

C10_VIO_ADJ

C24 4.7 µF 6.3V

C25 0.1 uF 0402

C26 0.1 uF 0402

C27 10000 pF 0402

C28 10000 pF 0402

VREF_FMC

C29 4.7 µF 6.3V

IC?H
10CX220YF780E5G

R33
100 Ohm
0402

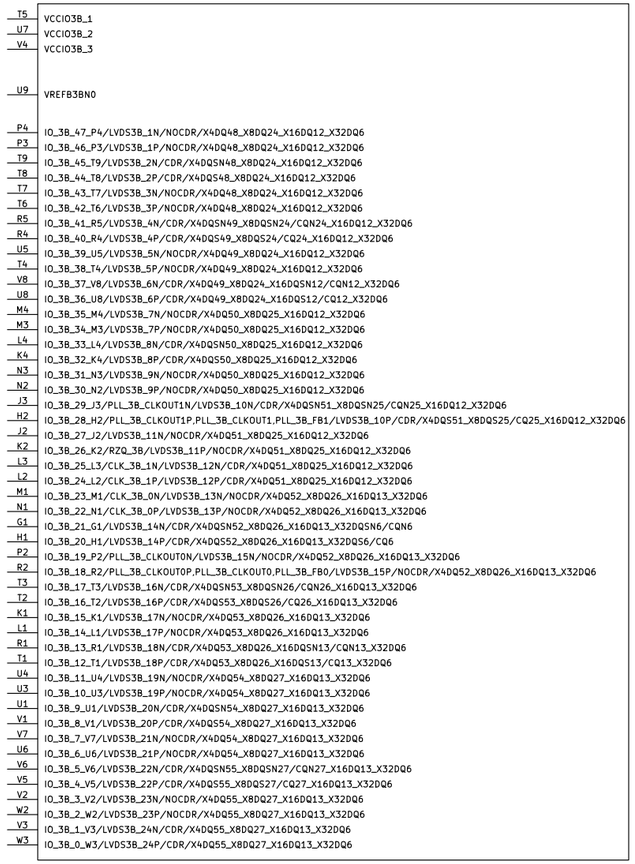
R34
100 Ohm
0402

Sheet: /09 – C10GX BANK3A – FMC LVDS/
File: 09 – C10GX BANK3A – FMC LVDS.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 9/43

C10GX BANK3B – FMC LVDS



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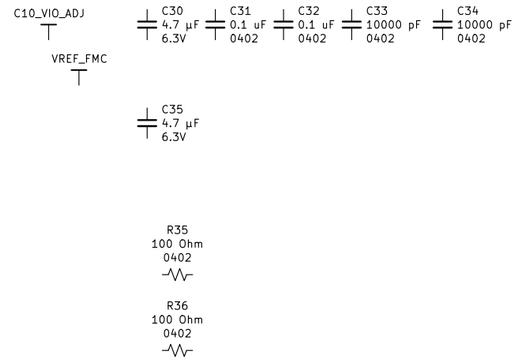

Sheet: /10 – C10GX BANK3B – FMC LVDS/
File: 10 – C10GX BANK3B – FMC LVDS.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 10/43

C10GX BANK2A – FPP/GPIO

C578
GRM155R60J475ME87D
4.7 µF
6.3V

C579
CC0402KRX7R7BB104
0.1 uF
16V
0402

C580
CC0402KRX7R7BB104
0.1 uF
16V
0402

C581
GRM155R71H103KA88D
10000 pF
50V
0402

R5024.7 K 0402

R5214.7 K 0402

R5224.7 K 0402

R5234.7 K 0402

R441 K 0402

Y?
SM5545TEX-100-0M
100 MHz

R45
RC0402FR-07100RL
100 Ohm
0402

R517
RC0402FR-07100RL
100 Ohm
0402

FB1
742792780
220 Ohm
0603

R46 4.7 K 0402

C37
GRM155R60J475ME87D
4.7 µF
6.3V

R47
RC0402FR-0733R2L
33.2 Ohm
0402

U1E
10CX220YF780E5G

AA20	VCCIO2J_1
AC19	VCCIO2J_2
Y18	VCCIO2J_3
W17	VREFB2JN0
AG9	IO_2J_47_AG9/LVDS2J_1N/NOCDR/X4DQ16_X8DQ8_X16DQ4_X32DQ2
AG10	IO_2J_46_AG10/LVDS2J_1P/NOCDR/X4DQ16_X8DQ8_X16DQ4_X32DQ2
AH17	IO_2J_45_AH17/LVDS2J_2N/CDR/X4DQSN16_X8DQ8_X16DQ4_X32DQ2
AH18	IO_2J_44_AH18/LVDS2J_2P/CDR/X4DQS16_X8DQ8_X16DQ4_X32DQ2
AH15	IO_2J_43_AH15/LVDS2J_3N/NOCDR/X4DQ16_X8DQ8_X16DQ4_X32DQ2
AH16	IO_2J_42_AH16/LVDS2J_3P/NOCDR/X4DQ16_X8DQ8_X16DQ4_X32DQ2
AH10	IO_2J_41_AH10/LVDS2J_4N/CDR/X4DQSN17_X8DQSN8/CQN8_X16DQ4_X32DQ2
AH11	IO_2J_40_AH11/LVDS2J_4P/CDR/X4DQS17_X8DQS8/CQ8_X16DQ4_X32DQ2
AG11	IO_2J_39_AG11/LVDS2J_5N/NOCDR/X4DQ17_X8DQ8_X16DQ4_X32DQ2
AH12	IO_2J_38_AH12/LVDS2J_5P/NOCDR/X4DQ17_X8DQ8_X16DQ4_X32DQ2
AG13	IO_2J_37_AG13/LVDS2J_6N/CDR/X4DQ17_X8DQ8_X16DQSN4/CQN4_X32DQ2
AH13	IO_2J_36_AH13/LVDS2J_6P/CDR/X4DQ17_X8DQ8_X16DQS4/CQ4_X32DQ2
Y21	IO_2J_35_Y21/LVDS2J_7N/NOCDR/X4DQ18_X8DQ9_X16DQ4_X32DQ2
AA21	IO_2J_34_AA21/LVDS2J_7P/NOCDR/X4DQ18_X8DQ9_X16DQ4_X32DQ2
W21	IO_2J_33_W21/LVDS2J_8N/CDR/X4DQSN18_X8DQ9_X16DQ4_X32DQ2
W20	IO_2J_32_W20/LVDS2J_8P/CDR/X4DQS18_X8DQ9_X16DQ4_X32DQ2
AB19	IO_2J_31_AB19/LVDS2J_9N/NOCDR/X4DQ18_X8DQ9_X16DQ4_X32DQ2
AB18	IO_2J_30_AB18/LVDS2J_9P/NOCDR/X4DQ18_X8DQ9_X16DQ4_X32DQ2
Y17	IO_2J_29_Y17/PLL_2J_CLKOUT1N/LVDS2J_10N/CDR/X4DQSN19_X8DQSN9/CQN9_X16DQ4_X32DQ2
AA17	IO_2J_28_AA17/PLL_2J_CLKOUT1P,PLL_2J_CLKOUT1,PLL_2J_FB1/LVDS2J_10P/CDR/X4DQS19_X8DQS9/CQ9_X16DQ4_X32DQ2
Y19	IO_2J_27_Y19/LVDS2J_11N/NOCDR/X4DQ19_X8DQ9_X16DQ4_X32DQ2
Y20	IO_2J_26_Y20/RZQ_2J/LVDS2J_11P/NOCDR/X4DQ19_X8DQ9_X16DQ4_X32DQ2
AA19	IO_2J_25_AA19/CLK_2J_1N/LVDS2J_12N/CDR/X4DQ19_X8DQ9_X16DQ4_X32DQ2
AA18	IO_2J_24_AA18/CLK_2J_1P/LVDS2J_12P/CDR/X4DQ19_X8DQ9_X16DQ4_X32DQ2
AB20	IO_2J_23_AB20/CLK_2J_0N/LVDS2J_13N/NOCDR/X4DQ20_X8DQ10_X16DQ5_X32DQ2
AC20	IO_2J_22_AC20/CLK_2J_0P/LVDS2J_13P/NOCDR/X4DQ20_X8DQ10_X16DQ5_X32DQ2
AH20	IO_2J_21_AH20/LVDS2J_14N/CDR/X4DQSN20_X8DQ10_X16DQ5_X32DQSN2/CQN2
AH21	IO_2J_20_AH21/LVDS2J_14P/CDR/X4DQS20_X8DQ10_X16DQ5_X32DQS2/CQ2
AB21	IO_2J_19_AB21/PLL_2J_CLKOUT0N/LVDS2J_15N/NOCDR/X4DQ20_X8DQ10_X16DQ5_X32DQ2
AC21	IO_2J_18_AC21/PLL_2J_CLKOUT0P,PLL_2J_CLKOUT0,PLL_2J_FB0/LVDS2J_15P/NOCDR/X4DQ20_X8DQ10_X16DQ5_X32DQ2
AE21	IO_2J_17_AE21/LVDS2J_16N/CDR/X4DQSN21_X8DQSN10/CQN10_X16DQ5_X32DQ2
AF21	IO_2J_16_AF21/LVDS2J_16P/CDR/X4DQS21_X8DQS10/CQ10_X16DQ5_X32DQ2
AG21	IO_2J_15_AG21/LVDS2J_17N/NOCDR/X4DQ21_X8DQ10_X16DQ5_X32DQ2
AH22	IO_2J_14_AH22/LVDS2J_17P/NOCDR/X4DQ21_X8DQ10_X16DQ5_X32DQ2
AG20	IO_2J_13_AG20/LVDS2J_18N/CDR/X4DQ21_X8DQ10_X16DQSN5/CQN5_X32DQ2
AG19	IO_2J_12_AG19/LVDS2J_18P/CDR/X4DQ21_X8DQ10_X16DQS5/CQ5_X32DQ2
AF23	IO_2J_11_AF23/LVDS2J_19N/NOCDR/X4DQ22_X8DQ11_X16DQ5_X32DQ2
AG23	IO_2J_10_AG23/LVDS2J_19P/NOCDR/X4DQ22_X8DQ11_X16DQ5_X32DQ2
AD23	IO_2J_9_AD23/LVDS2J_20N/CDR/X4DQSN22_X8DQ11_X16DQ5_X32DQ2
AE23	IO_2J_8_AE23/LVDS2J_20P/CDR/X4DQS22_X8DQ11_X16DQ5_X32DQ2
AA22	IO_2J_7_AA22/LVDS2J_21N/NOCDR/X4DQ22_X8DQ11_X16DQ5_X32DQ2
AA23	IO_2J_6_AA23/LVDS2J_21P/NOCDR/X4DQ22_X8DQ11_X16DQ5_X32DQ2
AB23	IO_2J_5_AB23/LVDS2J_22N/CDR/X4DQSN23_X8DQSN11/CQN11_X16DQ5_X32DQ2
AC23	IO_2J_4_AC23/LVDS2J_22P/CDR/X4DQS23_X8DQS11/CQ11_X16DQ5_X32DQ2
AE22	IO_2J_3_AE22/LVDS2J_23N/NOCDR/X4DQ23_X8DQ11_X16DQ5_X32DQ2
AF22	IO_2J_2_AF22/LVDS2J_23P/NOCDR/X4DQ23_X8DQ11_X16DQ5_X32DQ2
AC22	IO_2J_1_AC22/LVDS2J_24N/CDR/X4DQ23_X8DQ11_X16DQ5_X32DQ2
AD22	AD22_IO_2J_0_AD22/LVDS2J_24P/CDR/X4DQ23_X8DQ11_X16DQ5_X32DQ2

Sheet: /11 – C10GX BANK2A – FPP–GPIO/
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Title:

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KiCad E.D.A. kicad (5.1.9)–1		Id: 11/43

C10GX BANK2L – GPIO

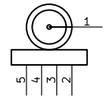
U58
IS25WP256D-RHLE-TR

D3	DQ0/SI	VCC	B4
D2	DQ1/SO	VSS	B3
C4	DQ2/W#		
D4	DQ3	NDU6	B5
		DNU7	C1
B2	C	DNU8	C3
		DNU9	C5
C2	S#	DNU10	D1
A4	RESET#/DNU3	DNU11	D5
		DNU12	E1
A2	DNU1	DNU13	E2
A3	DNU2	DNU14	E3
A5	DNU4	DNU15	E4
B1	DNU5	DNU16	E5

U1C
10CX220YF780E5G

E18	VCCIO2L_1
H19	VCCIO2L_2
K18	VCCIO2L_3
K16	VREFB2LN0
H16	IO_2L_47_H16/DIFFIO2L_1N/NOCDR/X4DQ0_X8DQ0_X16DQ0_X32DQ0
H17	IO_2L_46_H17/DIFFIO2L_1P/NOCDR/X4DQ0_X8DQ0_X16DQ0_X32DQ0
J19	IO_2L_45_J19/DIFFIO2L_2N/NOCDR/X4DQSN0_X8DQ0_X16DQ0_X32DQ0
J18	IO_2L_44_J18/DIFFIO2L_2P/NOCDR/X4DQS0_X8DQ0_X16DQ0_X32DQ0
K17	IO_2L_43_K17/DIFFIO2L_3N/NOCDR/X4DQ0_X8DQ0_X16DQ0_X32DQ0
J17	_IO_2L_42_J17/DIFFIO2L_3P/NOCDR/X4DQ0_X8DQ0_X16DQ0_X32DQ0
F18	IO_2L_41_F18/DIFFIO2L_4N/NOCDR/X4DQSN1_X8DQSN0/CQN0_X16DQ0_X32DQ0
F17	IO_2L_40_F17/DIFFIO2L_4P/NOCDR/X4DQS1_X8DQS0/CQ0_X16DQ0_X32DQ0F17
H18	IO_2L_39_H18/DIFFIO2L_5N/NOCDR/X4DQ1_X8DQ0_X16DQ0_X32DQ0
G18	IO_2L_38_G18/DIFFIO2L_5P/NOCDR/X4DQ1_X8DQ0_X16DQ0_X32DQ0
G19	IO_2L_37_G19/DIFFIO2L_6N/NOCDR/X4DQ1_X8DQ0_X16DQSN0/CQN0_X32DQ0
G20	IO_2L_36_G20/DIFFIO2L_6P/NOCDR/X4DQ1_X8DQ0_X16DQS0/CQ0_X32DQ0
E21	IO_2L_35_E21/DIFFIO2L_7N/NOCDR/X4DQ2_X8DQ1_X16DQ0_X32DQ0
D22	IO_2L_34_D22/DIFFIO2L_7P/NOCDR/X4DQ2_X8DQ1_X16DQ0_X32DQ0
E23	IO_2L_33_E23/DIFFIO2L_8N/NOCDR/X4DQSN2_X8DQ1_X16DQ0_X32DQ0
D23	IO_2L_32_D23/DIFFIO2L_8P/NOCDR/X4DQS2_X8DQ1_X16DQ0_X32DQ0
F22	IO_2L_31_F22/DIFFIO2L_9N/NOCDR/X4DQ2_X8DQ1_X16DQ0_X32DQ0
E22	IO_2L_30_E22/DIFFIO2L_9P/NOCDR/X4DQ2_X8DQ1_X16DQ0_X32DQ0
C22	IO_2L_29_C22/PLL_2L_CLKOUT1N/DIFFIO2L_10N/NOCDR/X4DQSN3_X8DQSN1/CQN1_X16DQ0_X32DQ0
C23	IO_2L_28_C23/PLL_2L_CLKOUT1P,PLL_2L_CLKOUT1,PLL_2L_FB1/DIFFIO2L_10P/NOCDR/X4DQS3_X8DQS1/CQ1_X16DQ0_X32DQ0
G21	IO_2L_27_G21/DIFFIO2L_11N/NOCDR/X4DQ3_X8DQ1_X16DQ0_X32DQ0
F21	IO_2L_26_F21/RZQ_2L/DIFFIO2L_11P/NOCDR/X4DQ3_X8DQ1_X16DQ0_X32DQ0
G23	IO_2L_25_G23/CLK_2L_1N/DIFFIO2L_12N/NOCDR/X4DQ3_X8DQ1_X16DQ0_X32DQ0
F23	IO_2L_24_F23/CLK_2L_1P/DIFFIO2L_12P/NOCDR/X4DQ3_X8DQ1_X16DQ0_X32DQ0
H23	IO_2L_23_H23/CLK_2L_0N/DIFFIO2L_13N/NOCDR/X4DQ4_X8DQ2_X16DQ1_X32DQ0
J23	IO_2L_22_J23/CLK_2L_0P/DIFFIO2L_13P/NOCDR/X4DQ4_X8DQ2_X16DQ1_X32DQ0
K21	IO_2L_21_K21/DIFFIO2L_14N/NOCDR/X4DQSN4_X8DQ2_X16DQ1_X32DQSN0/CQN0
J20	IO_2L_20_J20/DIFFIO2L_14P/NOCDR/X4DQS4_X8DQ2_X16DQ1_X32DQS0/CQ0
H22	IO_2L_19_H22/PLL_2L_CLKOUT0N/DIFFIO2L_15N/NOCDR/X4DQ4_X8DQ2_X16DQ1_X32DQ0
J22	IO_2L_18_J22/PLL_2L_CLKOUT0P,PLL_2L_CLKOUT0,PLL_2L_FB0/DIFFIO2L_15P/NOCDR/X4DQ4_X8DQ2_X16DQ1_X32DQ0
H21	IO_2L_17_H21/DIFFIO2L_16N/NOCDR/X4DQSN5_X8DQSN2/CQN2_X16DQ1_X32DQ0
H20	IO_2L_16_H20/DIFFIO2L_16P/NOCDR/X4DQS5_X8DQS2/CQ2_X16DQ1_X32DQ0
K20	IO_2L_15_K20/DIFFIO2L_17N/NOCDR/X4DQ5_X8DQ2_X16DQ1_X32DQ0
K19	IO_2L_14_K19/DIFFIO2L_17P/NOCDR/X4DQ5_X8DQ2_X16DQ1_X32DQ0
K22	IO_2L_13_K22/DIFFIO2L_18N/NOCDR/X4DQ5_X8DQ2_X16DQSN1/CQN1_X32DQ0
K23	IO_2L_12_K23/DIFFIO2L_18P/NOCDR/X4DQ5_X8DQ2_X16DQS1/CQ1_X32DQ0
D18	IO_2L_11_D18/DIFFIO2L_19N/NOCDR/X4DQ6_X8DQ3_X16DQ1_X32DQ0
D19	IO_2L_10_D19/DIFFIO2L_19P/NOCDR/X4DQ6_X8DQ3_X16DQ1_X32DQ0
E17	IO_2L_9_E17/DIFFIO2L_20N/NOCDR/X4DQSN6_X8DQ3_X16DQ1_X32DQ0
E16	IO_2L_8_E16/DIFFIO2L_20P/NOCDR/X4DQS6_X8DQ3_X16DQ1_X32DQ0
F19	IO_2L_7_F19/DIFFIO2L_21N/NOCDR/X4DQ6_X8DQ3_X16DQ1_X32DQ0
E19	IO_2L_6_E19/DIFFIO2L_21P/NOCDR/X4DQ6_X8DQ3_X16DQ1_X32DQ0
E20	IO_2L_5_E20/DIFFIO2L_22N/NOCDR/X4DQSN7_X8DQSN3/CQN3_X16DQ1_X32DQ0
D20	IO_2L_4_D20/DIFFIO2L_22P/NOCDR/X4DQS7_X8DQS3/CQ3_X16DQ1_X32DQ0
C16	IO_2L_3_C16/DIFFIO2L_23N/NOCDR/X4DQ7_X8DQ3_X16DQ1_X32DQ0
C17	IO_2L_2_C17/DIFFIO2L_23P/NOCDR/X4DQ7_X8DQ3_X16DQ1_X32DQ0
D17	IO_2L_1_D17/DIFFIO2L_24N/NOCDR/X4DQ7_X8DQ3_X16DQ1_X32DQ0
C18	IO_2L_0_C18/DIFFIO2L_24P/NOCDR/X4DQ7_X8DQ3_X16DQ1_X32DQ0

J4
SMA-J-P-H-ST-MT1



Sheet: /12 – C10GX BANK2L – GPIO/
File: 12 – C10GX BANK2L – GPIO.sch

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C10GX POWER

U1K
10CX220YF780E5G

L11	VCC_1		
L12	VCC_2	VCCLSENSE	T13
L16	VCC_3	GNDSENSE	T12
L17	VCC_4		
L18	VCC_5		
L19	VCC_6	VCCP_1	N11
M10	VCC_7	VCCP_2	N17
M11	VCC_8	VCCP_3	U11
M15	VCC_9	VCCP_4	U13
M16	VCC_10	VCCP_5	U16
M19	VCC_11		
M20	VCC_12		
N10	VCC_13		
N12	VCC_14	VCCPT_1	M14
N13	VCC_15	VCCPT_2	M18
N15	VCC_16	VCCPT_3	V12
N16	VCC_17	VCCPT_4	V18
N18	VCC_18		
N20	VCC_19		
P10	VCC_20		
P12	VCC_21	VCCPGM_1	W14
P14	VCC_22	VCCPGM_2	Y14
P17	VCC_23		
P18	VCC_24		
P19	VCC_25		
P20	VCC_26	VCCBAT	W13
R10	VCC_27		
R11	VCC_28		
R14	VCC_29		
R15	VCC_30		
R16	VCC_31		
R19	VCC_32	VCCA_PLL_1	P13
R20	VCC_33	VCCA_PLL_2	P15
T11	VCC_34		
T14	VCC_35		
T16	VCC_36		
T17	VCC_37		
T18	VCC_38		
T19	VCC_39		
U10	VCC_40		
U14	VCC_41		
U15	VCC_42		
U18	VCC_43		
U19	VCC_44		
U20	VCC_45		
V10	VCC_46		
V11	VCC_47		
V13	VCC_48		
V15	VCC_49		
V16	VCC_50		
V17	VCC_51		
V20	VCC_52		
W12	VCC_53		
W18	VCC_54		
W19	VCC_55		

U1L
10CX220YF780E5G

M22	VCCH_GXBL_1	VCCERAM_1	R12
T22	VCCH_GXBL_2	VCCERAM_2	R17
V23	VCCR_GXBL1C_1		
V24	VCCR_GXBL1C_2		
P23	VCCR_GXBL1D_1	RREF_BL	AH25
P24	VCCR_GXBL1D_2	RREF_TL	C28
T23	VCCT_GXBL1C_1	DNU_1	AH23
T24	VCCT_GXBL1C_2	DNU_2	AH24
		DNU_3	Y11
M23	VCCT_GXBL1D_1	DNU_4	Y12
M24	VCCT_GXBL1D_2	DNU_5	Y10

U1J
10CX220YF780E5G

H11	TEMPDIODEP		
H10	TEMPDIODEN		
E10	VSIGP_0	VREFP_ADC	K10
E11	VSIGN_0		
F11	VSIGP_1	VREFN_ADC	J10
G11	VSIGN_1	ADCGND	G10

U61
LT6656AIS6-1.25#TRMPBF

5	NC2	VOUT	6
3	NC1		
2	GND2		
1	GND1	VIN	4

J1
0022232021
Molex
0022232021
900-0022232021-ND

Q1
FDV305N

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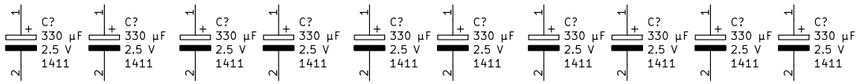
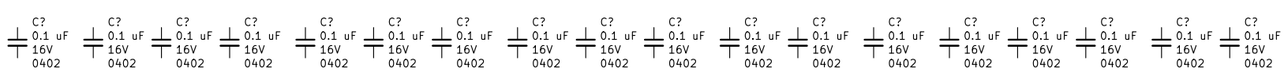
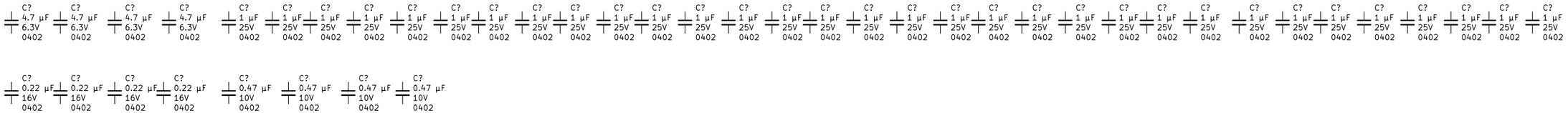
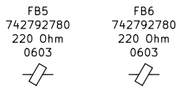
C10GX PWR Filter









Sheet: /14 – C10GX PWR Filter/
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Title:

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C10GX GND

U1M
10CX220YF780E5G

AB10	GND_1	GND_51	AF28
AD10	GND_2	GND_52	AF5
J13	GND_3	GND_53	AG12
H13	GND_4	GND_54	AG17
K14	GND_5	GND_55	AG2
L13	GND_6	GND_56	AG22
L14	GND_7	GND_57	AG24
M13	GND_8	GND_58	AG25
J14	GND_9	GND_59	AG26
A10	GND_10	GND_60	AG7
A15	GND_11	GND_61	AH14
A20	GND_12	GND_62	AH19
A25	GND_13	GND_63	AH26
A5	GND_14	GND_64	AH27
AA10	GND_15	GND_65	AH4
AA24	GND_16	GND_66	AH9
AA25	GND_17	GND_67	B17
AA26	GND_18	GND_68	B2
AB17	GND_19	GND_69	B22
AB2	GND_20	GND_70	B27
AB22	GND_21	GND_71	B28
AB24	GND_22	GND_72	B7
AB27	GND_23	GND_73	C19
AB28	GND_24	GND_74	C24
AB7	GND_25	GND_75	C25
AC24	GND_26	GND_76	C26
AC25	GND_27	GND_77	C27
AC26	GND_28	GND_78	C4
AC4	GND_29	GND_79	C9
AC9	GND_30	GND_80	D1
AD1	GND_31	GND_81	D11
AD11	GND_32	GND_82	D16
AD16	GND_33	GND_83	D21
AD21	GND_34	GND_84	D24
AD24	GND_35	GND_85	D27
AD27	GND_36	GND_86	D28
AD28	GND_37	GND_87	D6
AD6	GND_38	GND_88	E13
AE13	GND_39	GND_89	E24
AE18	GND_40	GND_90	E25
AE24	GND_41	GND_91	E26
AE25	GND_42	GND_92	E3
AE26	GND_43	GND_93	E8
AE3	GND_44	GND_94	F10
AE8	GND_45	GND_95	F20
AF10	GND_46	GND_96	F24
AF15	GND_47	GND_97	F27
AF20	GND_48	GND_98	F28
AF24	GND_49	GND_99	F5
AF27	GND_50	GND_100	G12

U?N
10CX220YF780E5G

G17	GND_101	GND_151	P21
G2	GND_102	GND_152	P22
G22	GND_103	GND_153	P27
G24	GND_104	GND_154	P28
G25	GND_105	GND_155	R13
G26	GND_106	GND_156	R18
H14	GND_107	GND_157	R21
H24	GND_108	GND_158	R22
H27	GND_109	GND_159	R25
H28	GND_110	GND_160	R26
H4	GND_111	GND_161	R3
J1	GND_112	GND_162	R8
J11	GND_113	GND_163	T10
J16	GND_114	GND_164	T15
J21	GND_115	GND_165	T20
J24	GND_116	GND_166	T21
J25	GND_117	GND_167	T27
J26	GND_118	GND_168	T28
J6	GND_119	GND_169	U12
K13	GND_120	GND_170	U17
K24	GND_121	GND_171	U2
K27	GND_122	GND_172	U21
K28	GND_123	GND_173	U22
K3	GND_124	GND_174	U25
L10	GND_125	GND_175	U26
L15	GND_126	GND_176	V14
L20	GND_127	GND_177	V19
L21	GND_128	GND_178	V21
L22	GND_129	GND_179	V22
L23	GND_130	GND_180	V27
L24	GND_131	GND_181	V28
L25	GND_132	GND_182	V9
L26	GND_133	GND_183	W1
M12	GND_134	GND_184	W11
M17	GND_135	GND_185	W16
M2	GND_136	GND_186	W22
M21	GND_137	GND_187	W25
M27	GND_138	GND_188	W26
M28	GND_139	GND_189	Y13
N14	GND_140	GND_190	Y22
N19	GND_141	GND_191	Y23
N21	GND_142	GND_192	Y24
N22	GND_143	GND_193	Y27
N25	GND_144	GND_194	Y28
N26	GND_145	GND_195	Y3
N4	GND_146		
N9	GND_147		
P1	GND_148		
P11	GND_149		
P16	GND_150		

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10CX220YF780E5G

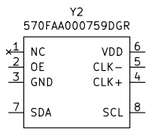
G14	NC_1	NC_52	G7
K11	NC_2	NC_53	G3
K12	NC_3	NC_54	P6
F12	NC_4	NC_55	F3
G16	NC_5	NC_56	M7
D12	NC_6	NC_57	F2
J12	NC_7	NC_58	L5
H12	NC_8	NC_59	E2
F14	NC_9	NC_60	D2
G13	NC_10	NC_61	C2
J15	NC_11	NC_62	D3
H15	NC_12	NC_63	D4
F16	NC_13	NC_64	A2
E12	NC_14	NC_65	A3
G15	NC_15	NC_66	F4
K15	NC_16	NC_67	E4
F13	NC_17	NC_68	A4
J9	NC_18	NC_69	B4
K9	NC_19	NC_70	B3
G9	NC_20	NC_71	C3
F9	NC_21	NC_72	G5
L8	NC_22	NC_73	G6
L9	NC_23	NC_74	N5
J8	NC_24	NC_75	P5
H8	NC_25	NC_76	M5
F7	NC_26	NC_77	M6
F6	NC_27	NC_78	K5
F8	NC_28	NC_79	J5
G8	NC_29	NC_80	R6
D7	NC_30	NC_81	R7
C7	NC_31	NC_82	N6
A7	NC_32	NC_83	P7
A6	NC_33	NC_84	P9
E7	NC_34	NC_85	P8
E6	NC_35	NC_86	L7
C6	NC_36	NC_87	M8
C5	NC_37	NC_88	K7
B6	NC_38	NC_89	J7
B5	NC_39	NC_90	G4
E5	NC_40	NC_91	H5
F1	NC_41	NC_92	L6
D5	NC_42	NC_93	K6
E1	NC_43	NC_94	N7
M9	NC_44	NC_95	N8
C1	NC_45	NC_96	H6
R9	NC_46	NC_97	H7
B1	NC_47	NC_98	AF4
K8	NC_48	NC_99	AG4
H3	NC_49	NC_100	AD5
H9	NC_50	NC_101	AE5
J4	NC_51		

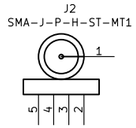
Sheet: /15 – C10GX GND/
File: 15 – C10GX GND.sch

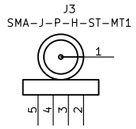
Title:

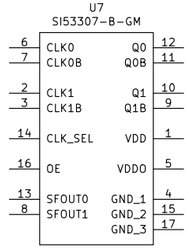
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 15/43

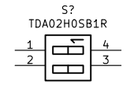
CLOCK – SI570/Si5332

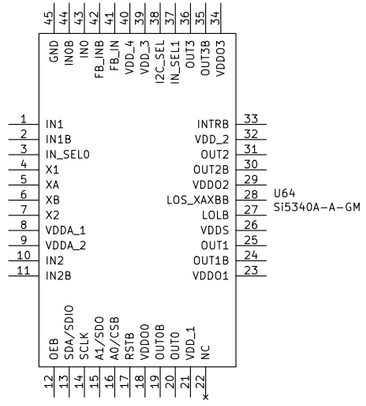


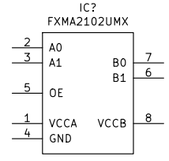










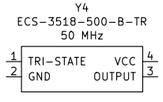


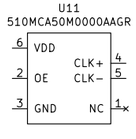
Sheet: /16 – CLOCK – SI570–Si5332/
File: 16 – CLOCK – SI570–Si5332.sch

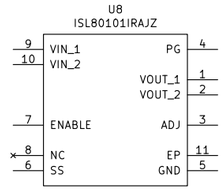
Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)–1		Id: 16/43

CLOCK – SI5340







Sheet: /17 – CLOCK – SI5340/
File: 17 – CLOCK – SI5340.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 17/43

EMIF-DDR3-1

U12
IS43TR16256B-107MBLI

N3	A0	DQ0	E3
P7	A1	DQ1	F7
P3	A2	DQ2	F2
N2	A3	DQ3	F8
P8	A4	DQ4	H3
P2	A5	DQ5	H8
R8	A6	DQ6	G2
R2	A7	DQ7	H7
T8	A8	DQ8	D7
R3	A9	DQ9	C3
L7	A10_AP	DQ10	C8
R7	A11	DQ11	C2
N7	A12_BC_L	DQ12	A7
T3	A13	DQ13	A2
T7	A14	DQ14	B8
		DQ15	A3
M2	BA0		
N8	BA1	LDQS	F3
M3	BA2	LDQS_L	G3
K9	CKE0	UDQS	C7
J9	CKE1	UDQS_L	B7
J7	CK	LDM	E7
K7	CK_L	UDM	D3
L2	CS0_L	VDDQ1	A1
L1	CS1_L	VDDQ2	A8
		VDDQ3	C1
K3	CAS_L	VDDQ4	C9
L3	WE_L	VDDQ5	D2
J3	RAS_L	VDDQ6	E9
		VDDQ7	F1
T2	RESET_L	VDDQ8	H2
		VDDQ9	H9
K1	ODT0		
J1	ODT1		
L8	ZQ0		
L9	ZQ1		
B2	VDD1	VREFCA	M8
D9	VDD2	VDDEFDQ	H1
G7	VDD3		
K2	VDD4		
K8	VDD5		
N1	VDD6	VSS1	A9
N9	VDD7	VSS2	B3
R1	VDD8	VSS3	E1
R9	VDD9	VSS4	G8
		VSS5	J2
B1	VSSQ1	VSS6	J8
B9	VSSQ2	VSS7	M1
D1	VSSQ3	VSS8	M9
D8	VSSQ4	VSS9	P1
E2	VSSQ5	VSS10	P9
E8	VSSQ6	VSS11	T1
F9	VSSQ7	VSS12	T9
G1	VSSQ8		
G9	VSSQ9	RFU	M7

U13
IS43TR16256B-107MBLI

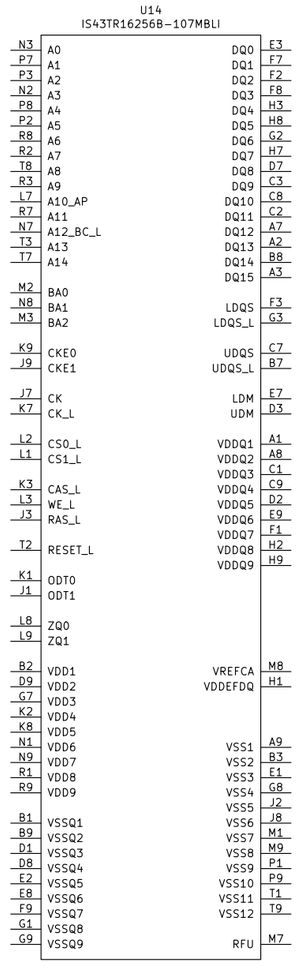
N3	A0	DQ0	E3
P7	A1	DQ1	F7
P3	A2	DQ2	F2
N2	A3	DQ3	F8
P8	A4	DQ4	H3
P2	A5	DQ5	H8
R8	A6	DQ6	G2
R2	A7	DQ7	H7
T8	A8	DQ8	D7
R3	A9	DQ9	C3
L7	A10_AP	DQ10	C8
R7	A11	DQ11	C2
N7	A12_BC_L	DQ12	A7
T3	A13	DQ13	A2
T7	A14	DQ14	B8
		DQ15	A3
M2	BA0		
N8	BA1	LDQS	F3
M3	BA2	LDQS_L	G3
K9	CKE0	UDQS	C7
J9	CKE1	UDQS_L	B7
J7	CK	LDM	E7
K7	CK_L	UDM	D3
L2	CS0_L	VDDQ1	A1
L1	CS1_L	VDDQ2	A8
		VDDQ3	C1
K3	CAS_L	VDDQ4	C9
L3	WE_L	VDDQ5	D2
J3	RAS_L	VDDQ6	E9
		VDDQ7	F1
T2	RESET_L	VDDQ8	H2
		VDDQ9	H9
K1	ODT0		
J1	ODT1		
L8	ZQ0		
L9	ZQ1		
B2	VDD1	VREFCA	M8
D9	VDD2	VDDEFDQ	H1
G7	VDD3		
K2	VDD4		
K8	VDD5		
N1	VDD6	VSS1	A9
N9	VDD7	VSS2	B3
R1	VDD8	VSS3	E1
R9	VDD9	VSS4	G8
		VSS5	J2
B1	VSSQ1	VSS6	J8
B9	VSSQ2	VSS7	M1
D1	VSSQ3	VSS8	M9
D8	VSSQ4	VSS9	P1
E2	VSSQ5	VSS10	P9
E8	VSSQ6	VSS11	T1
F9	VSSQ7	VSS12	T9
G1	VSSQ8		
G9	VSSQ9	RFU	M7

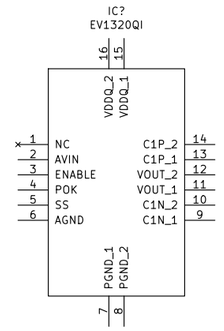
Sheet: /18 - EMIF-DDR3-1/
File: 18 - EMIF-DDR3-1.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 18/43

EMIF-DDR3-2





Sheet: /19 - EMIF-DDR3-2/
File: 19 - EMIF-DDR3-2.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 19/43

INTERFACE – PCIe

Sheet: /20 – INTERFACE – PCIe/
File: 20 – INTERFACE – PCIe.sch

Title:

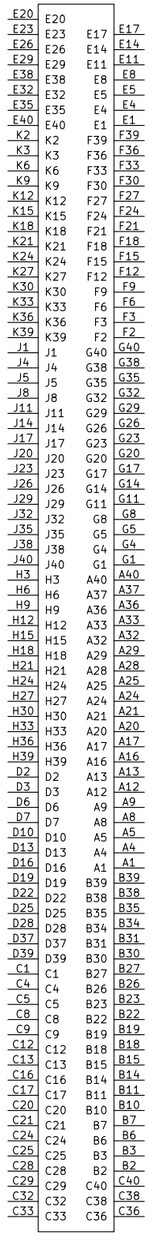
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 20/43

INTERFACE - SFP+

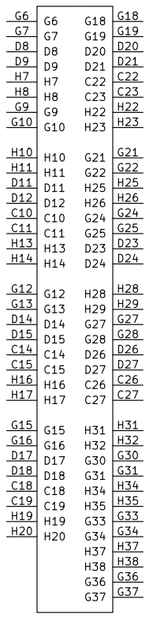
Sheet: /21 - INTERFACE - SFP+/	
File: 21 - INTERFACE - SFP+.sch	
Title:	
Size: B	Date:
KiCad E.D.A. kicad (5.1.9)-1	**Rev:** Id: 21/43

INTERFACE – FMC – 1

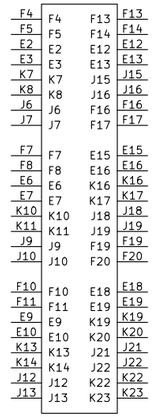
J7F
ASP-134486-01
Samtec Inc.
ASP-134486-01
SAM8728CT-ND



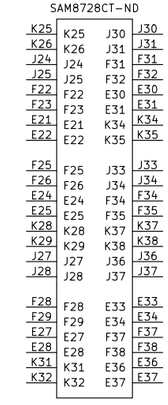
J7A
ASP-134486-01
Samtec Inc.
ASP-134486-01
SAM8728CT-ND



J7B
ASP-134486-01
Samtec Inc.
ASP-134486-01
SAM8728CT-ND



J7C
ASP-134486-01
Samtec Inc.
ASP-134486-01
SAM8728CT-ND



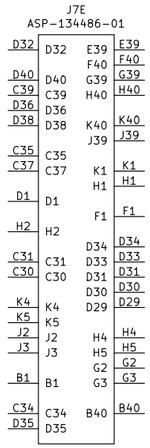
FMC cards Supported:
12G SDI: Semtech RDK-12GSRD-ALTRA00 Evaluation Board
8G Displayport: Bitec FMC DisplayPort Daughter Card
6G HDMI 2.0: Bitec FMC HDMI Daughter Card

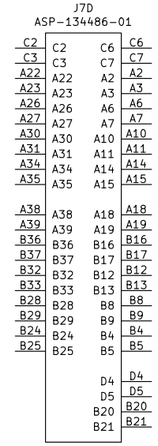
Sheet: /22 – INTERFACE – FMC – 1/
File: 22 – INTERFACE – FMC – 1.sch

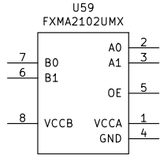
Title:

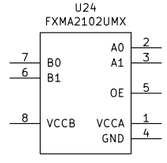
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 22/43

INTERFACE – FMC – 2









Sheet: /23 – INTERFACE – FMC – 2/
File: 23 – INTERFACE – FMC – 2.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 23/43

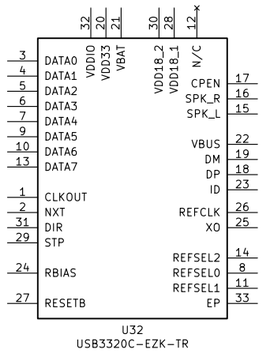
INTERFACE – USB3.1

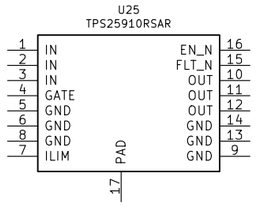
Sheet: /24 – INTERFACE – USB3.1/
File: 24 – INTERFACE – USB3.sch

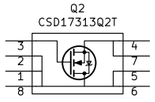
Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 24/43

INTERFACE – USB2.0







Sheet: /25 – INTERFACE – USB2.0/
File: 25 – INTERFACE – USB2.sch

Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 25/43

INTERFACE – GIGE – 1

RJ1
HFJ11-1G02ERL

	VCC	P9
	TDO_P	P1
	TDO_N	P2
	TD1_P	P3
	TD1_N	P6
	TD2_P	P4
	TD2_N	P5
	TD3_P	P7
	TD3_N	P8
MH2 MH1	GND	P10
MH2		
MH1		

U33A

A15	COMA	GTX_CLK	B4
B13	RESETn	TX_CLK	B2
		TX_EN	A5
A35	CONFIG0	TX_ER	A4
B30	CONFIG1		
A34	CONFIG2	TXD0	A6
A33	CONFIG3	TXD1	B6
B28	CONFIG4	TXD2	B7
A32	CONFIG5	TXD3	B8
B27	CONFIG6	TXD4	A9
		TXD5	B9
A16	MDI0+	TXD6	A10
A17	MDI0–	TXD7	B10
A18	MDI1+		
B16	MDI1–	RX_CLK	B1
A21	MDI2+	RX_DV	B44
A22	MDI2–	RX_ER	A2
B20	MDI3+		
A23	MDI3–	RXD0	A51
		RXD1	B43
A13	MDIO	RXD2	A50
A14	MDC	RXD3	A49
A12	INTn	RXD4	B42
		RXD5	A48
A20	HSDAC+	RXD6	A47
B18	HSDAC–	RXD7	B40
B14	RSET	CRS	B39
B26	SEL_FREQ	COL	A45
B11	125CLK	S_CLK+	A43
A30	XTAL1	S_CLK–	B37
B25	XTAL2	S_IN+	B38
A29	VSSC	S_IN–	A44
		S_OUT+	A42
A25	TRSTn	S_OUT–	A41
A26	TCK		
B21	TDI	LED_TX	B32
A27	TDO	LED_RX	A37
B22	TMS	LED_DUPLEX	B33
		LED_LINK1000	A39
		LED_LINK100	A40
		LED_LINK10	B35

U34
FXMA2102UMX

2	A0		
3	A1	B0	7
		B1	6
5	OE		
1	VCCA	VCCB	8
4	GND		

U35
FXMA2102UMX

2	A0		
3	A1	B0	7
		B1	6
5	OE		
1	VCCA	VCCB	8
4	GND		

Y7
SG-210STF_25.0000MY
25 MHz

1	EN	VCC	4
2	GND	OUT	3

D8
GREEN
D9
GREEN
D10
10
D11
GREEN
D12
GREEN

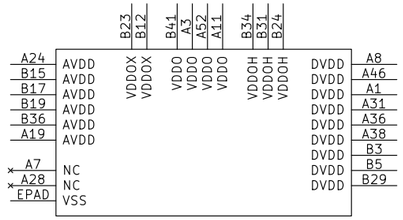
R240 220 OHM 0402
R243 220 OHM 0402
R246 220 OHM 0402
R249 220 OHM 0402
R250 220 OHM 0402

Sheet: /26 – INTERFACE – GIGE – 1/
File: 26 – INTERFACE – GIGE – 1.sch

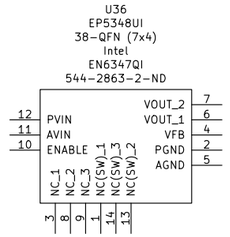
Title:

Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 26/43

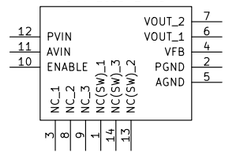
INTERFACE – GIGE – 2



U33B
88E1111–B2–NDC2C000
Marvell Semiconductor, Inc.
88E1111–B2–NDC2C000
30411–88E1111–B2–NDC2C000 Win Source Electronics



U36
EP5348UI
38–QFN (7x4)
Intel
EN6347QI
544–2863–2–ND



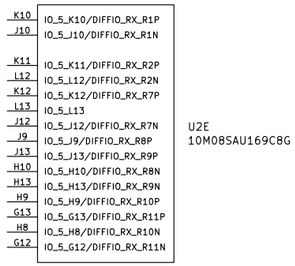
U37
EP5348UI
38–QFN (7x4)
Intel
EN6347QI
544–2863–2–ND

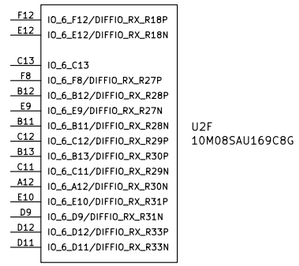
Sheet: /27 – INTERFACE – GIGE – 2/
File: 27 – INTERFACE – GIGE – 2.sch

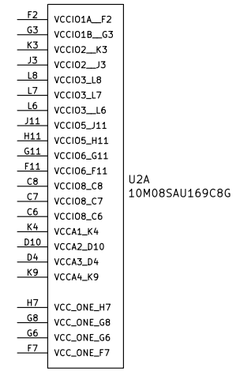
Title:

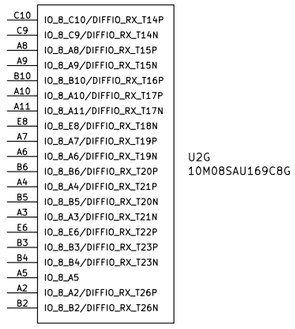
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)–1		Id: 27/43

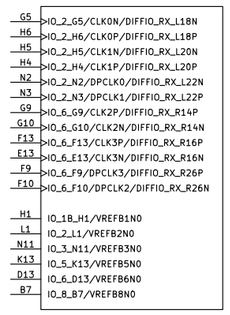
SYS MAX10 – CTRL

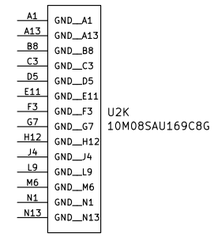


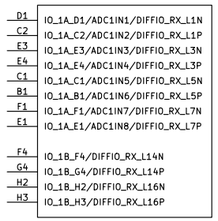


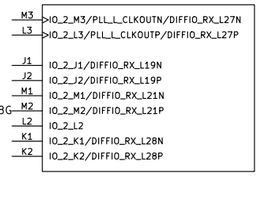


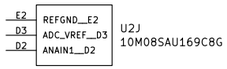












Sheet: /28 – SYS MAX10 – CTRL/
File: 28 – SYS MAX10 – CTRL.sch

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Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 28/43

SYS MAX10 – UBII

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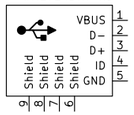
Molex
0475890001
WM17143TR-ND



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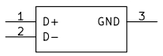

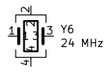

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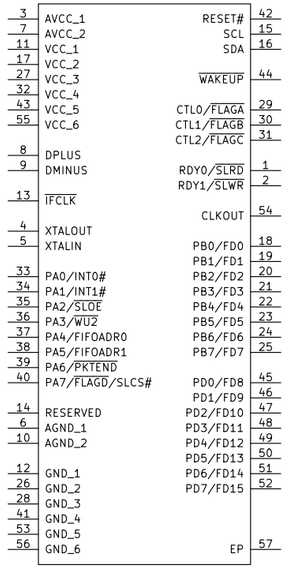
56-VFQFN Exposed Pad
Cypress Semiconductor Corp
CY7C68013A-56LTXC
428-4634-2-ND



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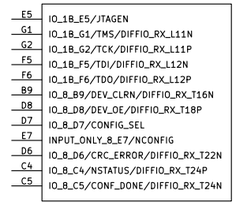

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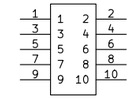
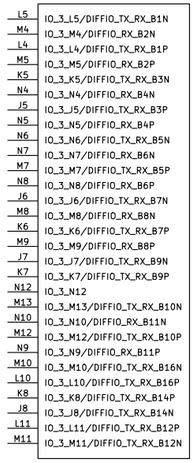
61001021121
732-5463-5-ND





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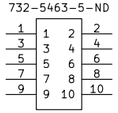

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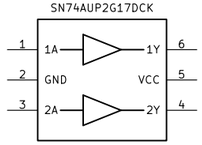

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File: 29 – SYS MAX10 – UBII.sch

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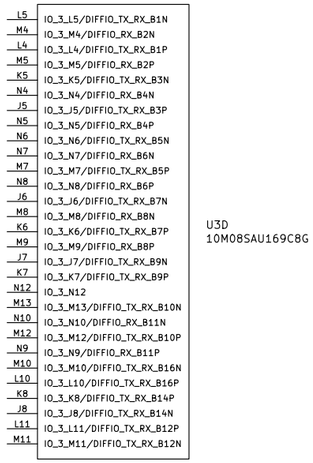
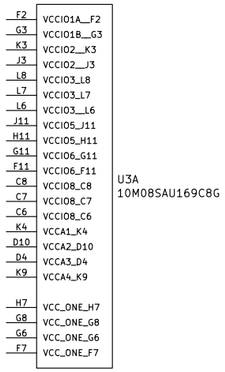
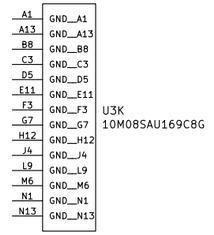
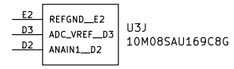
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 29/43

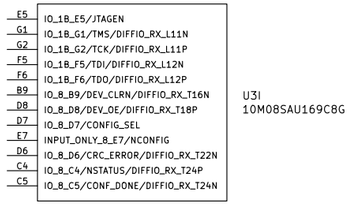
CFG MAX10 – FPP











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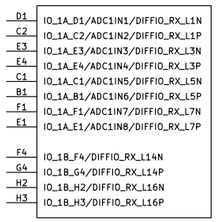
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 30/43

CFG MAX10 – PFL



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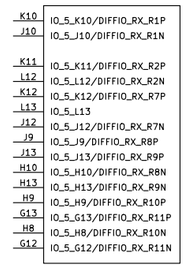

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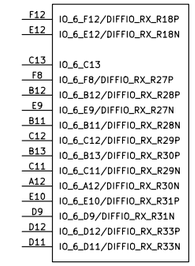

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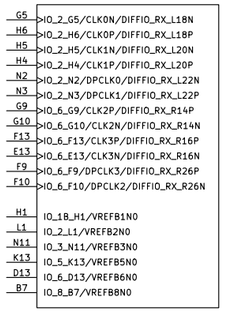

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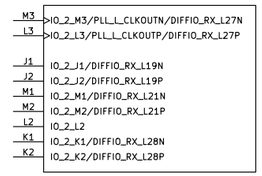

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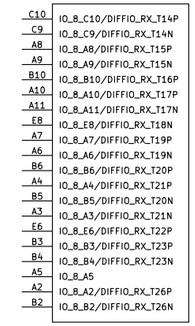

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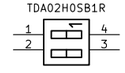

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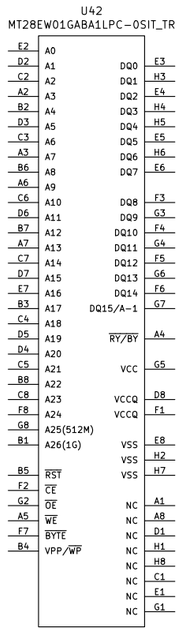
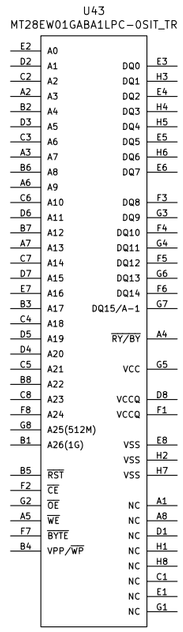
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 31/43

CFG PFL FLASH





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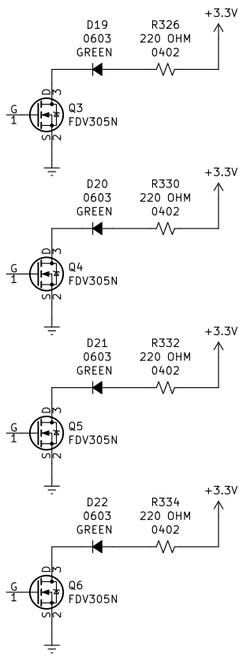
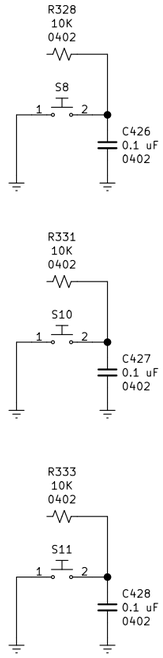
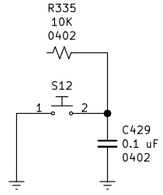
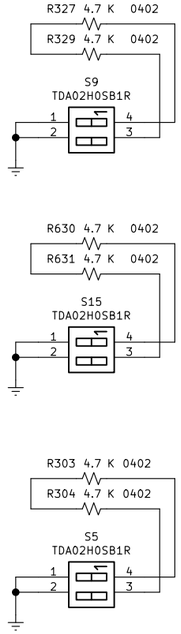
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 32/43

LED & PB & SW









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Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 33/43

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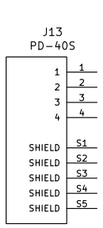

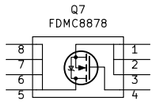

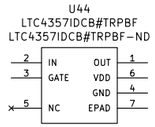

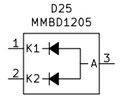

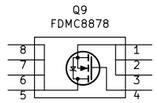

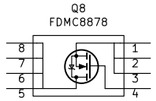

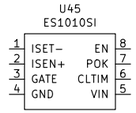

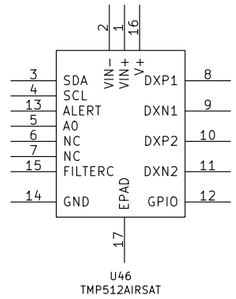

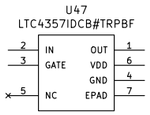

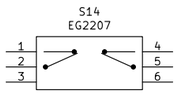

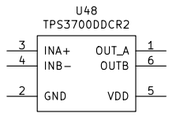

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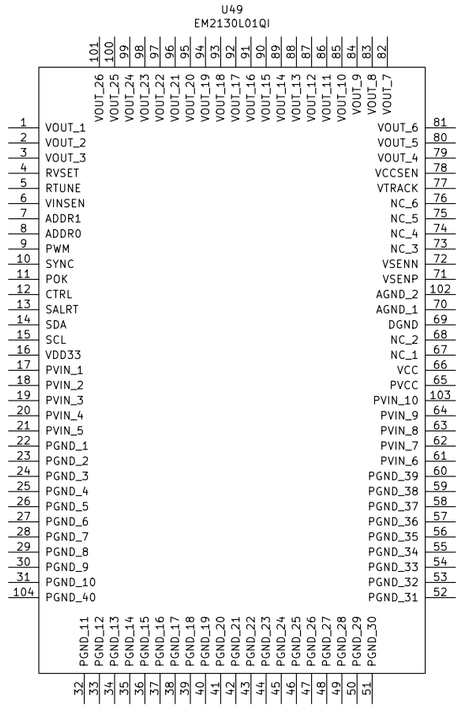
KiCad E.D.A. kicad (5.1.9)-1		Id: 34/43

POWER – 12V to 0.9V



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File: 35 – POWER – 12V to 0.sch

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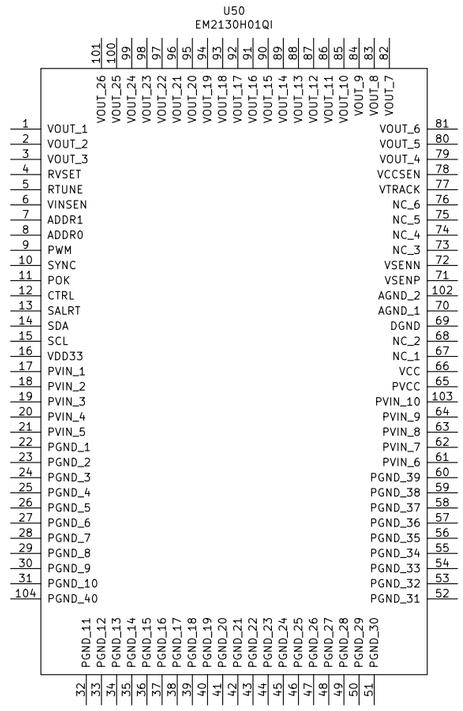
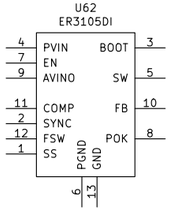
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 35/43

POWER – 12V to 3.3V





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File: 36 – POWER – 12V to 3.sch

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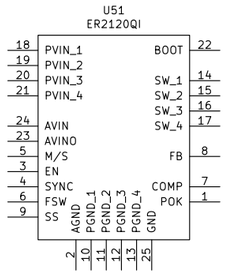
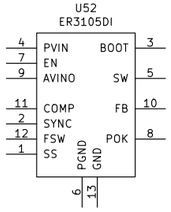
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 36/43

POWER – 12V to 5V





Sheet: /37 – POWER – 12V to 5V/
File: 37 – POWER – 12V to 5V.sch

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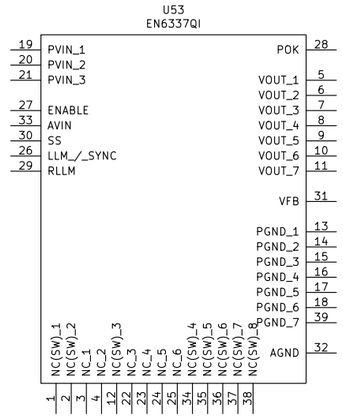
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 37/43

POWER – 3.3V to 0.95V



Sheet: /38 – POWER – 3.3V to 0.95V/
File: 38 – POWER – 3.3V to 0.sch

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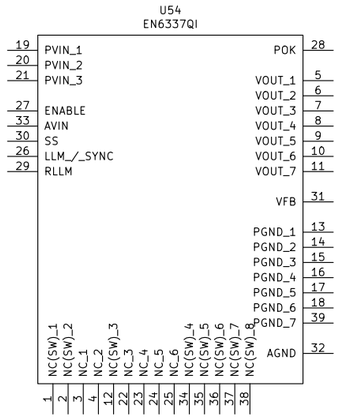
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 38/43

POWER – 3.3V to 1.5V



Sheet: /39 – POWER – 3.3V to 1.5V/
File: 39 – POWER – 3.3V to 1.sch

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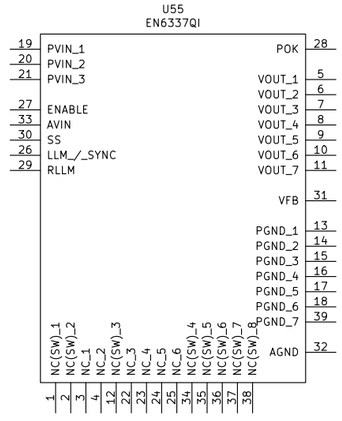
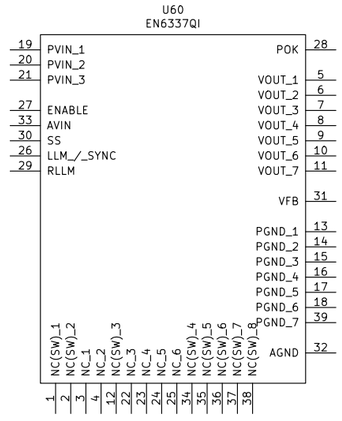
Size: B	Date:	**Rev:**
KiCad E.D.A. kicad (5.1.9)-1		Id: 39/43

POWER – 3.3V to 1.8V





Sheet: /40 – POWER – 3.3V to 1.8V/
File: 40 – POWER – 3.3V to 1.sch

Title:

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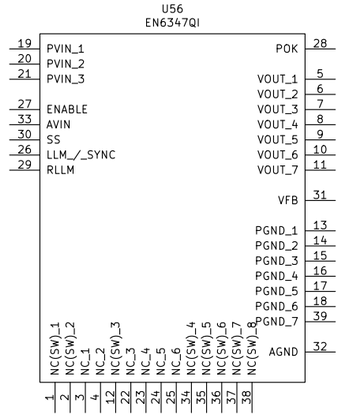
POWER – 3.3V to VADJ



Sheet: /41 – POWER – 3.3V to VADJ/
File: 41 – POWER – 3.sch

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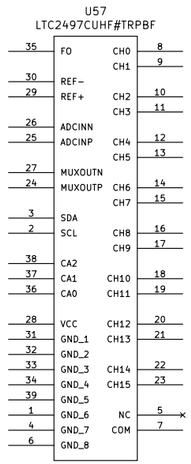
POWER – CURRENT SENSE



Sheet: /42 – POWER – CURRENT SENSE/
File: 42 – POWER – CURRENT SENSE.sch

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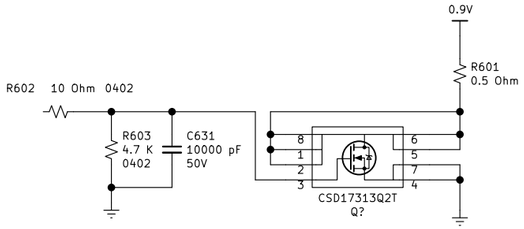
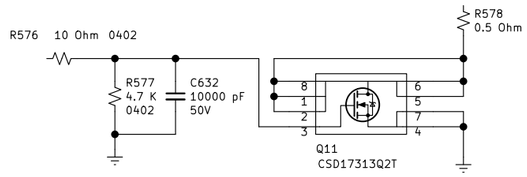
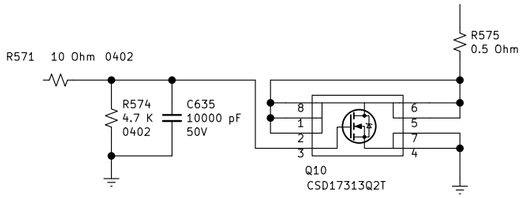
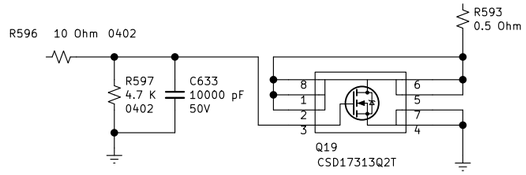
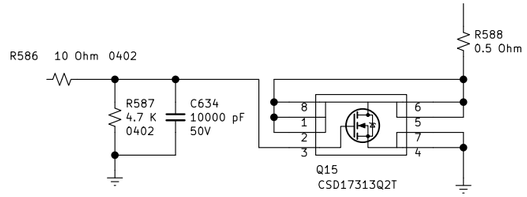
POWER – Fast Discharge

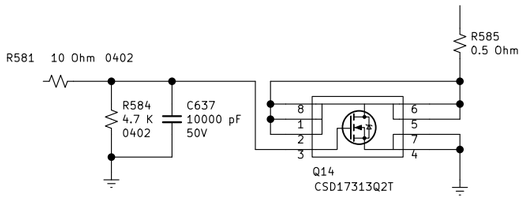


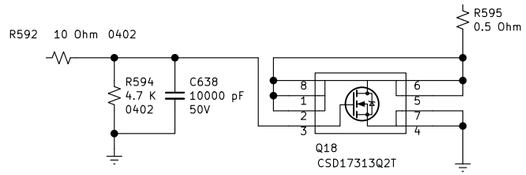


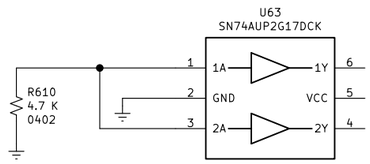












Sheet: /43 – POWER – Fast Discharge/
File: 43 – POWER – Fast Discharge.sch

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