**Filip Cakulev**: This week I cried a lot. I wrote an ewb module that works but is essentially useless because my l2 design sucked a lot. I spent the majority of my time staring at waveforms of my l2 cache to try and fix the writeback aspect of it as we were getting shadowmem errors after a certain time. After some solid hours I ended up with virtually 0 progress and would like this class to just be over. Thanks for listening to my Ted talk Sean. I guess next week I'll be working to debug the same issue and possibly fix my ewb as it is uncertain if it truly does something. Also we will work on the presentation aspect of the project. At the end of the day Robbie is a simp. Ricky did alright tho.

**Robbie Ernst:** This week I did two major things for the checkpoint. The first was implementing the basic hardware prefetcher which I actually had a pretty easy time doing, and I hooked up a performance counter for it as well. The second was completely changing the RVFI monitor to work with this checkpoint as it wasn't working correctly. So, I had to completely change it to account for our stalling and forwarding, and now it is functioning correctly. I also spent time debugging our cp4 code for this week, but ran into some issues with the cache we couldn't end up fixing. For this last checkpoint our goal is gonna be just to fix what we have.My weiner hurts.

**Ricky Machado:** For this week, Our team had to implement some advanced design features. I chose to do a tournament predictor. After careful paper design I designed it pretty well having a 97% accurate branch predictor. After getting this to work, I was able to have the datapath have no errors. However we realized cache was wrong and I wasted a lot of my Monday trying to get it to work. I wanna know if I should credit no credit this class. I like my gpa and I had to quit my job earlier this semester because it was riddled me with so much stress. Please help