# CAPP : A C++ Aspect-Oriented Based Framework for Parallel Programming with OpenCL

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#### **ABSTRACT**

Parallel programming can provide higher computational performance over a sequential implementation by making use of the many cores available in parallel systems. However, the parallel-capable devices introduce complexity into the programming model. Current parallel programming API's such as OpenCL and CUDA provide interfaces to the parallel devices, but are complex and result in code which includes cross-cutting components, such as setting up the parallel programming context, compiling the parallel kernel, and transferring data between the host and device memory spaces when the kernel is executed. A C++ Aspect-Oriented based Parallel Programming (CAPP) framework is developed, using AspectC++ and OpenCL, which defines aspects to remove the cross-cutting components from the C++ code. The aspects set up the OpenCL context, compile the OpenCL kernel, and manage the data transfer between the memory spaces each time a kernel is executed. The aspects are woven into the C++ code before compilation, rather than at run time, which improves performance. An interface is provided for executing OpenCL kernels from the C++ code, essentially providing parallel programming in C++. The framework was applied to the SAXPY and Black-Scholes option pricing problems. Computational performance was, on average, 1.4-7% slower than the OpenCL implementation and up to 9 times faster than the sequential C++ implementation for the Black-Scholes problem. The amount of code was greatly reduced from the OpenCL implementation, and the resulting CAPP framework C++ code was simple and modular, resembling the sequential C++ implementation code. The amount of CAPP code required for the examples was found to be comparable to that of C++ AMP, another parallel programming framework.

# **CCS Concepts**

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SAICSIT '15 September 28–30, 2015, Stellenbosch, ZAR

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DOI: 10.1145/1235

rallel architectures;  $\bullet Software$  and its engineering  $\rightarrow$  Context specific languages;

# **Keywords**

CAPP; Aspect; Parallel; Cross-cutting; Device; Host

### 1. INTRODUCTION

In recent years CPU core frequencies have started to plateau [7]. Multiple core CPUs and many core GPUs provide a solution to the physical limitations causing the plateau, and allow computational performance to once again follow Moore's law. The cores used for CPUs and GPUs differ in complexity and are hence advantageous for different tasks. GPUs use hundreds to thousands of simple cores to increase computational performance, while CPUs use fewer, complex cores which generally have higher frequencies than those used in GPUs. Due to the number of cores available when using a GPU, GPUs are suited to data parallel tasks where the same operation can be performed on each element of a high dimensional dataset. Modern CPUs can also provide data parallelism through single instruction multiple data (SIMD) operations and their numerous cores. However, due to having fewer cores they provide less dramatic increases in performance and require large amounts of power to perform the instruction level parallelism [12]. General-Purpose GPU (GPGPU) programming involves combining CPUs and GPUs into a single, hybrid system which provides increased computational performance by using the CPU (host) to pass data to the GPU (device) which performs the computation on the data in a parallel manner. For GPGPU programming there are two main API's available to the programmer: OpenCL [10] and CUDA [14].

OpenCL is an attempt to provide a standard API for programming parallel-capable hardware. It provides support for all main CPU and GPU hardware vendors, namely Nvidia, AMD and Intel. This wide range of support is advantageous as a parallel implementation using the OpenCL API could run simultaneously on the CPU and GPU, maximising the hardware capabilities of the parallel system. CUDA applies a similar methodology and also provides an API for writing programs which can be executed on parallel-capable hardware, however, it is specific to Nvidia hardware and hence parallel kernels cannot be executed on CPUs or GPUs from any other hardware vendor.

These parallel systems are more difficult to program than sequential systems. This is mostly due to the addition of the

external devices, their low level nature and that communication is required between the device and the host. This is illustrated in Listing 2 through a vector addition example. To perform parallel computation on the device using Open-CL the generalised sequence of events, which is similar to the generalised sequences of events for a CUDA C program [8], is shown below. The colours after the description relate to the examples given in Listings 1 and 2.

- 1. Initialize the data on the host (Green)
- 2. Setup the OpenCL variables, which involves (Red):
  - Setting the OpenCL platform
  - Creating the OpenCL Context
  - Setting the parallel device(s) to use
  - Creating the OpenCL command queue
- 3. Create the OpenCL kernel (Blue)
- 4. Create the OpenCL buffers and move the data from the host to the device (Magenta)
- 5. Execute the kernel on the parallel device(s) (Green)
- Move the results from device memory back to the host memory (Magenta)

This is a substantial amount of work to perform parallel computation. Comparatively, to perform the same computation on the host only steps 1 and 5 defined above are required. All other steps increase the complexity and crosscut the main intention of the C++ code, which for parallel programming would be the application of an algorithm on the data. This overhead is illustrated by a comparison of Listings 1 and 2 which provide a simple vector addition implementation using C++ and OpenCL respectively.

The need for GPGPU programming arises from the computational complexity of algorithms which results in non-GPGPU systems not being able to perform the computation in a sufficiently small amount of time. The additional complexities require programmers to learn the complex OpenCL or CUDA API's, hence increasing the development time and difficulty of parallel solutions. However, the computational performance increase provided by GPGPU programming is substantial and thus it is desirable to reduce the complex, low-level interaction between the host and device.

To remove the requirement of the programmer having to deal with the parallel programming complexities, Aspect-Oriented Programming (AOP) [11] can provide a solution which allows the cross-cutting components to be modularized into aspects which are not part of the C++ files, but are rather separately defined. This results in simple C++ code consisting of code only directly related to the program's intention. The aspects are *woven* into the C++ files before compilation so that when the code is compiled the cross-cutting code is present.

An AOP implementation for C++ has been available since 2001 in the form of AspectC++ [5, 18]. However, the use of aspects in low-level parallel programming is limited. While there are very few examples of using aspects to modularize the complexities of parallel programming, numerous proposals have been made which aim to reduce the complexities using object-orientated API's or new parallel languages. This paper presents the C++ Aspect-Oriented based Parallel Programming (CAPP) framework which makes

```
void VectAdditionC++(int argc, char** argv) {
    // Instantiate vector addition class which
    // encapsulates the vector addition algorithm
    VectAddCppClass vectAdd;
    // Declare data vectors
    vector<float> in1, in2, out;
    // Fill data vectors
    for (int i = 0; i < NUM_ELEMENTS; ++i) {
        out.push_back(0.f);
        in1.push_back(rand());
        in2.push_back(rand());
    }
    // Execute Kernel
    vectAdd.RunKernel(in1, in2, out);
}</pre>
```

Listing 1: Vector addition on the host using C++.

use of aspects, implemented using AspectC++, to hide the above mentioned complexities present in OpenCL parallel programming from the programmer.

The aspects modularise both static components - the initialisation of the OpenCL variables - and dynamic components - creating the relevant buffers and allocating and deallocating memory on the host and device when kernels are executed. The kernel function is not dealt with by the CAPP framework. This decision was made as the kernel is specific to the implementation of the algorithm and would involve significant complexity to allow a general aspect to convert a C++ function to an OpenCL kernel without loss of performance. To write an OpenCL kernel the programmer does not need to learn the OpenCL API, however, he/she does require an understanding of how parallel computation is performed in terms of the thread arrangement. It assumes that the programmer would have an understanding of how their algorithm is parallelized and hence could implement the kernel. Future improvements which remove this requirement are discussed in Section 7.

The CAPP framework has two main goals:

- To allow for a parallel implementation which is comparatively simple, in terms of code structure and number
  of lines, with a sequential, C++ only implementation
- To provide performance which is comparable with a non-aspect, parallel implementation written using Open-CL or CUDA

The rest of the report is structured as follows: Section 2 reviews work related to the simplification of parallel programming. Section 3 describes the CAPP framework and the use of AspectC++, and its features, to achieve the above mentioned goals. Section 4 presents the results of CAPP, CPU and OpenCL implementations to two problem domains: SAXPY and Black Scholes option pricing. Section 5 discusses the results and comments on the limitations of the CAPP framework. Section 6 concludes and Section 7 provides possible directions of exploration for future work in this area.

# 2. RELATED WORK

With the increasing popularity and necessity of parallel implementations, attempts to minimise the complexity of writing parallel code have both increased in number and made the task simpler. OpenCL and Nvidia CUDA are both examples of this. They provide extensions to the C language which are interfaces to the low-level parallel hardware.

```
void VectAdditionCl(int argc, char** argv) {
 // Declare OpenCl variables
 vector < cl::Platform > platforms;
 vector <cl::Device>
                        devices:
 vector < cl::Buffer >
                        buffers;
 cl::Context
                        context:
 cl::CommandQueue
                        queue;
 cl::Program
                        program;
 cl::Kernel
                        kernel;
 // Declare data vectors
 vector < vector < float >>
                             inputs;
 vector <float > in1, in2, out;
  // Fill data vectors
 for (int i = 0; i < NUM_ELEMENTS; i++) {</pre>
    out.push_back(0.f);
    in1.push_back(rand());
    in2.push back(rand()):
 inputs.push back(in1): inputs.push back(in2):
 // Get OpenCL Platforms
 clPlatform::get(&platforms);
  // Create context parameters
  cl_context_properties cps[3] = {
    CL CONTEXT PLATFORM.
    (cl_context_properties)(platforms[0])(),
 }:
 // Create OpenCL context
  context = cl::Context(CL_DEVICE_TYPE_GPU, cps);
  // Get available devices
 devices = context.getInfo<CL_CONTEXT_DEVICES>();
 // Create command queue
  queue = cl::CommandQueue(context, devices[0]);
  // Get kernel source
  ifstream kSource("vectadd.cl");
 // Convert kernel source to string
  string kstring(
    istreambuf_iterator < char > (kSource),
    (istreambuf_iterator < char > ()) );
  // Create OpenCL program source
  cl::Program::Sources source(1,
    make_pair(kstring.c_str(),
              kstring.length() + 1) );
 // Create OpenCL program
 program = cl::Program(context, source);
  // Create OpenCL kernel
 kernel = cl::Kernel(program, "vectadd");
    Create input buffers
  for (auto& input : inputs) {
    buffers.emplace_back(context,
      CL_MEM_READ_ONLY,
      input.size() * sizeof(float) );
    queue.enqueueWriteBuffer(buffers.back(),
      CL_TRUE, 0, input.size() * sizeof(float),
            &input[0]);
  // Create output buffer
 buffers.emplace_back(context, CL_MEM_WRITE_ONLY,
   out.size() * sizeof(T) );
   / Set kernel arguments
 for (int i = 0; i < buffers.size(); i++) {
    kernel.setArg(i, buffers[i]);
 // Set thread dimensions
 cl::NDRange global(NUM_ELEMENTS);
 cl::NDRange local(1);
 // Execute Kernel
 queue.enqueueNDRangeKernel(kernel,
    cl::NullRange, global, local);
    Get results from GP
 float* res[NUM_ELEMENTS];
 queue.enqueueReadBuffer(buffers.back(),
    CL TRUE, 0, out.size() * sizeof(float), res):
```

Listing 2: Vector addition on the device using OpenCL which highlights the different cross-cutting components.

As mentioned in Section 1 this introduces cross-cutting code which 'tangles' the C++ code. Additionally the API's are extensive which require the programmer to invest a significant of time amount to learn before writing a parallel program.

APIs, frameworks and entirely separate languages have been proposed which hide the low-level interactions with parallel-capable hardware from the programmer. These systems generally provide wrappers around the low-level interfaces which often reduces the performance - a key consideration in parallel programming.

CuPP [1] provides a C++ framework designed to increase the ease of implementing parallel applications using CUDA. It provides both low-level and high-level interfaces which interact with the parallel-capable hardware, hence providing a C++ interface for programming parallel applications. This is also an attempt to wrap the cross-cutting code into modules such that it is hidden from the programmer, but in an object-oriented manner. The object-orientated additions are not external to the C++ code, but rather form part of the C++ code, intoducing cross-cutting concerns which results in more code than what is required for a CUDA implementation. Since the framework is built around CUDA, support is only provided for Nvidia devices.

CAF [4, 3] uses the actor model to allow computation to be performed in a distributed manner. It provides bindings for OpenCL which allow the programmer to specify only the OpenCL kernel from which CAF creates an actor capable of executing the kernel. The framework allows computation on a vast number of parallel-capable devices, which is a notable feature. However, providing support for so many devices introduces overhead and makes the framework very large. Furthermore, it requires learning the actor model which introduces additional complexity into the parallel programming process.

C++ AMP [13] is a Microsoft product and provides extensions to the C++ language which allow data parallel-capable hardware to be used to accelerate computation. Parallel kernels can be written as C++ lambda functions, requiring no knowledge of the language specific to the parallel hardware. C++ AMP is primarily for the Windows environment, which is contrary to the platform-independent intention of CAPP. MulticoreWare, however, are working with Microsoft to develop an extension to the Clang compiler to allow C++ AMP to be platform independent.

RapidMind [16] and Brook [2], while being similar to standard C++, essentially define new languages for parallel programming.

RapidMind provides a parallel programming environment which, by taking advantage of C++ template metaprogramming, provides the programmer with three data types: Value, Array, and Program. The Value and Array data types hold data elements and groups of data elements, respectively. The Program data type stores operations which can act on Array data types in a parallel manner. The Program data type is essentially the same concept as the kernel provided by Nvidia and OpenCL, but in the C++ language. RapidMind also makes use of macros which add complexity to the code, rather than simplifying the code – which is the intention of aspects.

Brook provides high-level abstractions to hide the low-level parallel programming complexities from the programmer. Similarly to RapidMind there are three abstractions

which create the high-level parallel programming environment: *Stream*, *Kernel*, and *Reduction*. The *Stream* deals with the data, while the *Kernel*, similar to both the kernel in CUDA and OpenCL and the *Program* data type for RapidMind, allow operations to be defined which act on the *Streams* (data). The *Reduction* is provided to generate a result from a high dimensional *Stream*.

Both RapidMind and Brook do a lot of work at run time to move the data to the device memory and to allow parallel computation to be performed on the device. This reduces their performance when compared with CUDA or OpenCL implementations [20] which has led to these parallel environments not gaining high levels of acceptance within the parallel development community.

Work done on the use of aspects for parallel programming is limited, especially for low-level programming which interfaces directly with the parallel hardware. Use of aspects in a parallel context is proposed by [17]. However, Java is used and the model is based on multi-core CPUs, hence does not include the numerous complexities which arise from having additional computational devices such as GPUs.

A new parallel aspect language, based on AspectC++, is proposed by [19]. Features closely related to AspectC++, but more specific to parallel programming, are defined to hide most of the cross-cutting complexities from the programmer. They manage to reduce the cross-cutting components considerably and allow the core computations to be defined using C++. The aspect model defines a kernel feature which behaves in the same way as advice in AOP. Furthermore, the memory structure of the device is encapsulated using templates and allows the programmer to specify the type of device memory to be used, in C++. They provide a compiler to weave the aspects defined using their aspect language, into the C++ code. A performance reduction of only  $\approx 20\%$  of the CUDA implementation was achieved. The aspect language and compiler are not yet fully functional as well as being specific to parallel programming. This specificity is beneficial for parallel-only applications, however, does not lend itself to large C++ applications where only specific components may benefit from parallel acceleration. Additionally, it cannot be used to modularize non-parallel cross-cutting concerns into aspects.

## 3. THE CAPP FRAMEWORK

The CAPP framework presented in this section allows the programmer to write parallel programs in traditional C++ with the addition of specifying the kernel function to perform tasks on some data in a parallel manner. The aspects are written using AspectC++ and provide the functionality of the cross-cutting OpenCL code, hence 'untangling' the C++ code. The aspects are then woven into the C++ class before compilation to allow the cross-cutting functionality to exist within the C++ class at compile time. A high-level representation of the weaving process, and the minimum OpenCL functionality which is woven into the C++ class, is shown in Figure 1. The rest of this section explains the specific use of AspectC++ and its features in the CAPP framework to provide a modular parallel programming environment. The OpenCL vector addition example of Listing 2 is the reference on which the subsequent aspect listings are based.

#### 3.1 Abstract ClContext Aspect

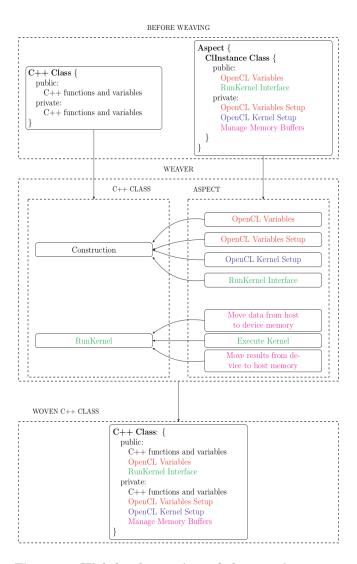


Figure 1: High-level overview of the weaving process for the CAPP framework. The output of the weaving process produces the code for compilation.

AspectC++ is based on C++ and hence provides functionality for both inheritance and polymorphism. The CAPP framework makes use of these features and defines an abstract aspect, the ClContext aspect, which provides the C++ code with the cross-cutting OpenCL components necessary for executing a parallel kernel. The OpenCL components, both variables and member functions, are defined in the abstract aspect and are inherited by the derived aspect. Any additional OpenCL functionality (which may improve specific parallel implementations) can then be defined in the derived aspect. Polymorphism is provided by AspectC++ through virtual pointcuts, which are used in the abstract ClContext aspect to define an interface for the derived aspect to specify which C++ classes require parallel capability. The CoreClasses pointcut in Listing 3 shows this. This abstract aspect provides all the necessary Open-CL functionality for a parallel-capable class, without defining which C++ class the aspect functionality should be woven into. Listing 4 shows an example of a derived aspect

for a C++ vector addition class (VectAddCppClass), which uses the virtual pointcut CoreClasses to define the C++ class into which the functionality must be woven.

# 3.2 OpenCL Class Introduction

Observing Listing 2, there are numerous OpenCL specific variables (variables defined below the red comments) which are required to setup the OpenCL environment. These are only the necessary variables – there are many more which provide additional features for parallel programming. Many of these variables must be modified or recreated each time a new kernel needs to be executed, requiring a significant amount of overhead when compared to the C++ version of Listing 1.

AspectC++ provides the slice feature for defining C++ classes within aspects. Furthermore, it allows for advice to be defined for a virtual pointcut. This advice can be implemented to use C++ inheritance which will allow the aspect weaver to weave the slice class as a base class for any C++ classes, or to derive from any C++ classes. Using these features, the CAPP framework defines the ClInstance slice class within the abstract ClContext aspect. The ClInstance class has the cross-cutting OpenCL variables as public variables, and the cross-cutting OpenCL operations as private member functions. The weaver then weaves the Clinstance class to be a base class of the C++ classes defined by the CoreClasses pointcut, so that the OpenCL variables are part of those C++ classes. This is known as introduction in aspect-orientated programming. Listing 3 shows the ClInstance slice class within the abstract aspect, as well as the cross-cutting variables (below the red comments as per Listing 2) and member functions which provide the cross-cutting OpenCL setup functionality.

# 3.3 OpenCL Setup Function Introduction

Again observing Listing 2, a large amount of the crosscutting code comes from the operations on the OpenCL variables. These operations (below the red comments in the Listings) are for the setup of the OpenCL environment, and determine the available devices and platforms, and create the OpenCL context. They cross-cut the C++ code since they are not directly related to the intention of the program and will need to be reimplemented for any class that is parallelised. Furthermore, these operations are generic – they are the same for any OpenCL program – allowing them to be defined as functions of the CInstance slice class. The operations for creating the kernel (below the blue comments in Listings) have the same problems and can therefore also be defined as functions of the ClInstance slice class.

The ClContext aspect then defines the ClSetup pointcut to specify where in the C++ classes these functions should be executed. The CAPP framework defines the functions to be executed on construction of the C++ class. This is done so that once the C++ class has been instantiated, all Open-CL variables will have been initialized to the appropriate values to allow parallel computation from the C++ class.

To provide the programmer with flexibility regarding the parallel context, the <code>ClSetup</code> advice makes use of AspectC++'s tjp pointer. The tjp pointer provides the aspect with access to the C++ class into which the aspect will be woven. Using  $tjp{>}that()$  in an aspect is equivalent to using this within a C++ class . This feature was used to allow the programmer to specify the computation device (CPU or GPU), the kernel

```
// ClContext.ah
aspect ClContext {
  class ClInstance {
  public:
    // OpenCL variables
    vector < cl::Platform >
                          platforms:
    vector < cl:: Device >
                           devices;
    vector < cl::Buffer >
                           buffers
    cl::clContext
                           context;
    cl::CommandQueue
                           queue;
    cl::Kernel
                           kernel:
    cl::Program
                           program;
    // Other variables and functions
  private:
    // Setup OpenCL variables
    void SetupOpenCl(string devType, string kSource);
    // Setup OpenCL kernel
    void SetupKernel(string kSource, string kName);
    // Manage buffers on kernel execution
    void ManageBuffers(vector<vector<float>>* inputs,
                        vector < vector < float >>* outputs
                             ):
  };
  // Interface for defining C++ classes
  pointcut virtual CoreClasses() = 0;
  // Specify ClInstance as baseclass of classes
  // defined by CoreClasses()
  advice CoreClasses() : baseclass(ClInstance);
  // Other aspect components
};
```

Listing 3: Abstract ClContext aspect which defines the OpenCL variables required for parallel programming.

```
#include"ClContext.ah"
aspect VectAdd : public ClContext {
    // Define C++ class to weave
    // OpenCL functionality into
    pointcut CoreClasses() = "VectAddCppClass";
};
```

Listing 4: Derived aspect defining a C++ class into which the OpenCL functionality should be woven.

source file, and kernel name as arguments of the C++ class constructor. Using tjp->that() allows the aspect to use the values provided by the programmer from the C++ class. The aspect can then set up the OpenCL environment to the programmer's preference. Listing 5 shows the definition of ClSetup pointcut, and its advice which calls the private member functions of the ClInstance class, as defined in Listing 3.

#### 3.4 RunKernel Interface

Executing the kernel is the main component of a parallel application. Before the kernel is run, the data on which the kernel operates must be moved from the memory of the host to the memory of the device. Once the kernel has finished executing, the results reside in the memory of the device and are not available to the host until transferred back from the device memory to the host memory. In OpenCL this is done using the cl::Buffer type and specifying if the device memory must be read from or written to. These operations are shown in Listing 2 below the magenta comments.

This process of memory allocation, movement, and deallocation on each call of the kernel is not required for a C++ im-

```
// ClContext.ah
aspect ClContext {
  class ClInstance {
    // As per Listing 3 ...
  // Other pointcuts and advice
  // Pointcut which defines where the OpenCL
  // setup function should be woven
 pointcut ClSetup = construction(CoreClasses());
  // Advice specifying how the OpenCL
  // setup functions should be woven
  advice ClSetup() : around() {
    // Setup OpenCL variables
    tjp->that()->SetupOpenCl(
      tjp->that()->devType, tjp->that()->kSource);
    // Setup OpenCL kernel
    tjp->that()->SetupKernel(
      tjp->that()->kSource, tjp->that()->kName);
    // Continue with core class constructor
    tjp->proceed();
}:
```

Listing 5: Abstract aspect with the pointcut and advice for OpenCL setup.

```
// ClContext.ah
aspect ClContext {
  class ClInstance {
  public:
    // OpenCL variables ...
    // Execute kernel
    virtual void RunKernel(
      vector < vector < float >> & inputs
      vector < vector < float >> & outputs) = 0;
  private:
    // Setup OpenCL variables ...
    // Setup OpenCL kernel ...
    // Manage memory buffers
    void ManageBuffers(
      vector < vector < float >>* inputs
      vector<vector<float>>* outptus);
  // Other advice and pointcuts
  // Pointcut defines where the memory
  // buffers should be managed
  pointcut ManageKernel() =
    // Syntax means to weave ManageKernel advice
    // into any RunKernel function in CoreClasses
    execution("% ...::RunKernel(...)") &&
    within(CoreClasses());
  // Advice specifies how to manage buffers
  advice ManageKernel() : before() {
    // Manage memory buffers
    tjp->that()->ManageBuffers(tjp->arg<0>(),
                    tjp->arg<1>());
};
```

Listing 6: Abstract aspect components which hide kernel cross-cutting concerns.

plementation and therefore cross-cut's the program's main intention. Furthermore, memory management is difficult in the C and C++ languages because it is the programmer's responsibility. This is magnified by the introduction of the device memory. As these operations are required each time the kernel is executed, the amount of cross-cutting code grows linearly with the number of kernels and kernel calls.

These problems are solved by the CAPP framework th-

```
void VectAddCpp(int argc, char** argv) {
    // Instantiate vect addition class
    ParVectAddCppCore vectAdd;
    // Create input and output buffers
    vector<vector<float>> inputs;
    vector<vector<float>> outputs;
    // Fill vectors with data ...

    // Execute kernel
    vectAdd.RunKernel(inputs, outputs);
}
```

Listing 7: Execution of a parallel kernel from a C++ class.

rough aspects by defining a pure virtual Runkernel function in the ClInstance slice class. Since the ClInstance slice class will be a baseclass of any C++ class requiring parallel functionality, the Runkernel function implementation must be defined in the C++ class. By defining the Runkernel function as a pure virtual function, the aspect knows the structure of the function, which allows the ClContext aspect to define advice which can use the arguments passed to the Runkernel function by the programmer to perform the crosscutting memory management code each time the Runkernel function is called from the C++ class.

The RunKernel function specifies that the arguments provided to it from the C++ code must be vectors of vectors. which hold data vectors for each input and output of the kernel. This was influenced by the OpenCL API, which can only operate on simple data structures (pointers, constants and OpenCL vector data types), rather than complex data structures (C++ maps for example). By using vectors of vectors, the RunKernel interface can take any number of inputs and outputs, and each of these inputs and outputs can have any number of elements, providing the programmer with a lot of flexibility from the C++ class. The more complex C++ vector data structure is then accessed through pointers by the RunKernel advice to create OpenCL buffers, which the OpenCL kernel can operate on. This allows the C++ class defined by the programmer to execute a kernel by simply calling the RunKernel function and passing the inputs and outputs as function arguments, as shown by Listing 7. The tjp pointer is used again by the aspect, but this time to access the RunKernel function arguments, and hence the input and output data for the kernel so that the memory can be appropriately managed between the host and device.

Since the RunKernel interface requires the programmer to invoke it, and provide it with the input data, the C++ code is not completely devoid of the aspects. Typically for an aspect-oriented approach, the non-aspect code has no knowledge of the aspects, which is not the case for the RunKernel interface. This deviation from a pure aspect-oriented approach is required as the CAPP framework needs the input data from the C++ code to determine the amount of memory to allocate and then transfer to the device memory space. Although this is more of an object-oriented approach, using aspects in this situation is advantageous as they are woven into the code before compilation, essentially making the RunKernel function part of the programmers C++ code (to the compiler), whereas the object-oriented approach would provide an interface behind which the OpenCL code would hide, which the programmer could then call from their own code. Additionally, while the C++ code is not completely

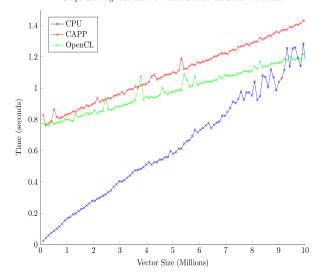


Figure 2: Results for the three implementations of the SAXPY problem. Lower values are better.

ignorant of the framework's presence, it is largely so, allowing most of the framework's aspects to be hidden which simplifies the end user's C++ code.

Listing 6 shows the ManageKernel pointcut and advice, which calls the ManageBuffers function in the ClInstance class, to perform the cross-cutting memory management between the host and device before the RunKernel function is called from the C++ class.

## 4. RESULTS

This section presents the results of application of the CAPP framework to two GPGPU programming problems. The first problem is known as SAXPY (Single-precision aX + Y) [9], and is relatively simple to implement in parallel. The second problem is the evaluation of option prices using the Black-Scholes option pricing model [15], which is more complex than the SAXPY problem, and hence applies the CAPP framework to more 'tangled' C++ code. Results are given for both the performance (run times) and number of lines of code. The performance results are shown for CAPP, C++ (CPU only), and OpenCL implementations, to highlight the similarity between raw OpenCL and CAPP performance. As there is currently not a compiler which provides support for both AspectC++ and C++ AMP, the performance results for C++ AMP are not shown. The number of lines of

Table 1: Comparison of the number of lines of code per file type for the SAXPY problem.

	CAPP	OpenCL	C++	C++ AMP
.h	48	0	38	28
.cpp	43	152	73	54
.cl	5	5	-	-
.ah	53	-	-	-
.cc	78	-	-	-
Total	227	157	111	82

Graph Showing Run-time vs. No. Options for the Black-Scholes Option Pricing Problem

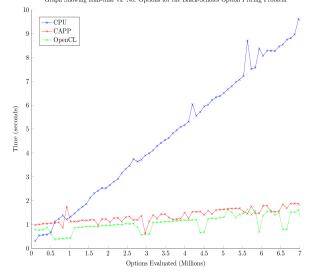


Figure 3: Results for the three implementations of the Black-Scholes option evaluation problem. Lower values are better.

code comparison, however, does include C++ AMP as it is also a higher level framework which aims to minimize the amount of code required for writing parallel programmes. For each implementation 50 runs were performed. The results shown are an average of the 50 runs. An Intel i7-3635QM (2.4GHz, quad-core, 6M cache) CPU was used for the CPU implementations and an Nvidia GeForce GT 650M (384 cores, 900Mhz) GPU was used for the CAPP and OpenCL implementations.

## 4.1 SAXPY Problem

Various vector sizes were used and the run time of each of the implementations was measured to validate that the solution using the CAPP framework scales similarly to the OpenCL implementation. Figure 2 shows the results for the run time of each implementation. Transferring the data between the CPU and the GPU is a large initial overhead for the SAXPY problem [6]. This is evident by the offset of the OpenCL and CAPP results for small vector sizes. Since the CPU implementation does not require data transfer to an external device the offset is not present. The gradient of each of the results provides important information as it shows how each of the implementations scale with an increase in the data size. The gradient of the CAPP

Table 2: Comparison of the number of lines of code per file type for the Black-Scholes option pricing problem.

bblein.								
ſ		CAPP	OpenCL	C++	C++ AMP			
ſ	.h	37	0	32	34			
	.cpp	55	181	121	104			
ſ	.cl	73	73	-	-			
ſ	.ah	53	-	-	-			
ſ	.cc	78	-	-	-			
	Total	296	254	153	138			

(0.61) and OpenCL (0.48) solutions are comparable, with the CAPP solution being slightly worse. The gradient of the C++ implementation (1.19) is considerably worse and does not scale particularly well. The average performance of the CAPP implementation was 7% slower than the OpenCL implementation with the worst case performance being 21% slower.

The second performance metric is the number of lines of code of each implementation. The results are shown in Table 1. The important files are the C++ (.h, .cpp) and kernel (.cl) files as these are what the programmer will need to write. The aspect files (.ah, .cc) are part of the CAPP framework and do not need to be re-implemented by the programmer.

# 4.2 Black-Scholes Option Pricing Problem

This problem requires evaluating option prices using the Black-Scholes model for options pricing and is a good example of an application of the CAPP model to a real world problem. The number of options was varied and the run time was measured for the each implementation, the results are shown in Figure 3. This example illustrates level of performance the CAPP framework can provide. The CAPP and OpenCL run times are very similar. The average performance of the CAPP implementation was 1.4% slower than the OpenCL implementation, while the C++ implementation was up to 9 times slower than both the CAPP and OpenCL implementations.

The comparison of the number of lines of code for each of the implementations is shown in Table 2. The file types of interest are the same as those mentioned in the SAXPY example.

# 5. EVALUATION AND LIMITATIONS

# 5.1 Analysis of Results

The results show some dips and spikes for both the SAXPY and Black-Scholes problems. These can be caused by both temperature changes which affect the clock speeds of both the GPU and the CPU, the effectiveness of compiler optimisations for the data size, and for the GPUs, the arrangement of the data which affects how easily it can be loaded into the GPU memory. For each implementation, 50 runs were performed to reduce these effects as much as possible.

The performance results of the GPU relative the CPU vary greatly between the SAXPY and Black-Scholes implementations. This is due to the nature of each of the problems. While vector addition is an operation which is well suited to being parallelized, it is only beneficial to use the GPU when the problem size is sufficiently large so that the time required to transfer the data between the CPU and GPU is only a small fraction of the total computational time. The CPU implementation does not need to move the data between memory spaces, hence its superior performance for small problem sizes in the SAXPY example.

Since the Black-Scholes example is very computationally complex, the time required to transfer the data between the CPU and GPU is minimal in comparison to the time required to compute the result. As a result, the GPU performance is superior to the CPU performance even for small problem sizes. Additionally, the gradient of the GPU curves is almost flat due to the problem being well suited to using a GPU. As the problem size increases the GPU is able

to use more cores to evaluate the additional options. The CPU, however, does not have many cores available and has to wait for cores to finish evaluating options before evaluating additional options, hence the performance of the CPU does not scale well as the problem size increases.

The results presented in the Section 4 show the similarity in the performance of a CAPP implementation and an OpenCL implementation, as well as the performance advantage over a C++, CPU only implementation. The similarity of the CAPP and OpenCL performance proves the viability of using the CAPP framework for parallel programming. The number of lines of code comparison provides further justification for using a CAPP implementation as the number of lines of C++ code were dramatically reduced, requiring only 91 lines and 92 lines of C++ code for the SAXPY and Black-Scholes CAPP implementations respectively. The C++ AMP framework required 82 and 134 lines of C++ code for the same examples. Since the C++ AMP framework allows the kernels to be written using C++ lambda functions, the resulting C++ code is longer, however, there is no external kernel function as is the case when using the CAPP framework. As well as being reduced in size, the resultant C++ code when using the CAPP framework is modular and hence easily maintainable, which is typically not the case for OpenCL code. Therefore, the CAPP implementation code was more comparable to the C++ implementation's code, both structurally and in length. The CAPP framework thus provides performance comparable to an OpenCL implementation but written in modular C++.

# 5.2 Advantages

The CAPP framework provides numerous advantages which should be mentioned.

Low-level parallel programming complexities are hidden from the programmer using aspects. This results in C++ code which has no components that cross-cut the code's intention, as well as kernel functions which can be executed from the C++ code with a single function call through the RunKernel interface.

The RunKernel interface provides flexibility to the programmer through the use of vectors. Any number of input and output arguments can be specified due to the dynamic nature of vectors. Each of these inputs and outputs can have any dimension, and the dimensionality of each of the inputs and outputs can vary.

While the OpenCL functionality is hidden from the programmer by the aspects, this code is still available to the programmer if desired. This could be beneficial in many scenarios, for example where a team is using the CAPP framework, GPU experts could modify the implementation provided by the CAPP framework to improve performance but non-GPU experts could still use the C++ interface.

There is negligible loss in performance since the aspects are woven into the C++ code before compilation and hence do not have run time implications other than the necessary OpenCL related work.

The use of AspectC++ results in the CAPP framework being capable of providing general aspect-oriented programming support for C++. The benefit of this is that large-scale applications could use standard AspectC++ to modularize other cross-cutting concerns from the C++ application – logging for example – and use the CAPP framework to improve the performance of critical areas of the application, in C++.

The CAPP code which performs all the OpenCL components is reliable. A developer would not have to debug the OpenCl setup and memory management code when testing a parallel algorithm. Furthermore, the C++ code is simple. These features results in a faster overall parallel programming development cycle and limits potential sources of error to the kernel function provided by the programmer.

#### 5.3 Limitations and Weaknesses

The lack of complete decoupling between the aspect and C++ code due to the RunKernel interface is a weakness of the CAPP model in the sense that the resulting framework is not purely aspect-oriented.

Due to the weaving process occurring before compilation, it was expected that the CAPP implementation would provide performance equal to that of the OpenCL implementation. However, the results showed a marginal performance reduction. This was determined to be due to AspectC++ weaver producing code which compiles to a slightly less efficient executable.

Since AspectC++ is still in the development process it does not support all the features of C++, for example generic programming through templates, within aspects. This places a limitation on the C++ code the programmer may write if they are using the CAPP framework. The development of the AspectC++ compiler is therefore a limitation of the CAPP framework as the latest C++ features will not be available. However, future releases of the AspectC++ compiler are scheduled to include support for many more modern C++ features.

#### 6. CONCLUSION

This paper described the CAPP framework which uses aspects to remove cross-cutting code from C++ code which uses OpenCL for parallelism, thereby providing simpler, more modular parallel-capable C++ code. The aspects were implemented using AspectC++, and the parallel functionality was provided through OpenCL. The cross-cutting components handled by the aspects are the setup of the OpenCL context, the compilation of the kernel, the transfer of memory between the host and device when the kernel is executed, and the kernel execution itself. The aspects defined by the CAPP framework are abstract and can be used for any parallel programming application. An interface is provided for calling the OpenCL kernel from the C++ code so that no cross-cutting OpenCL code is present in the C++ code, and the developer is not required to learn the OpenCL API. The kernel is left to be written by the developer since it is specific to the application. This is similar to most existing solutions, however, the existing solutions require operations to be defined using the proposed language or framework, which operate on data in a parallel manner, rather than requiring an OpenCL or CUDA kernel.

Two example problems were looked at: SAXPY and options pricing. CAPP, OpenCL and C++ implementations were compared for each problem. The performance of the CAPP framework was shown to be similar to that of the OpenCL implementation, with 7 and 1.4% performance reductions, respectively. Structurally the CAPP implementation was similar to the C++ implementation in terms of the number of lines and modularity, and better designed than the OpenCL implementation. Both CAPP implementations required no OpenCL code in the C++ header or implemen-

tation files, allowing the CAPP framework to provide high performance parallel programming with C++.

#### 7. FUTURE WORK

The CAPP framework proposed in this paper looked at using aspects to hide the complexities and cross-cutting concerns of parallel programming. However, only a minimal amount of the OpenCL API was used to allow OpenCL kernels to be run from the C++ code. The OpenCL API is extensive and provides many additional features which could be ideal candidates for encapsulation with aspects, such as timing, tracing, and performance profiling, to name a few. More specific aspects could provide these features as well as deriving from the abstract aspect, providing the additional features of the OpenCL API.

The RunKernel interface could be modified to remove the requirement of the programmer to provide its implementation in the C++ code, which would make the C++ code devoid of aspects.

The slight performance reduction of the CAPP implementation over the OpenCL implementation could be reduced by optimising the aspect code.

OpenCL provides functionality which is available for parallel devices from multiple vendors, which may not take advantage of features specific to some vendors – for example dynamic parallelism available to some Nvidia GPUs. Future work would involve extending the CAPP aspects to take advantage of these specific hardware features. This would require including support for the CUDA API in the framework. To perform this efficiently, templates would need to be used, and hence the next release of the AspectC++ compiler would be required to minimise run time evaluation, preferring rather to perform the evaluation at compile time.

To provide a parallel programming environment which can be written explicitly in C++, the kernel would need to be written using C++ – as is done by the Intel TBB framework – but to allow GPUs to be used. Future versions of the CAPP framework could provide the option to the programmer to either specify the kernel themselves, or to have the CAPP framework attempt to parallelize a C++ function into a kernel. It is unlikely that this feature would ever achieve performance levels equivalent to kernels specified by experienced programmers. However, even if only minimal performance increases were to be achieved, this would prove useful for applications where any additional performance is needed but where the programmers have no knowledge of parallel programming.

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