

ECEN 4013 Design of Engineering Systems

Agenda

Project 1 Questions
4024 Materials
New Sprint
Schmitt Triggers



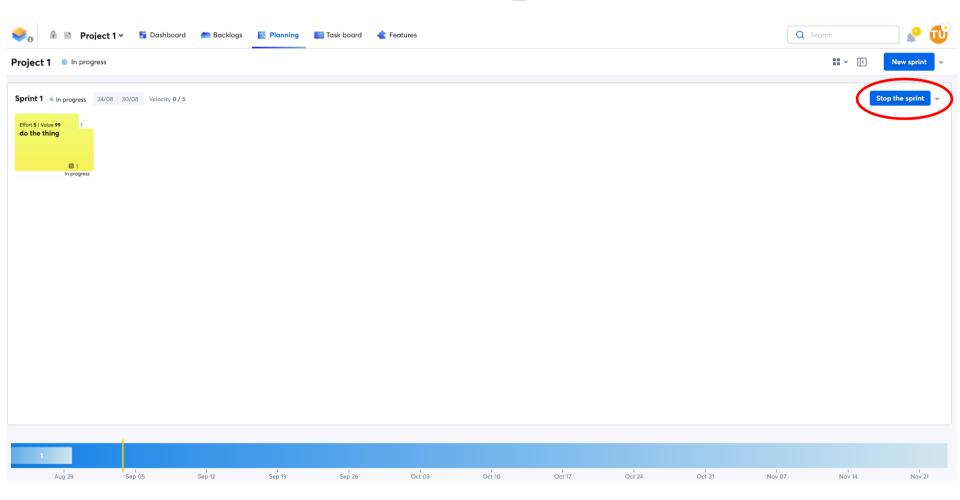
Project 1

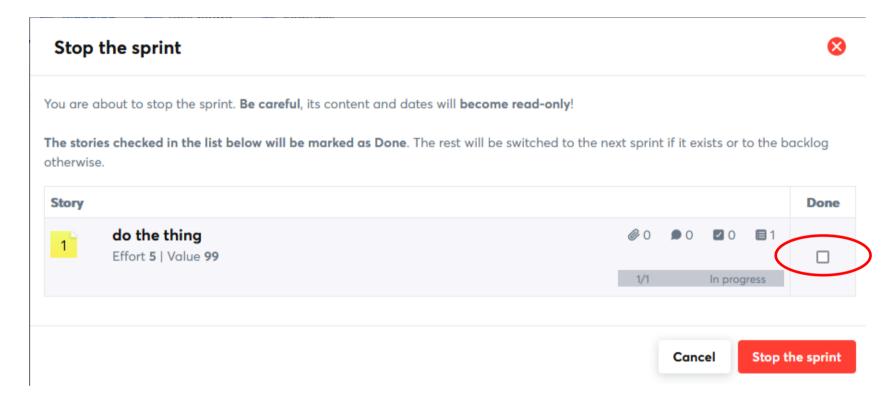
• Questions?

• It is now the first week of Sprint 2.

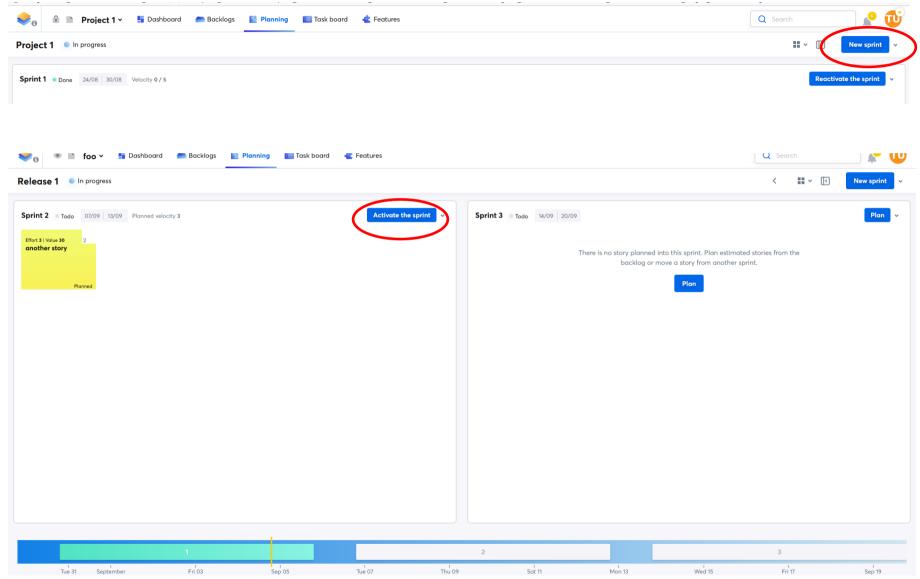
ECEN 4024 Materials

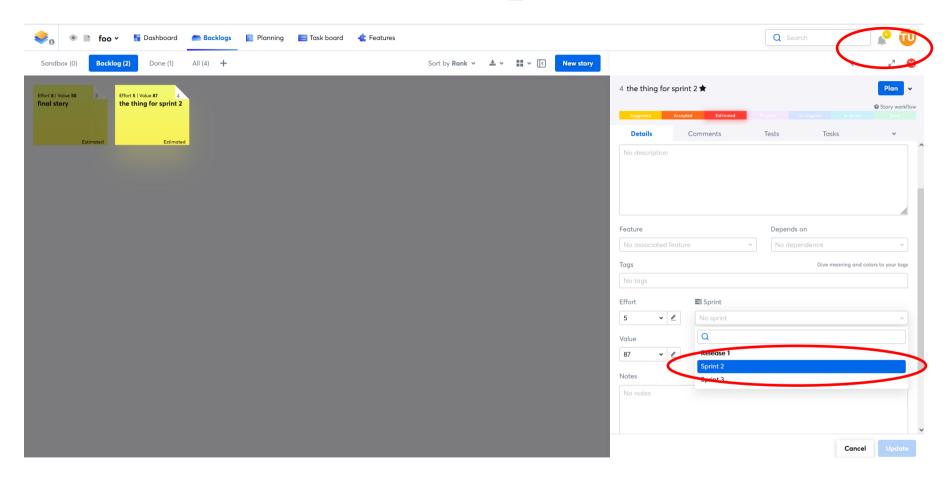
Throughout the semester I will provide presentation videos and slides for teams in 4024 so that you can get a feel for what is expected of you. Also, it helps to have knowledge of the projects because some of them are multi-semester, and you might find yourself





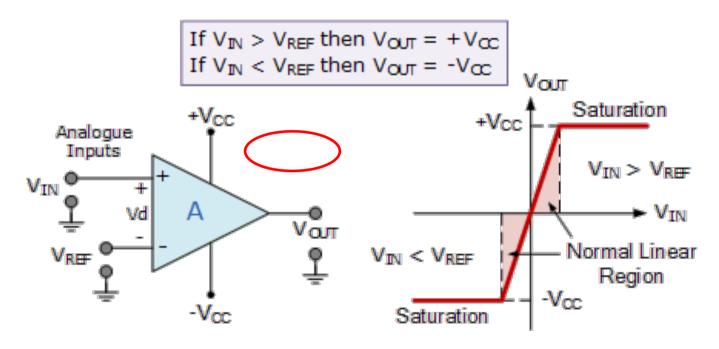
All completed stories will be moved to the "done" section of the backlog. Stories not finished will be moved to the next sprint.





Schmitt Trigger

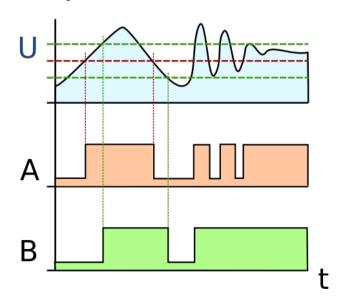
Basic Op amp Comparator



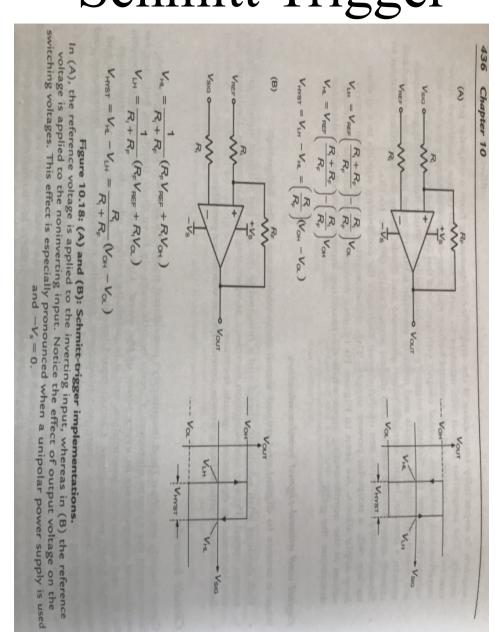
What happens at the decision boundary?

Schmitt Trigger

- Why are Schmitt Triggers used?
 - The hysteresis loop is beneficial. There are two thresholds, a low to high and a high to low. This effectively reduces noise in the stage where you are very close to your decision line



Schmitt Trigger



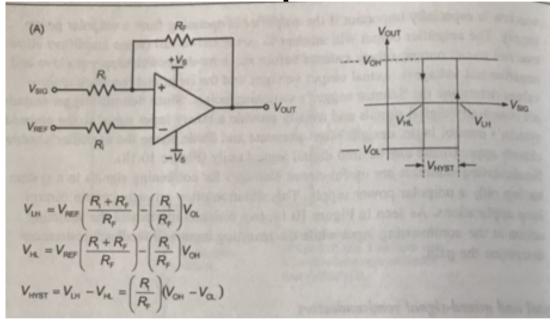
A Schmitt trigger has two possible output states and two switching points – one when the input signal is low and going high, the other when the input signal is high and going low.

The circuit is clearly nonlinear, but analysis can be done using techniques of conventional linear op amp analysis.

There are two inputs: the signal input and a voltage reference input.

The voltage reference is the approximate middle of the switching hysteresis loop.

*** The reference voltage will be the center of the loop if using a bipolar supply and an amplifier with rail-to-rail outputs.

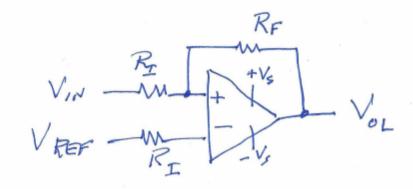


The input signal is assumed to be approaching the threshold from the left – that is, negative and going positive.

The amplifier will saturate positive when the noninverting input voltage exceeds the reference voltage present at the inverting input.

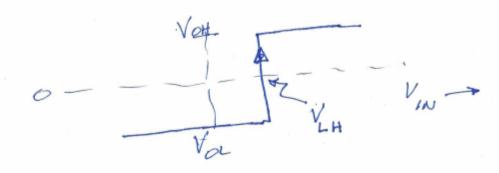
The amplifier will saturate negative when the reference voltage at the inverting input exceeds the voltage at the noninverting

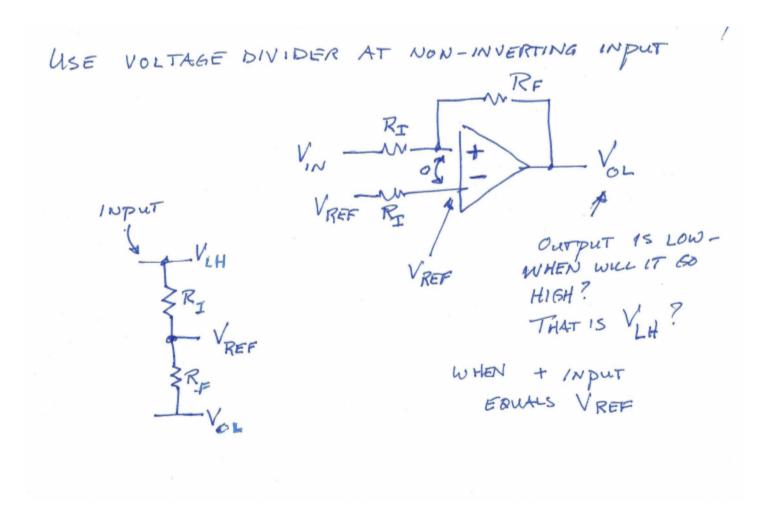
Analysis of the two conditions and input node voltages are how we obtain the equations defining the switching points and the hysteresis window.



ASSUME

- (1) V, XX REF, SO THE AMPLIFIER OUT DUT IS
 SATURATED LOW AT VOL.
- (2) WE INCREASE VIN UNTIL THE AMPLIFIER OUTPUT SATURATES HIGH AT VOH 1 +Vs.
- (3) WHAT IS THE LOW-HIGH TEANSITION VOLTAGE





$$V_{RFF} = V_{OL} + \left(\frac{R_{F}}{R_{I} + R_{F}}\right) \left(V_{LH} - V_{OL}\right)$$

$$= V_{OL} \left(1 - \frac{R_{F}}{R_{I} + R_{F}}\right) + \left(\frac{R_{F}}{R_{I} + R_{F}}\right) V_{LH}$$

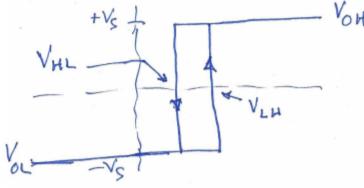
$$= \left(\frac{R_{I}}{R_{I} + R_{F}}\right) V_{OL} + \left(\frac{R_{F}}{R_{I} + R_{F}}\right) V_{LH}$$

$$V_{LH} = \left(\frac{R_{I} + R_{F}}{R_{F}}\right) \left(V_{REF} - \left(\frac{R_{I}}{R_{I} + R_{F}}\right)\right) V_{OL}$$

SOLVE FOR VLH:

$$V_{LH} = \left(\frac{R_{I} + R_{F}}{R_{F}}\right) V_{REF} - \left(\frac{R_{I}}{R_{F}}\right) V_{OL}$$

NOW FIND HIGH-LOW TRANSITION LEVEL VHL when output is HIGH (VOH) AND VIN IS DECREASING



USE STME VOLTAGE DIVIDER ANALYSIS

$$V_{REF} = V_{OH} \left(1 - \frac{R_F}{R_I + R_F} \right) + V_{HL} \left(\frac{R_F}{R_I + R_F} \right)$$

$$= \left(\frac{R_I}{R_I + R_F} \right) V_{OH} + \left(\frac{R_F}{R_I + R_F} \right) V_{HL}$$

$$Solve FOR V_{HL} AS BEFORE!$$

$$V_{HL} = \left(\frac{R_I + R_F}{R_F} \right) V_{REF} - \left(\frac{R_I}{R_F} \right) V_{OH}$$

Things to notice:

Resistor ratio of RI to RF is a large part of the hysteresis, but not all of it.

If RI = 10 K and RF = 10 M, the hysteresis is 0.1% of the voltage difference between VOH and VOL.

VOH and VOL will equal the supply voltages only if the amplifier has rail-to-rail output. If not, you must use specified amplifier saturated output voltages.

These results are perfectly general. If the amplifier has a unipolar supply, then VOH and VOL will equal the positive supply voltage and zero Volts, respectively (if rail-to-rail output).

The reference voltage will be in the center of the hysteresis window only if VOH and VOL are equal in magnitude and opposite in sign.

Suggestions:

- 1) Assume outputs
- 2) Set Vref at anticipated midpoint of the hysteresis window.
- Select RI and RF based on anticipated output voltages and desired hysteresis window
- 4) Vary Vref slightly to position the hysteresis window where you want it.

Reversing the input and reference voltages in the same structure gives the designer the flexibility to change the output switching and possibly eliminate the need for an additional inverter.

The analytical process is the same. The results are deceptively similar, but the two results are not the same.

Questions?