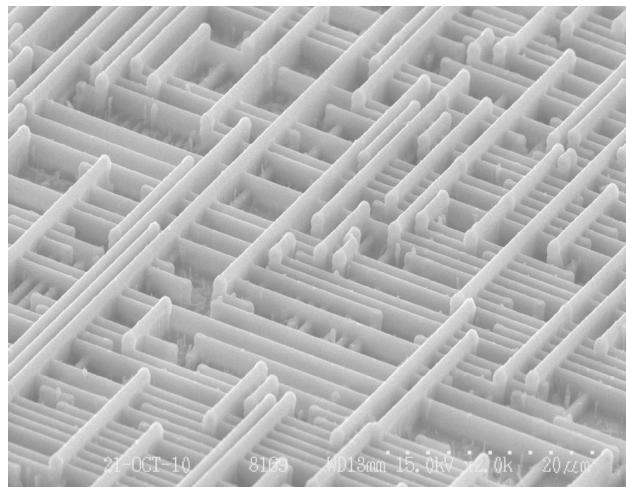
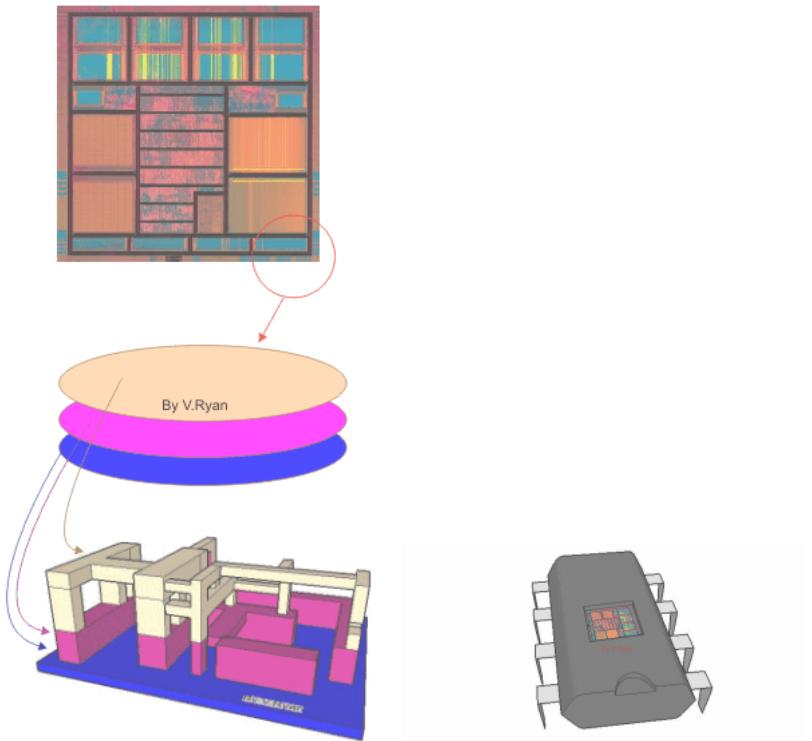
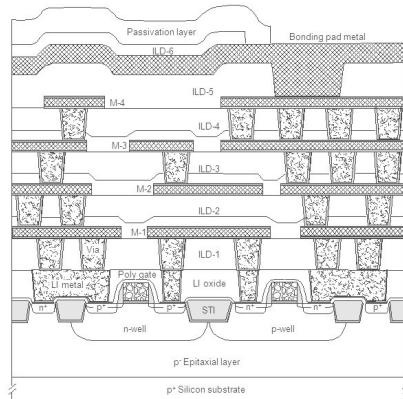


Source: EV Group (EVG)



Full 0.18  $\mu\text{m}$  CMOS Cross Section



Semiconductor Manufacturing Technology  
by Michael Quirk and Julian Serna

Figure 9.29

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