Logic Synthesis & Verification 2020

Instructions for final report submission:

1. Final report due by 2021/1/24 23:59 (extension possible based on instructor's consent).

2. Please submit a zip file on CEIBA. Name the zip file by your student IDs, e.g. "D00112233_R01230123.zip". In case your file size is too large to be uploaded, please submit a link to your file for download.

3. The zip file should include:
   A. A readme file, which describes what are included in the zip file and provides necessary information to reproduce your results (e.g., how to compile and run your program, etc.).
   B. Your report in the pdf format. The suggested length is 4 pages for a two-column form or 8 pages in a single-column form of conference papers. Please write it in a precise way to show your methods and experimental results. Note that in case of survey-type projects, be careful about the copyright issue. You should summarize in your own words unless you have exact quotes.
   C. Your source codes for implementation. (Note that your source code is necessary for the course evaluation purpose. By submitting the source code, you retain your copyright. No one will use it in other purposes without your permission.)
   D. Your presentation slides with update.
   E. Referenced materials. (Benchmarks for experiment; referenced papers, slides, documents, etc.)
   F. Other supplemental materials.

4. Please be aware that in case your program or report needs clarification, you will be further contacted.