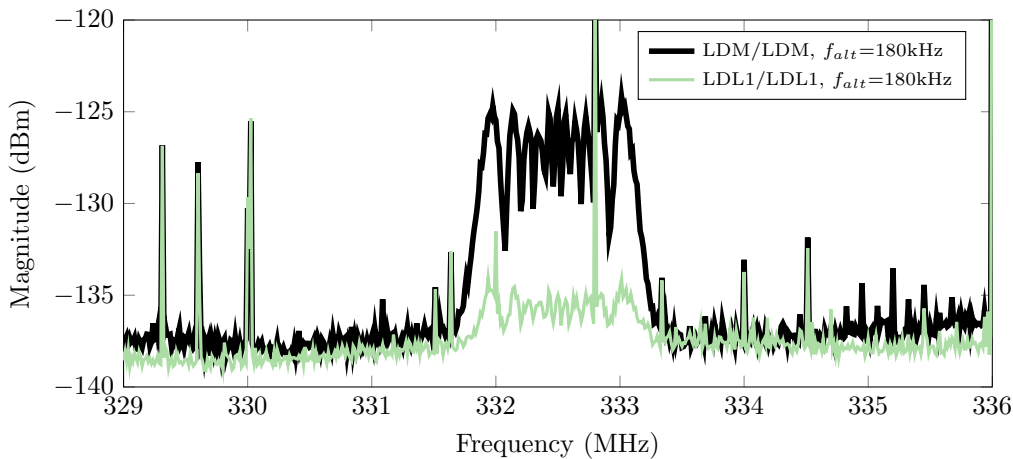
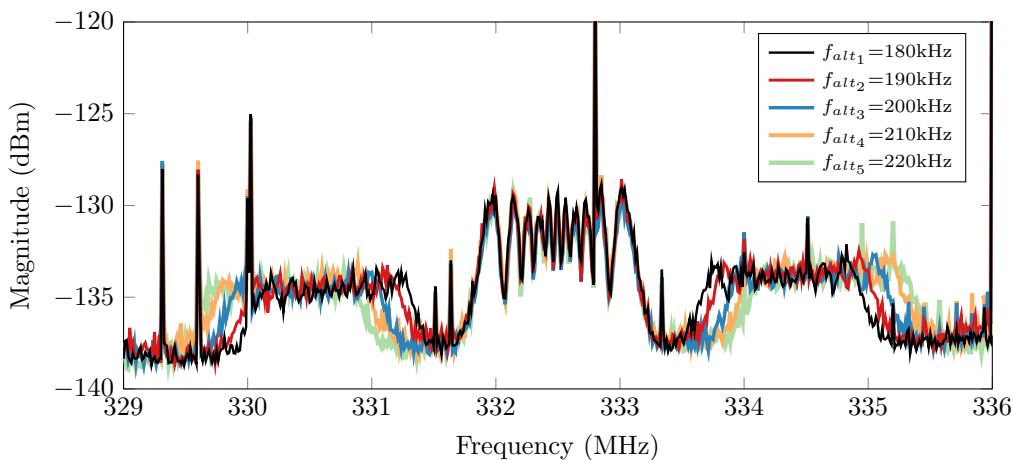


A) DRAM clock spectrum with 0% (LDL1/LDL1)  
and 100% (LDM/LDM) memory activity



B) DRAM clock spectrum with 50% (LDM/LDL1) memory activity



C) Heuristic carrier detection function output

