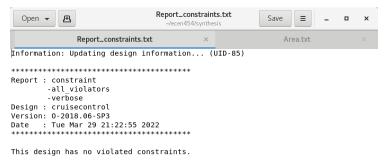
ECEN 454
Lab 7
Roberto Luis
Section 505 - Rahul

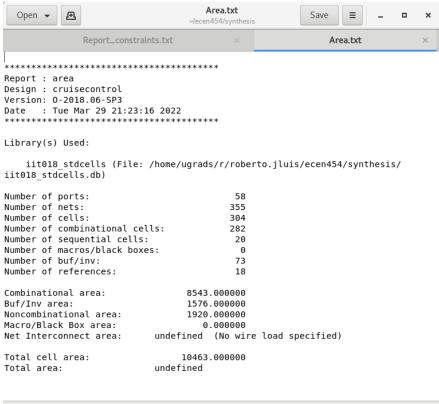
Part A:

Constraints:



Plain Text ▼ Tab Width: 8 ▼ Ln 1, Col 1 ▼ INS

Area:



```
Netlist:
At end of document
```

Number of regs = 20

Part B

Max_paths:

At end of file

Min_paths:

At end of file

Part C:

Wire length and vias:

```
#Total wire length = 9328 um.
#Total half perimeter of net bounding box = 10143 um.
#Total wire length on LAYER metal1 = 902 um.
#Total wire length on LAYER metal2 = 4336 um.
#Total wire length on LAYER metal3 = 3901 um.
#Total wire length on LAYER metal4 = 157 um.
#Total wire length on LAYER metal5 = 32 um.
#Total wire length on LAYER metal6 = 0 um.
#Total number of vias = 1436
#Up-Via Summary (total 1436):
```

Number of cells:

```
#std cell=302 (0 fixed + 302 movable) #block=0 (0 floating + 0 preplaced)
#ioInst=0 #net=323 #term=1008 #term/net=3.12, #fixedIo=0, #floatIo=0, #fixedPin=0, #floatPin=26
stdCell: 302 single + 0 double + 0 multi
Total standard cell length = 1.0808 (mm), area = 0.0108 (mm^2)
```

