





v1.3: Buffer for SCK and MOSI placed at output, internal signals directly to STM32  
 CSRAM directly to U1, RAMCSOR new, Clock chip added  
 WP doesnt work, changed to OR-Gate U3, U21C wrong,  
 uMWR from A12 to A9 of STM32 bec. USB, MWR to R/W on U1 Pin1

**100% Offner**

Sheet: /Bus/  
 File: bus.kicad\_sch

**Title: Unicomp v2 – RAMROM Board**

Size: A4 Date: 2023-07-06

KiCad E.D.A. kicad 7.0.7-7.0.7-ubuntu20.04.1

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