



v1.3: U21C wrong, $\overline{\text{CSRAM}}$ directly to U1, $\overline{\text{RAMCSOR}}$ new, Clock chip added
v1.2: WP doesnt work, changed to OR-Gate U3.

100% Offner

Sheet: /Bus/
File: bus.kicad_sch

Title: Unicomp v2 – RAMROM Board

Size: A4 Date: 2023-07-06
KiCad E.D.A. kicad 7.0.6-7.0.6-ubuntu20.04.1

Rev: v1.3
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