





v1.3: Buffer for SCK and MOSI placed at output, internal signals directly to STM32  
 CSRAM directly to U1, RAMCSOR new, Clock chip added  
 WP doesnt work, changed to OR-Gate U3, U21C wrong,

#### 100% Offner

Sheet: /Bus/  
 File: bus.kicad\_sch

#### Title: Unicom v2 – RAMROM Board

Size: A4 Date: 2023-07-06  
 KiCad E.D.A. kicad 7.0.6-7.0.6-ubuntu20.04.1

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