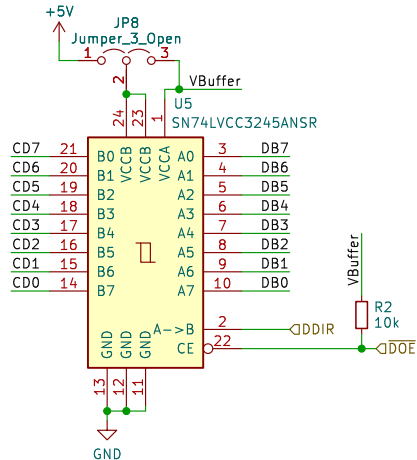
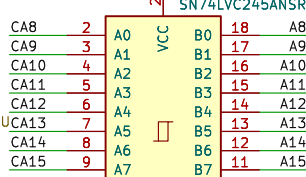
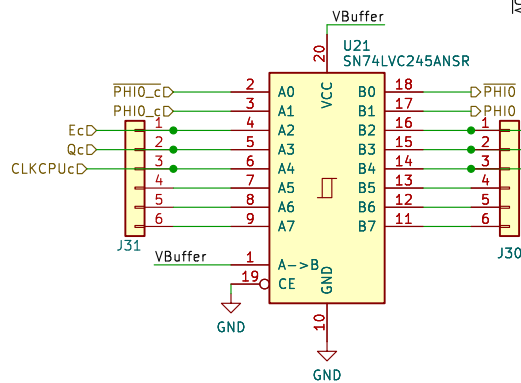
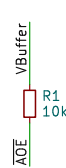
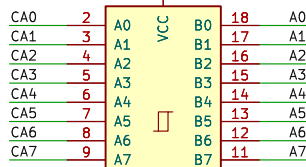


CA[0..15]D

A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15

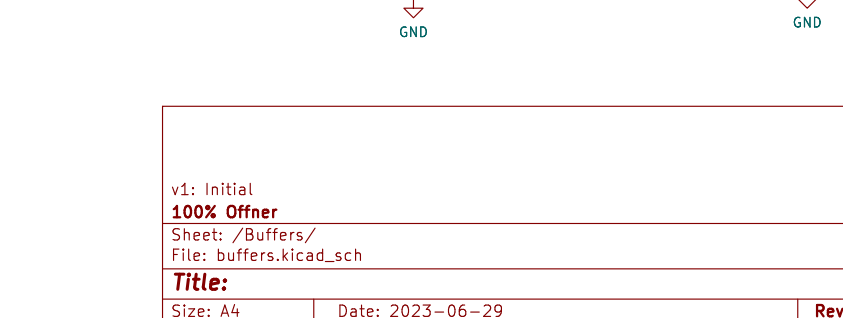
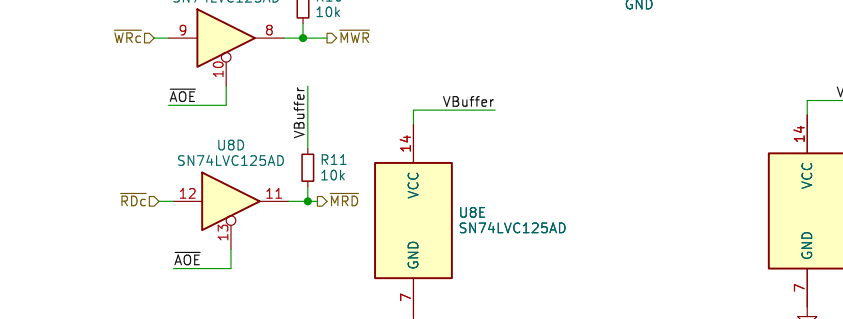
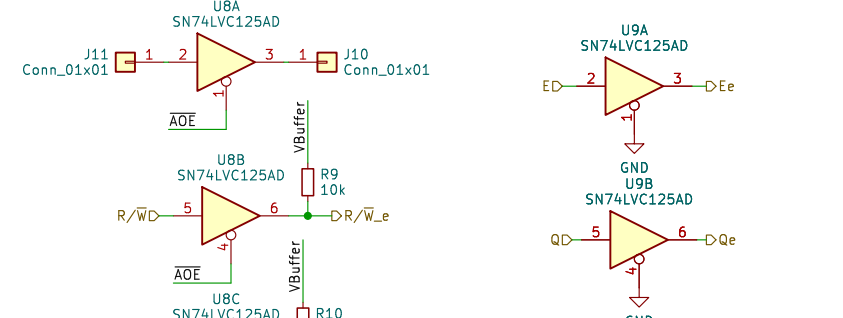
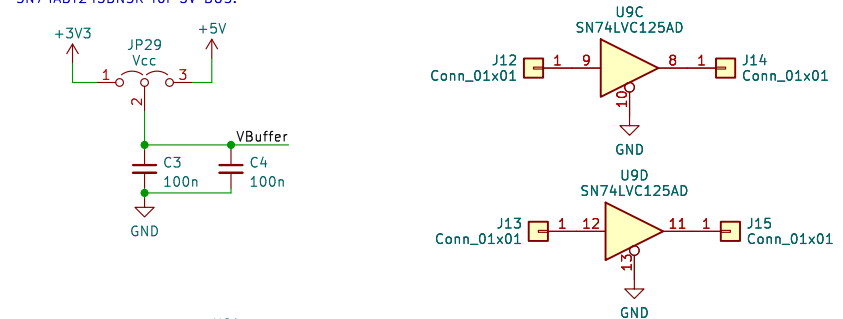
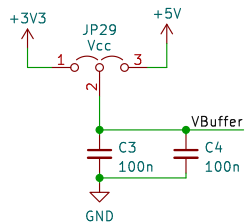
CD[0..7]

DB0 DB1 DB2 DB3 DB4 DB5 DB6 DB7



One can place a SN74LVC245ANSR for 3.3V BUS and CPU or a SN74ABT245BNSR for 5V BUS and CPU on the Pads from U5 or leave the SN74LVCC3245ANSR for 3.3V BUS and 5V CPU.

For Address Buffers use SN74LVC245ANSR for 3.3V BUS and SN74ABT245BNSR for 5V BUS.



v1: Initial
100% Offner

Sheet: /Buffers/
File: buffers.kicad_sch

Title:

Size: A4 Date: 2023-06-29

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Rev: v1.1
Id: 3/3

